TOSHIBA

SERVICE MANUAL

LCD Color Television

42Z3030D

Ver. 2.00

This model is classified as a green product (*1), as indicated by the underlined serial number. This Service Manual describes replacement parts for the green product. When repairing this green product, use the part(s) described in this manual and lead-free solder (*2).
For (*1) and (*2), refer to **GREEN PRODUCT PROCUREMENT** and **LEAD-FREE SOLDER**.

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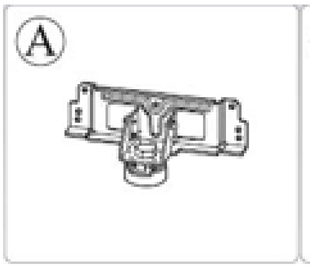
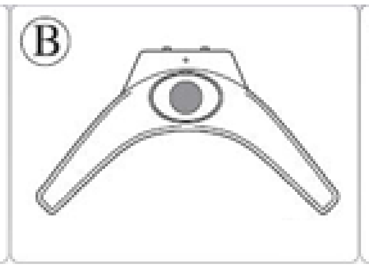
CABINET

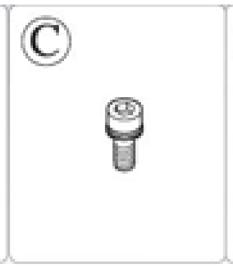
Stand Exploded View



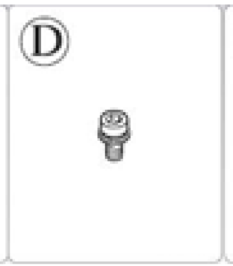
A425



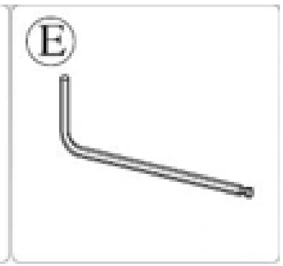
A426



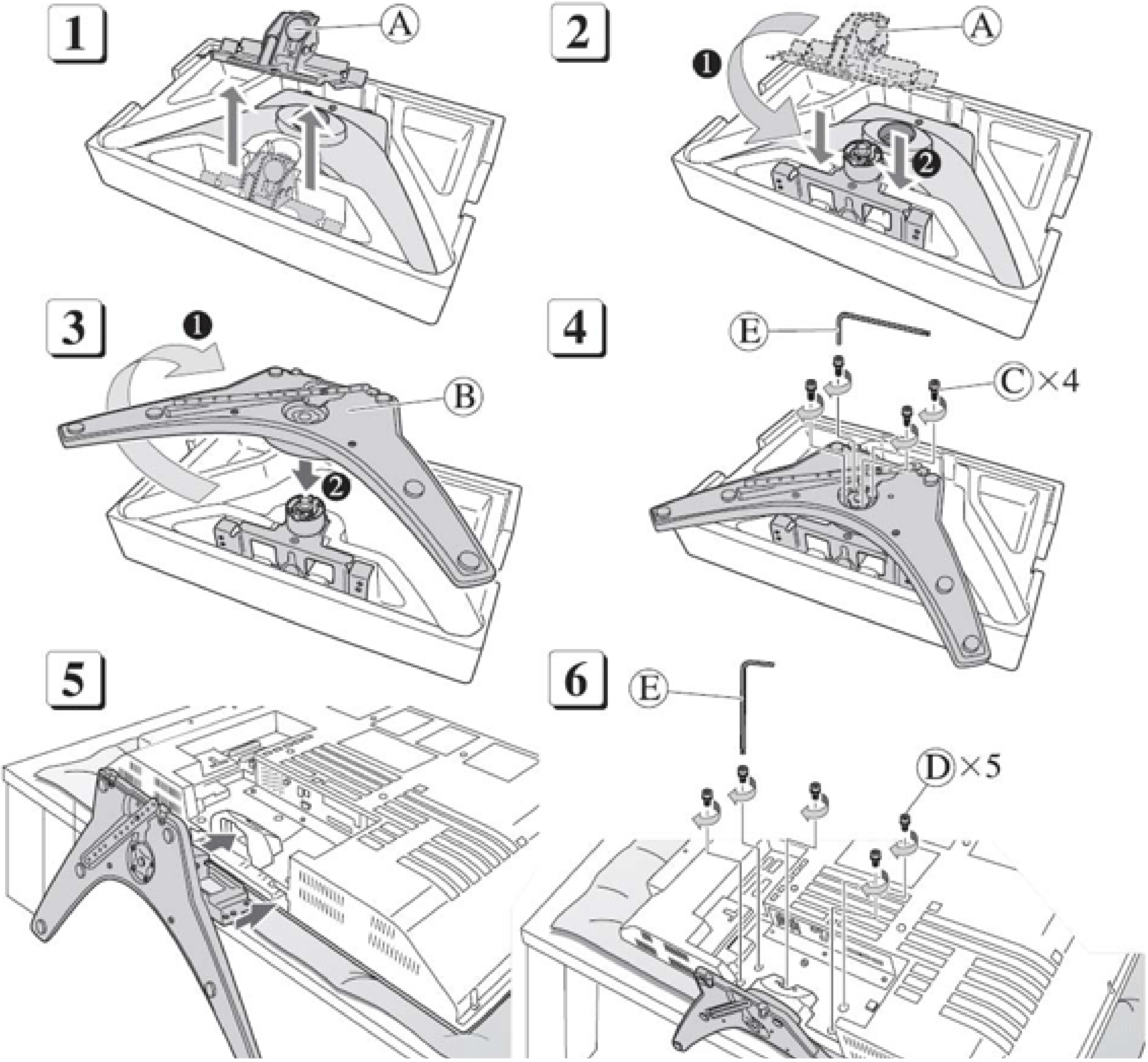
A427

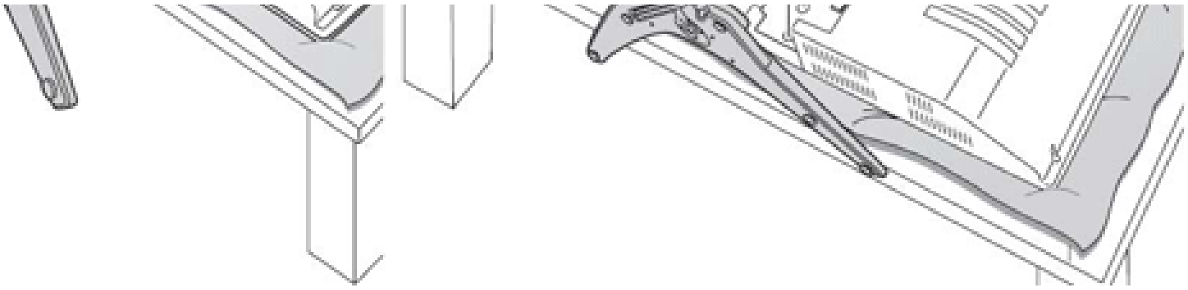


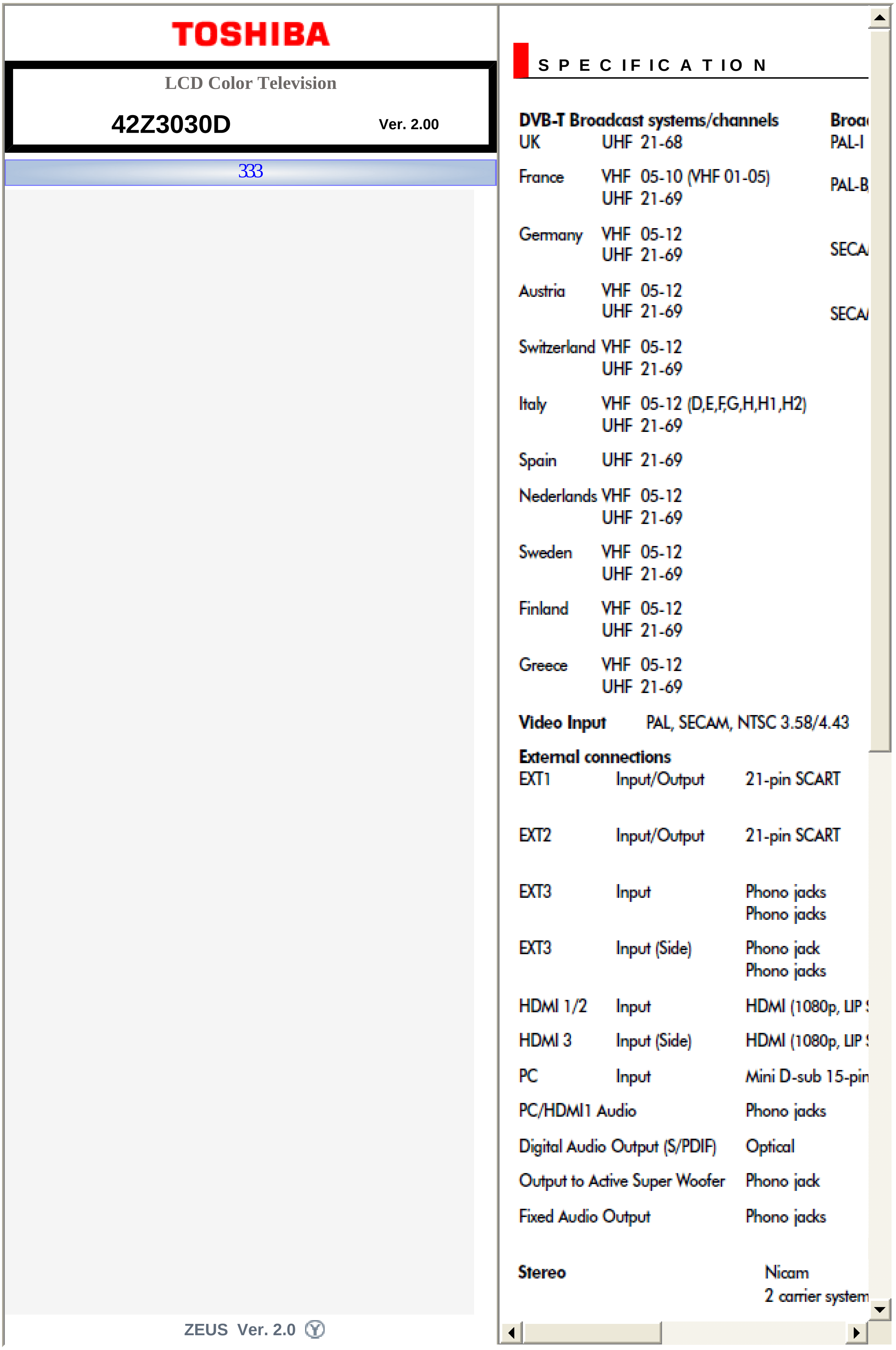
A428



A429


1
A
2
A
❶
❷
3
❶
B
❷
4
E
C×4
5
6
E
D×5





TOSHIBA

LCD Color Television

42Z3030D Ver. 2.00

333

SPECIFICATION

DVB-T Broadcast systems/channels		**Broa**
UK	UHF 21-68	PAL-I
France	VHF 05-10 (VHF 01-05) UHF 21-69	PAL-B
Germany	VHF 05-12 UHF 21-69	SECA
Austria	VHF 05-12 UHF 21-69	SECA
Switzerland	VHF 05-12 UHF 21-69	
Italy	VHF 05-12 (D,E,F,G,H,H1,H2) UHF 21-69	
Spain	UHF 21-69	
Nederlands	VHF 05-12 UHF 21-69	
Sweden	VHF 05-12 UHF 21-69	
Finland	VHF 05-12 UHF 21-69	
Greece	VHF 05-12 UHF 21-69	

Video Input PAL, SECAM, NTSC 3.58/4.43

External connections

EXT1	Input/Output	21-pin SCART
EXT2	Input/Output	21-pin SCART
EXT3	Input	Phono jacks Phono jacks
EXT3	Input (Side)	Phono jack Phono jacks
HDMI 1/2	Input	HDMI (1080p, LIP
HDMI 3	Input (Side)	HDMI (1080p, LIP
PC	Input	Mini D-sub 15-pin
PC/HDMI1 Audio		Phono jacks
Digital Audio Output (S/PDIF)		Optical
Output to Active Super Woofer		Phono jack
Fixed Audio Output		Phono jacks

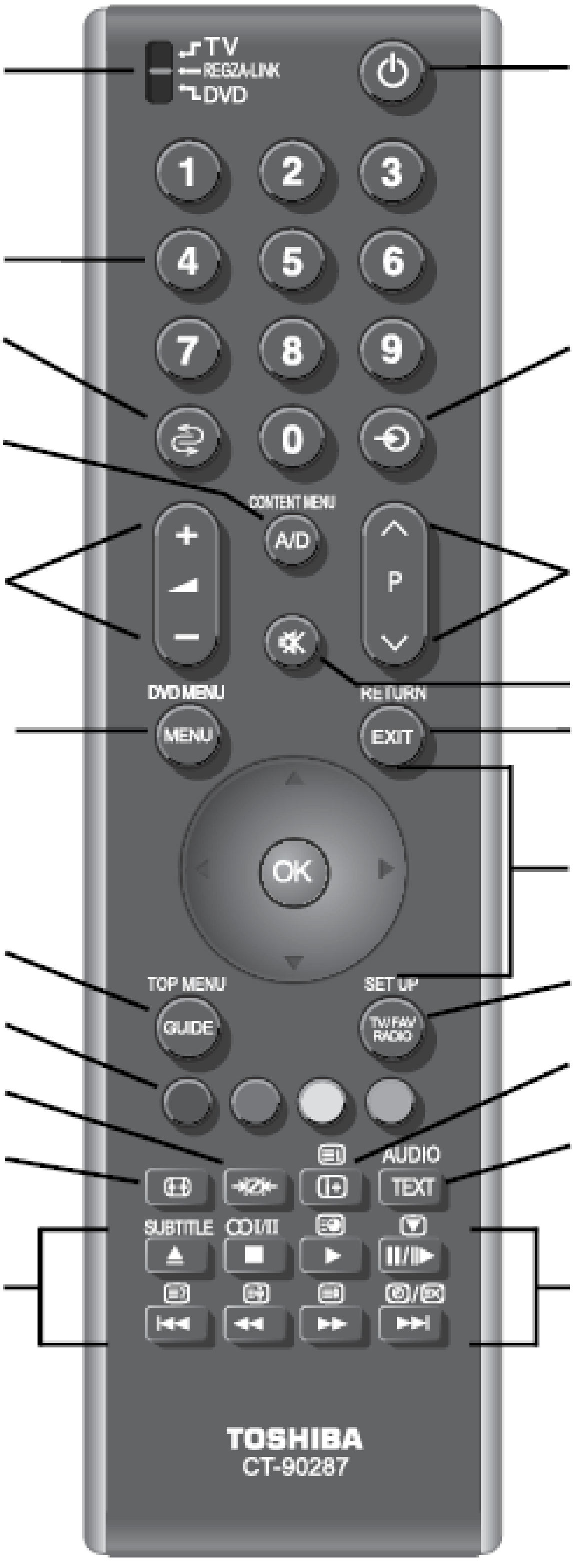
Stereo Nicam
2 carrier system

ZEUS Ver. 2.0

FUNCTION AND OPERATION

The Remote Control

Simple at-a-glance reference of your remote control.


TV
REGZA-LINK
DVD
1
2
3
4
5
6
7
8
9
0
CONTENT MENU
A/D
+
−
P
DVD MENU
MENU
RETURN
EXIT
OK
TOP MENU
GUIDE
SET UP
TV/FAV RADIO
AUDIO
TEXT
SUBTITLE
TOSHIBA
CT-90287

To select mode

For On/Standby mode

Number buttons

To return to the previous programme

To select input from external sources, ***analogue*** *or* ***digital*** *TV.*

To switch between **ATV** *(****analogue*** *television) and* **DTV** *(****digital*** *television)*

To alter the volume

To change programme positions and text pages

To mute the sound

On-screen Menus

To exit Menus

When using menus the arrows move the cursor on the screen up, down, left or right. **OK** *to confirm your selection*

To display the ***digital*** *on-screen Programme Guide*

To switch between TV, Radio and Favourite programmes when in ***digital*** *mode*

Text and interactive service control buttons

To display ***digital*** *on-screen information*

Selectable picture preferences

To call up text services in ***analogue*** *mode and interactive services in* ***digital*** *mode*

Widescreen viewing

When using a TOSHIBA VCR or DVD:

press ⏸/|▶ to PAUSE/STEP
press ▶ to PLAY
press ◀◀ to REWIND
press ▶▶ to FAST FORWARD
press ⏏ to EJECT
press ■ to STOP
press |◀◀ to SKIP-REWIND
press ▶▶| to SKIP-FORWARD

When in TV mode:

▼	Still picture
OO I/II	Stereo/bilingual transmissions
SUBTITLE	Digital Subtitles
⊙/⊠	Analogue time display

When using the Programme Guide:

|◀◀	– 24 hours
▶▶|	+ 24 hours
◀◀	– 1 page
▶▶	+ 1 page

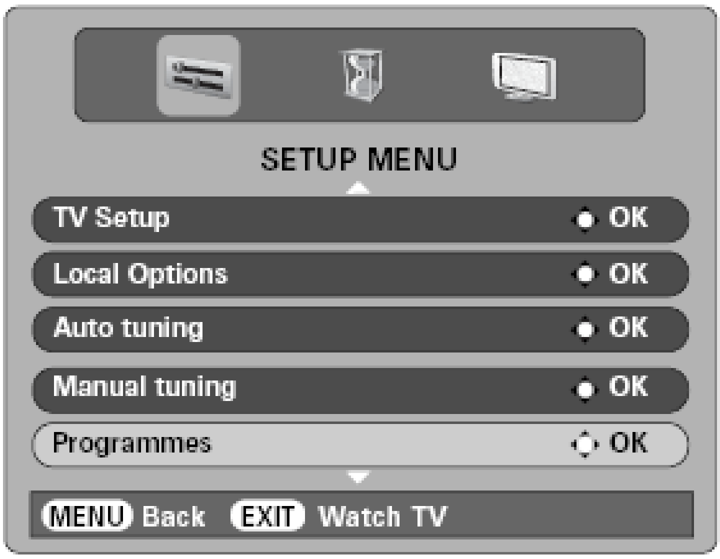
Digital Programme Sorting, Manual Tuning

Note: When the television is switched off the mode being viewed at the time will automatically be selected when the television is switched back on.

Programme Sorting

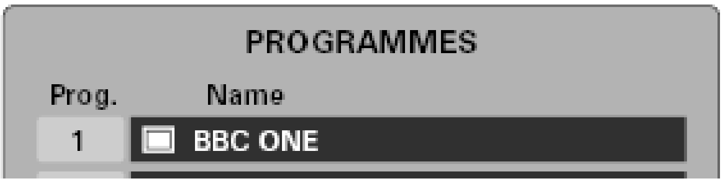
1. The station order may be changed to suit personal preference. Press **MENU**, the **SETUP MENU** will appear on screen.

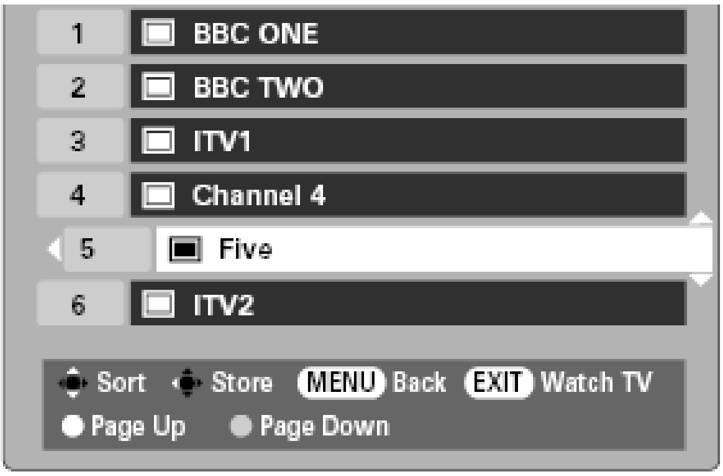
2. Use ▼ to move down through the list and highlight **Programmes**. Press **OK**.


SETUP MENU
TV Setup OK
Local Options OK
Auto tuning OK
Manual tuning OK
Programmes OK
MENU Back
EXIT Watch TV

3. With the list of stations now showing use ▼ or ▲ to highlight the station you want to move and press ▶.

   The selected station will be moved to the right of the screen.


PROGRAMMES
Prog.
Name
1
BBC ONE


1 BBC ONE
2 BBC TWO
3 ITV1
4 Channel 4
5 Five
6 ITV2
Sort
Store
MENU Back
EXIT Watch TV
Page Up
Page Down

4. Use ▼ or ▲ to move through the list to your *preferred* position. As you do so the other stations will move to make room.

5. Press ◀ to store your move. Repeat as necessary, then press **EXIT**.

Manual Tuning

This feature is available for service engineers or can be used for direct channel entry if the multiplex channel is known.

1. Select **Manual tuning** from the **SETUP MENU** and press **OK**.

2. Enter the multiplex number using the number buttons and then press **OK**. The television will automatically search for that multiplex.

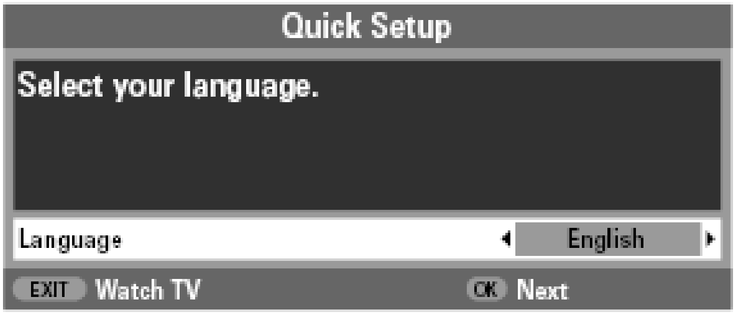
   When the multiplex is found any channels not currently on the programme list will be added and the programme information at the top of the screen will be updated.

3. Repeat as necessary. Press **EXIT** to finish.

Tuning Analogue Mode for the First Time

The **Quick Setup** feature will tune all **analogue** channels currently available in the area. To put the television into **ATV** mode press the A/D button.

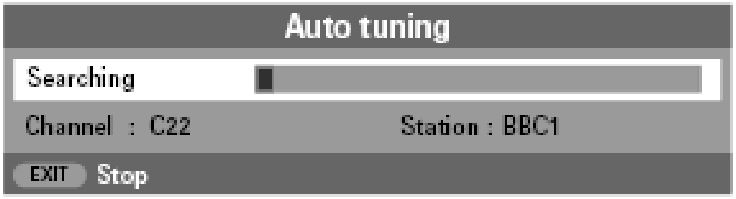
1. Select **ATV** mode. The **Quick Setup** screen will appear.


Quick Setup

Select your language.

Language ◄ English ►

2. Using ◄ or ► select your **language**, then press **OK**.
3. Then press ◄ or ► to select your **country**.
4. To start **Auto tuning** press **OK**.


Auto tuning

Searching

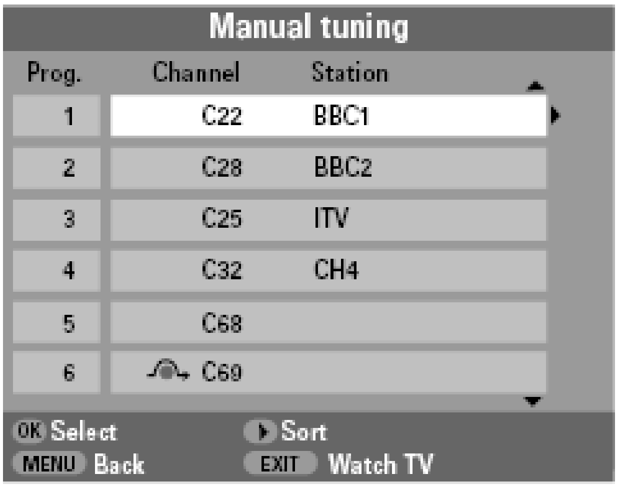
Channel : C22 Station : BBC1

5. The search will begin for all available stations. The slide bar will move along the line as the search progresses.

You must allow the television to complete the search.

When the search is complete the television will automatically select programme position 1 and the **Manual tuning** screen will appear.

automatically select programme position 1 and the **Manual tuning** screen will appear.


Manual tuning

Prog.	Channel	Station
1	C22	BBC1
2	C28	BBC2
3	C25	ITV
4	C32	CH4
5	C68	
6	C69	

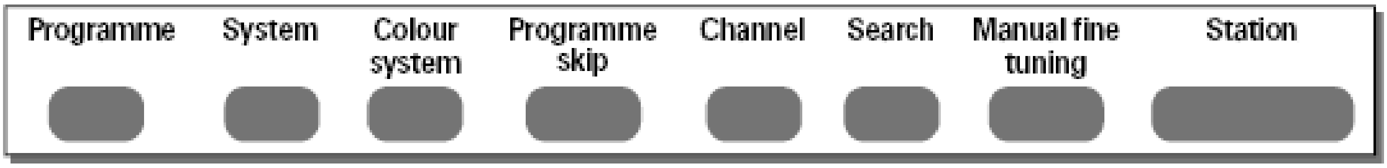
6 Use ▼ or ▲ to move through the list to select a programme then press **EXIT** to view.

The station order may be changed using the **Manual tuning** screen (*see the* **analogue** *'Sorting programme positions' section*).

Analogue Manual Tune

The television can be tuned-in manually using **Manual tuning**. For example: if the television cannot be connected to a media recorder/decoder with a SCART lead or to tune-in a station on another **System**.

Use ◀ and ▶ to move across the screen and select any of these Manual Tune options.



Programme : The number to be pressed on the remote control.

System : Specific to certain areas.

Colour system : Factory set to Auto, should only be changed if problems are experienced, i.e. NTSC input from external source.

Programme skip : 👁 means nothing has been stored or the facility to skip the channel is ON.

Channel : The channel number on which a station is being broadcast.

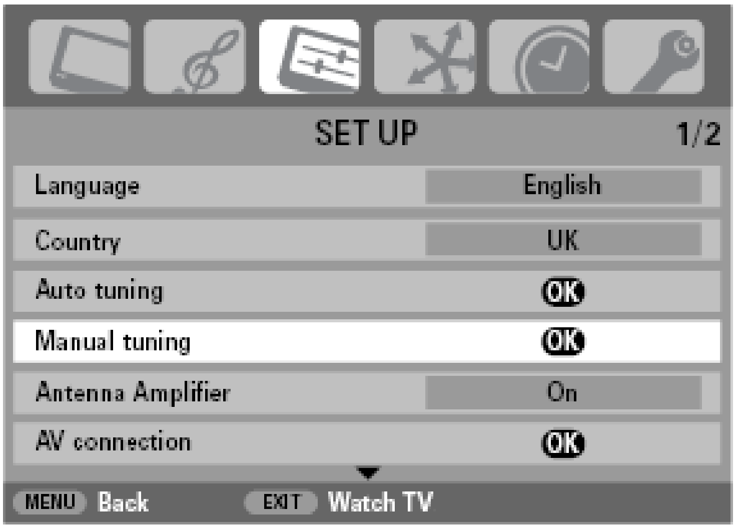
Search : Search up and down for a signal.

Manual fine tuning : Only used if interference/weak signal is experienced.

Station : Station identification. Use the ▲ or ▼ and ◀ or ▶ buttons to enter up to seven characters.

To allocate a programme position on the television for a decoder and media recorder: turn the decoder on, insert a pre-recorded film in the media recorder and press PLAY, then manually tune.

1. Press **MENU** and using ◀ or ▶ select the **SET UP** menu, with ▼ highlight **Manual tuning**, press **OK** to select.

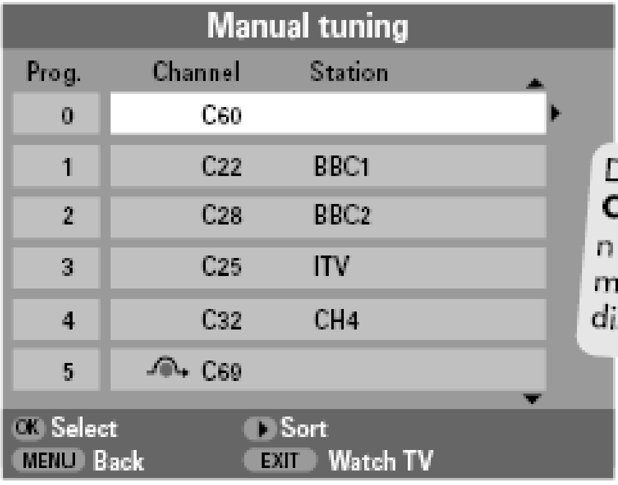


SET UP 1/2

Language	English
Country	UK
Auto tuning	OK
Manual tuning	OK
Antenna Amplifier	On
AV connection	OK

MENU Back
EXIT Watch TV

2. Use ▲ or ▼ to highlight the programme position required e.g. we suggest ***Prog. 0*** for a media recorder.



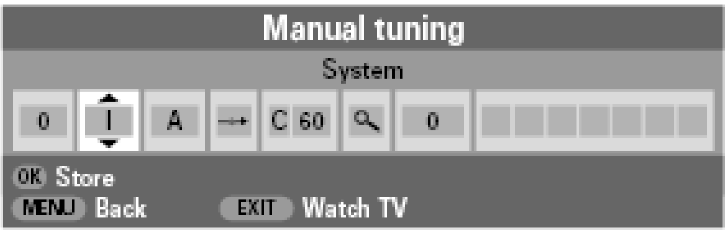
Manual tuning

Prog.	Channel	Station
0	C60	
1	C22	BBC1
2	C28	BBC2
3	C25	ITV
4	C32	CH4
5	C69	

OK Select
Sort
MENU Back
EXIT Watch TV

Different **Channel** numbers may be displayed

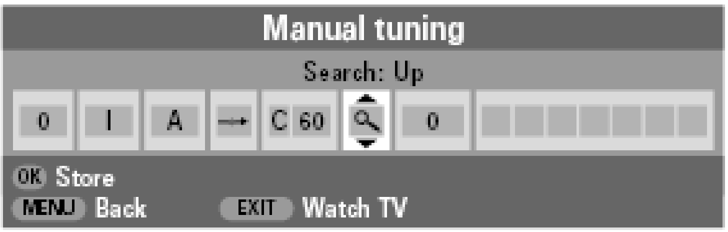
3 Press **OK** to select. If the programme is skipped, **Programme skip** must be removed before storing.


Manual tuning
System
0
I
A
C 60
0
OK Store
MENU Back
EXIT Watch TV

4 Press ▶ to select **System** and use ▲ or ▼ to change if required.

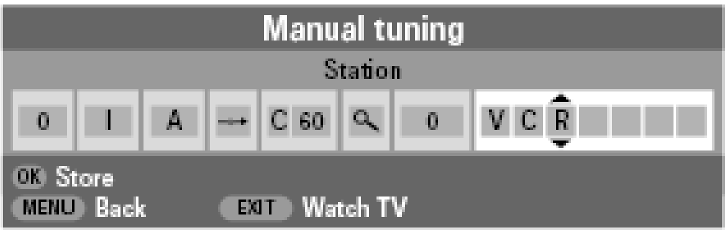
5 Then press ▶ to select **Search**.

6 Press ▲ or ▼ to begin the search. The search symbol will flash.


Manual tuning
Search: Up
0
I
A
C 60
0
OK Store
MENU Back
EXIT Watch TV

7 Each signal will show on the television, if it is not your media recorder, press ▲ or ▼ again to restart the search.

8. When your media recorder signal is found, press ▶ to move along to **Station**. With ▲, ▼, ◀ and ▶ put in the required characters, e.g. **VCR**.



Manual tuning

Station

0 | I | A | → | C 60 | 0 | V C R

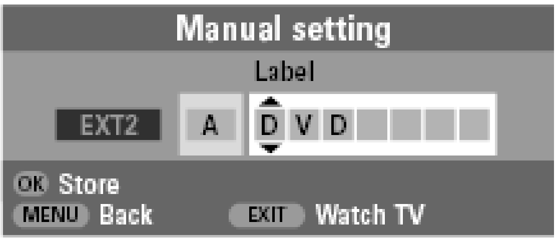
9. Press **OK** to store.

10. Repeat for each ***programme position*** you want to tune or, press **MENU** to return to the list of channels and select the next number to tune.

11. Press **EXIT** when you have finished.

12. To name external equipment, e.g. DVD on EXT2, press ⊕ to select ⊕**2**, then select **Manual setting** from the **SET UP** menu.

13. Press ▶ to select **Label** then using ▲, ▼, ◀ and ▶ put in the required characters. Press **OK**.



Manual setting

Label

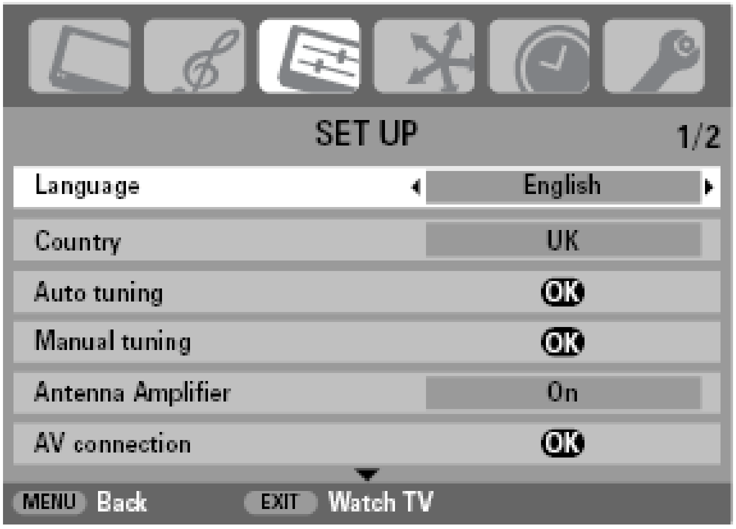
EXT2 | A | D V D

PLEASE NOTE This television has direct channel entry if the channel numbers are known. Select the **Manual tuning** screen in ***Step 3***. Enter the **Programme number**, the **System**, then **C** for standard (terrestrial) channels or **S** for cable channels and the **Channel** number. Press **OK** to store.

Auto Tune

Using Auto Tuning

1. To set up the television using **Auto tuning**, press **MENU** and use ◀ or ▶ to select the **SET UP** menu.



SET UP 1/2

Language ◀ English ▶

Country UK

Auto tuning OK

Manual tuning OK

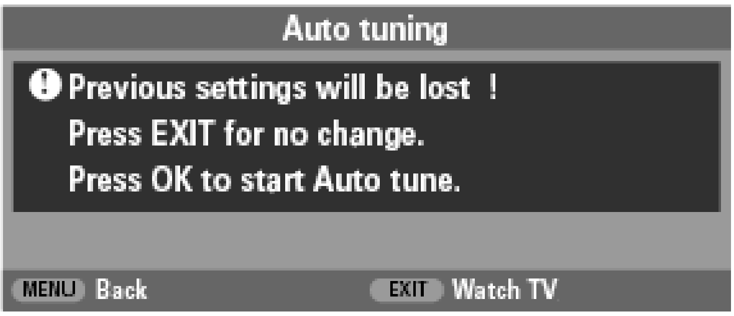
Antenna Amplifier On

AV connection OK

2. Press ▼ to highlight **Language** and ◀ or ▶ to select.

3. Using ▼ highlight **Country**, then ◀ or ▶ to select. The television will now tune the stations for your country.

4. Press ▼ to highlight **Auto tuning**, then press **OK**.



Auto tuning

Previous settings will be lost !
Press EXIT for no change.
Press OK to start Auto tune.

5. To start **Auto tuning** press **OK**.

5 To start **Auto Tuning** press **OK**.

You must allow the television to complete the search.

When the search is complete the television will automatically select programme position 1. This may not be a clear picture, so the station order can be changed (*see* 'Sorting programme positions' *section*).

SAFETY INSTRUCTION

Handling the LCD Module

Safety Precaution

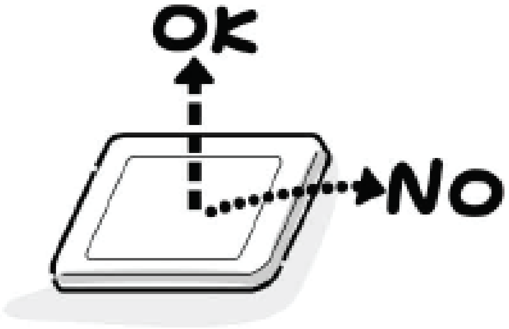
In the event that the screen is damaged or the liquid crystal (fluid) leaks, do not breathe in or drink this fluid.
Also, never touch this fluid. Such actions could cause toxicity or skin irritation. If this fluid should enter the mouth, rinse the mouth thoroughly with water. If the fluid should contact the skin or clothing, wipe off with alcohol, etc., and rinse thoroughly with water. If the fluid should enter the eyes, immediately rinse the eyes thoroughly with running water.

Precautions for Handling the LCD Module

CAUTION: The metal edges of the LCD module are sharp, handle it with care.

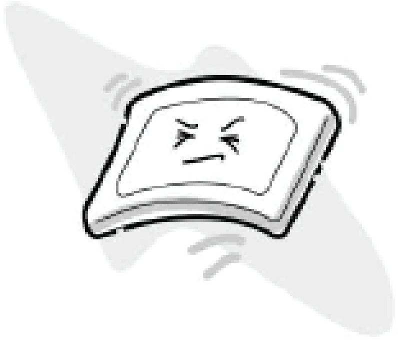
The LCD module can easily be damaged during disassembly or reassembly; therefore, always observe the following precautions when handling the module.

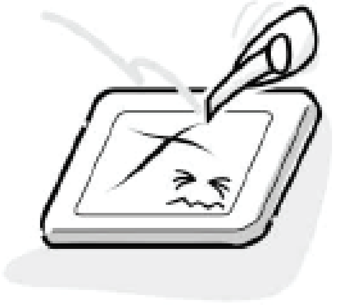
1. When attaching the LCD module to the LCD cover, position it appropriately and fasten at the position where the display can be viewed most conveniently.



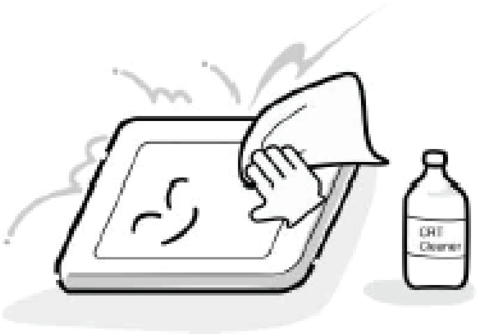
2. Carefully align the holes at all four corners of the LCD module with the corresponding holes in

the LCD cover and fasten with screws. Do not strongly push on the module because any impact can adversely affect the performance. Also use caution when handling the polarized screen because it can easily be damaged.

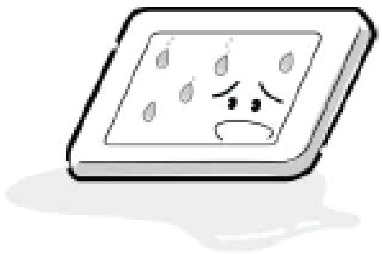




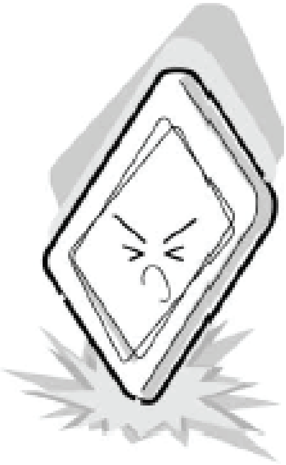
3. If the panel surface becomes soiled, wipe with cotton or a soft cloth. If this does not remove the soiling, breathe on the surface and then wipe again.
   If the panel surface is extremely solied, use a CRT cleaner as a cleaner. Wipe off the panel surface by drop the cleaner on the cloth. Do not drop the cleaner on the panel. Pay attention not to scratch the panel surface.



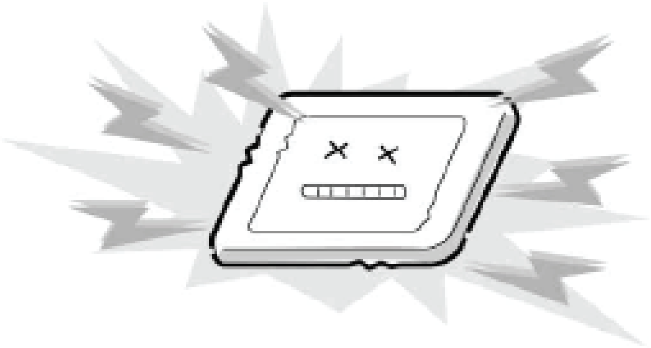
4. Leaving water or other fluids on the panel screen for an extended period of time can result in discoloration or stripes. Immediately remove any type of fluid from the screen.



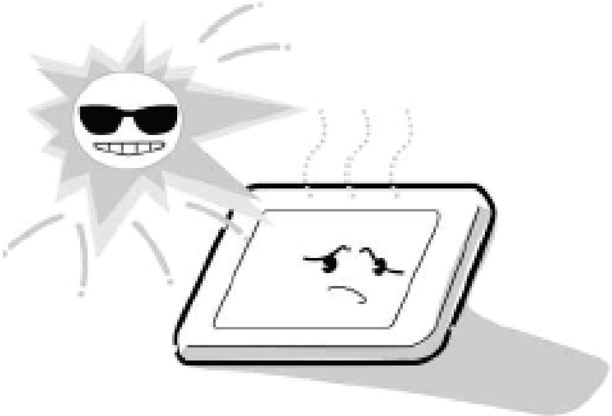
5. Glass is used in the panel, so do not drop or strike with hard objects. Such actions can damage the panel.



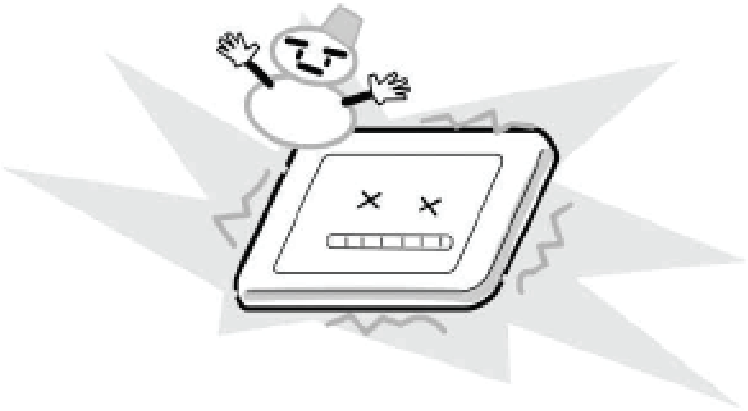
6. CMOS-LSI circuitry is used in the LCD module, so avoid damage due to static electricity. When handling the module, use a wrist ground or anchor ground.

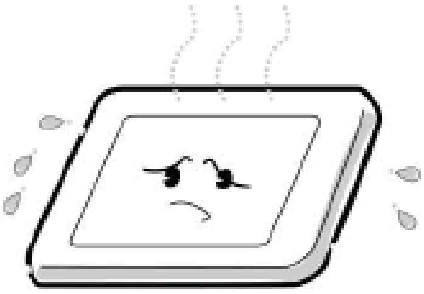


7. Do not expose the LCD module to direct sunlight or strong ultraviolet rays for an extended period of time.

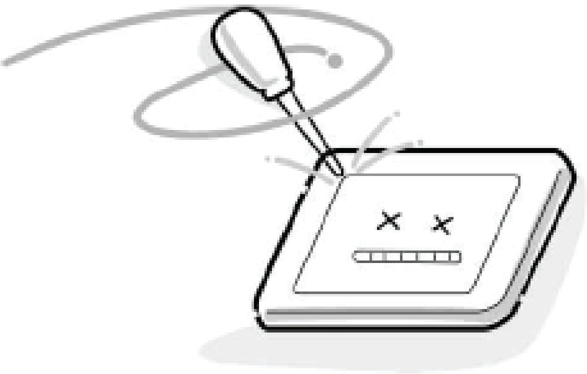


8. Do not store the LCD module below the temperature conditions described in the specifications. Failure to do so could result in freezing of the liquid crystal due to cold air or loss of resilience or other damage.

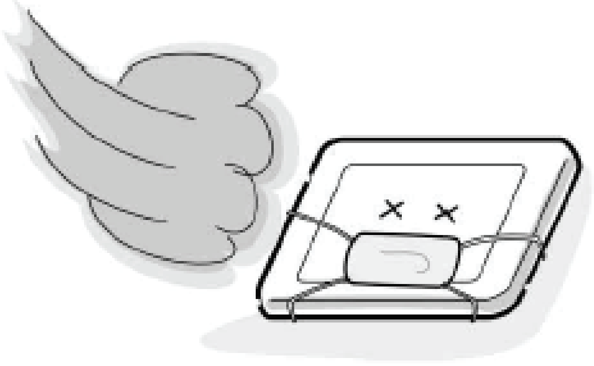




9. Do not disassemble the LCD module. Such actions could result in improper operation.



10. When transporting the LCD module, do not use packing containing epoxy resin (amine) or silicon resin (alcohol or oxim). The gas generated by these materials can cause loss of polarity.



PANEL IDENTIFICATION

Panel Identification

If the several panels are alternatively used in the same model without amending the model name, the identification marking will be shown at the last digit of the set serial number on the specification label on the back cover.

In servicing, do not alter the panel because several setting and parts are different.

Marking	Panel Vendor	Marking	Panel Vendor	Marking	Panel Vendor
A	AUO	J		S	SHARP
B		K	SAMSUNG	T	
C	CMO	L	LPL	U	
D		M		V	
E		N		W	
F		P		X	
G		Q		Y	
H	IPS	R		Z	

With this alternative use, some of key parts may differ and their combinations are indicated with the suffix marking on the location number in the part list (Miscellaneous).

e.g.

Location No.	Part No.	Description
B001A	75007869	LCD Panel, 32" LPL
B001B	75007870	LCD Panel, 32" AUO
MZ01A	75006036	LDVS Cable
MZ01B	75007893	LDVS Cable

SAFETY INSTRUCTION

WARNING: BEFORE SERVICING THIS CHASSIS, READ THE "SAFETY PRECAUTION" AND "PRODUCT SAFETY NOTICE" INSTRUCTIONS BELOW.

Safety Precaution

WARNING: SERVICING SHOULD NOT BE ATTEMPTED BY ANYONE UNFAMILIAR WITH THE NECESSARY PRECAUTIONS ON THIS RECEIVER. THE FOLLOWING ARE THE NECESSARY PRECAUTIONS TO BE OBSERVED BEFORE SERVICING THIS CHASSIS.

1. An isolation transformer should be connected in the power line between the receiver and the AC line before any service is performed on the receiver.

2. Always disconnect the power plug before any disassembling of the product. It may result in electrical shock.

3. When replacing a chassis in the cabinet, always be certain that all the protective devices are put back in place, such as nonmetallic control knobs, insulating covers, shields, isolation resistor-capacitor network, etc.

4. Always keep tools, components of the product, etc away from the children, These items may cause injury to children.

5. Depending on the model, use an isolation transformer or wear suitable gloves when servicing with the power on, and disconnect the power plug to avoid electrical shock when replacing parts. In some cases, alternating current is also impressed in the chassis, so electrical shock is possible if the chassis is contacted with the power on.

6. Always use the replacement parts specified for the particular model when making repairs. The parts used in products require special safety characteristics such as inflammability, voltage resistance, etc. therefore, use only replacement parts that have these same characteristics. Use only the specified parts when the ⚠ mark is indicated in the circuit diagram or parts list.

7. Parts mounting and routing dressing of wirings should be the same as that used originally. For safety purposes, insulating materials such as isolation tube or tape are sometimes used and printed circuit boards are sometimes mounted floating. Also make sure that wirings is routed and clamped to avoid parts that generate heat and which use high voltage. Always follow the manufactured wiring routes / dressings.

8. Always ensure that all internal wirings are in accordance before re-assembling the external casing after a repairing completed. Do not allow internal wiring to be pinched by cabinets, panels, etc. Any error in reassembly or wiring can result in electrical leakage, flame, etc., and may be hazardous.

9. NEVER remodel the product in any way. Remodeling can result in improper operation, malfunction, or electrical leakage and flame, which may be hazardous.

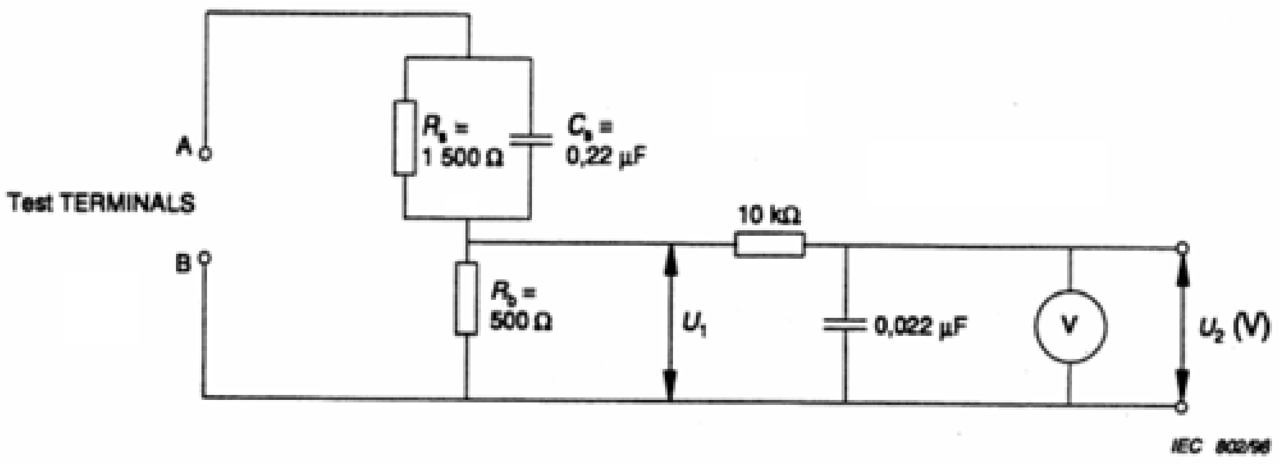
10. Touch current check. (After completing the work, measure touch current to prevent an electric shock.)

    - Plug the AC cord directly into the AC outlet. Do NOT use an isolation transformer for this check.

    - Connect a measuring network for touch currents between each exposed metallic part on the set and a good earth ground such as a water pipe.

Annex D
(normative)

Measuring network for TOUCH CURRENTS



Resistance values in orms (Ω).

V: Voltmeter or oscilloscope
(r.m.s. or peak reading)

Input resistance : ≧ 1 MΩ
Input capacitance : ≦ 200 pF
Frequency range : 15 Hz to 1 MHz and d.c. respectively

Note: Appropriate measures should be taken to obtain the correct value in case of non sinusoidal waveforms.

The measuring instrument is calibrated by comparing the frequency factor of U_2 with the solid line in figure F.2 of IEC 60990 at various frequencies. A calibration curve is constructed showing the deviation of U_2 from the ideal curve as a function of frequency.

TOUCH CURRENT = U_2/500 (peak value).

- The potential at any point (TOUCH CURRENT) expressed as voltage U_1 and U_2 does not exceed the following value:

The part or contact of a TERMINAL is not HAZARDOUS LIVE if:

a) The open-circuit voltage should not exceed 35 V (peak) a.c. or 60 V d.c. or, if a) is not met.
b) The measurement of the TOUCH CURRENT shall be carried out in accordance with IEC 60990, with the measuring network described in **Annex D** of this standard.

The TOUCH CURRENT expressed as voltages U_1 and U_2, does not exceed the following values:
- for a.c. : U_1 = 35 V (peak) and U_2 = 0.35 V (peak);
- for d.c. : U_1 = 1.0 V

Note: The limit values of U_2 = 0.35 V (peak) for a.c. and U_1 = 1.0 V for d.c. correspond to the values 0.7 mA (peak) a.c. and 2.0 mA d.c.

Product Safety Notice

Many electrical and mechanical parts in this chassis have special safety-related characteristics. These characteristics are often passed unnoticed by a visual inspection and the protection afforded by them cannot necessarily be obtained by using replacement components rated for higher voltage, wattage, etc. Replacement parts which have these special safety characteristics are identified in this manual and its supplements; electrical components having such features are identified by the international hazard symbols on the schematic diagram and the parts list.
Before replacing any of these components, read the parts list in this manual carefully. The use of substitute replacement parts which do not have the same safety characteristics as specified in the parts list may create electrical shock, fire, or other hazards.

SPECIFICATION

DVB-T Broadcast systems/channels

UK	UHF	21-68
France	VHF	05-10 (VHF 01-05)
	UHF	21-69
Germany	VHF	05-12
	UHF	21-69
Austria	VHF	05-12
	UHF	21-69
Switzerland	VHF	05-12
	UHF	21-69
Italy	VHF	05-12 (D,E,F,G,H,H1,H2)
	UHF	21-69
Spain	UHF	21-69
Nederlands	VHF	05-12
	UHF	21-69
Sweden	VHF	05-12
	UHF	21-69
Finland	VHF	05-12
	UHF	21-69
Greece	VHF	05-12
	UHF	21-69

Broadcast systems/channels

PAL-I	UHF	UK21-UK69
PAL-B/G	UHF	E21-E69
	VHF	E2-E12, S1-S41
SECAM-L	UHF	F21-F69
	VHF	F1-F10, B-Q
SECAM-D/K	UHF	R21-R69
	VHF	R1-R12

Video Input PAL, SECAM, NTSC 3.58/4.43

External connections

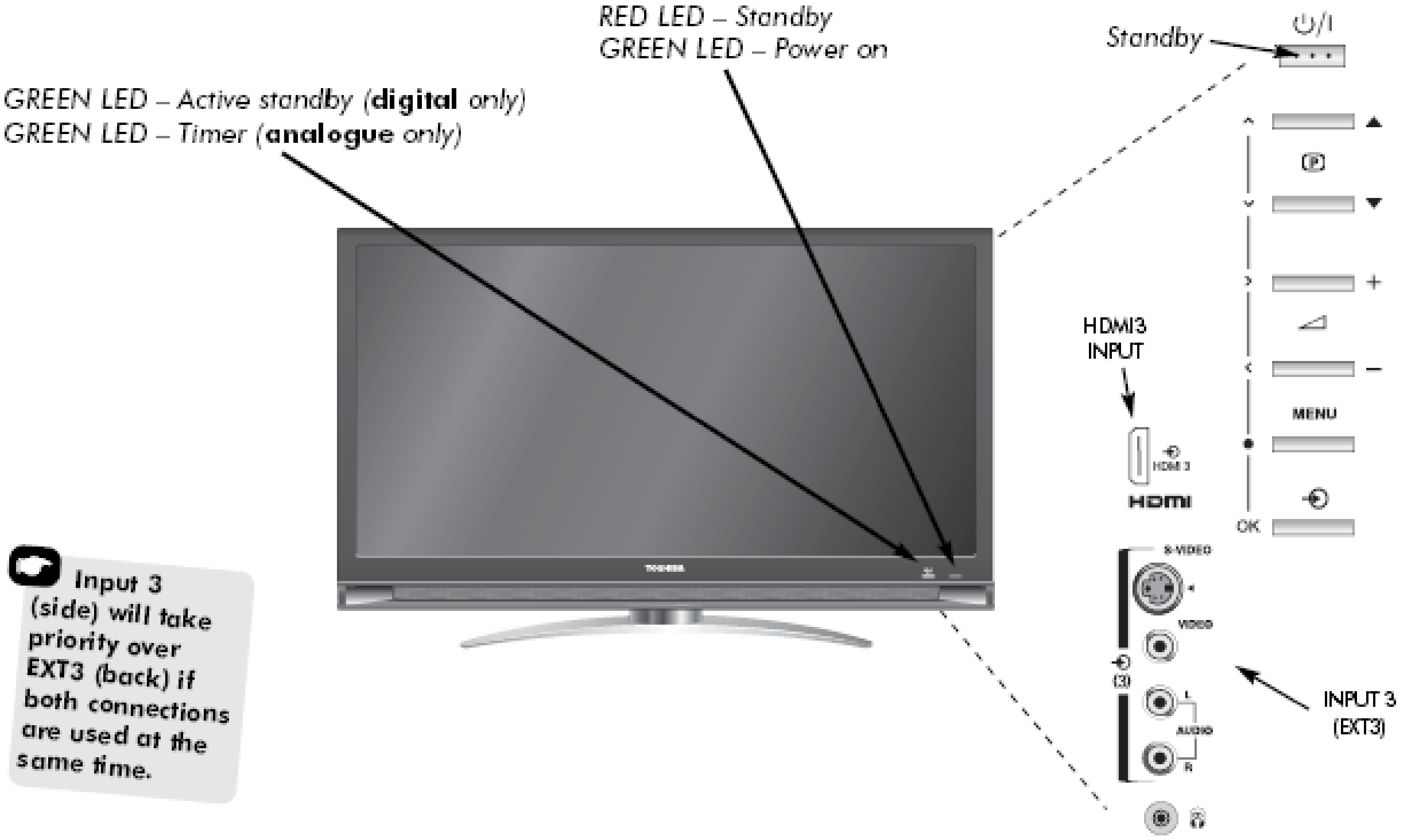
EXT1	Input/Output	21-pin SCART	RGB, A/V Selectable output
EXT2	Input/Output	21-pin SCART	RGB, A/V, S-video Selectable output
EXT3	Input	Phono jacks	Y, P_B/C_B, P_R/C_R
		Phono jacks	A/V
EXT3	Input (Side)	Phono jack	Video, S-video
		Phono jacks	Audio L + R
HDMI 1/2	Input	HDMI (1080p, LIP SYNC, Deep Color, xvYCC)	
HDMI 3	Input (Side)	HDMI (1080p, LIP SYNC, Deep Color, xvYCC)	
PC	Input	Mini D-sub 15-pin	Analog RGB signal
PC/HDMI1 Audio		Phono jacks	Audio L + R
Digital Audio Output (S/PDIF)		Optical	
Output to Active Super Woofer		Phono jack	

Stereo			Nicam
			2 carrier system
Visible Screen Size	Model	37	94cm
(approx.)		42	107cm
		47	120cm
		52	132cm
		57	144cm
Display			16:9
Sound output (at 10% distortion)		Main	10W + 10W
Power consumption	Model	37	222W
(approx.)		42	258W
		47	325W
		52	338W
		57	465W
Standby (approx.)	Model	37	0.9W
		42	0.9W
		47	0.9W
		52	0.9W
		57	0.9W
Dimensions	Model	37	64cm (H) 92cm (W) 30cm (D)
(approx.)		42	71cm (H) 103cm (W) 36cm (D)
		47	77cm (H) 114cm (W) 36cm (D)
		52	85cm (H) 128cm (W) 40cm (D)
		57	90cm (H) 137cm (W) 40cm (D)
			(Height dimension includes foot stand)
Weight (approx.)	Model	37	22kg
		42	29kg
		47	36kg
		52	46kg
		57	58kg
Headphone socket			3.5mm stereo
Accessories			Remote control
			2 batteries
			(AAA, IEC R03 1.5V)

FUNCTION AND OPERATION

Controls and Input Connections

A wide variety of external equipment can be connected to the input sockets on the side of the television. Whilst all the necessary adjustments and controls for the television are made using the remote control, the buttons on the television may be used for some functions.


RED LED – Standby
GREEN LED – Power on
GREEN LED – Active standby (**digital** only)
GREEN LED – Timer (**analogue** only)
Standby
⏻/I
P
+
−
MENU
OK
HDMI3 INPUT
HDMI 3
HDMI
S-VIDEO
VIDEO
(3)
L
AUDIO
R
INPUT 3 (EXT3)

Input 3 (side) will take priority over EXT3 (back) if both connections are used at the same time.

Switching On

If the RED LED is unlit check that the mains plug is connected to the power supply. If the picture does not appear press ⏻ on the remote control, it may take a few moments.

To put the television into Standby press ⏻ on the remote control, the GREEN active standby LED will appear (see *'Software Upgrade' section*) next to the RED for a few moments then disappear. To view the television press ⏻ again. The picture may take a few seconds to appear.

Using the Remote Control

Press the **MENU** button on the remote control to see the menus.

In **digital** mode the menu bar at the top of the screens will show three different topics shown in symbols. As each symbol is selected by pressing the ◀ or ▶ button on the remote control, its respective options will appear below.

In **analogue** mode the menu appears as a list of five topics. As each symbol is selected by pressing the ◀ or ▶ button on the remote control, its respective options will appear below.

To use the options press the ▲ and ▼ button on the remote control to move up and down through them and **OK**, ◀ or ▶ to select the required choice. Follow the on screen instructions. The functions of each menu are described in detail throughout the manual.

Using the Controls and Connections

To alter the volume press — ⊿ +.

To alter the programme position press ▼ Ⓟ ▲.

Press **MENU** and ❮, ❯, ⌄ or ⌃ to control the sound and picture options.

Press **MENU** twice to finish.

When connecting sound equipment to **EXT3**, connect the audio output of the equipment to the phono sockets on the television.

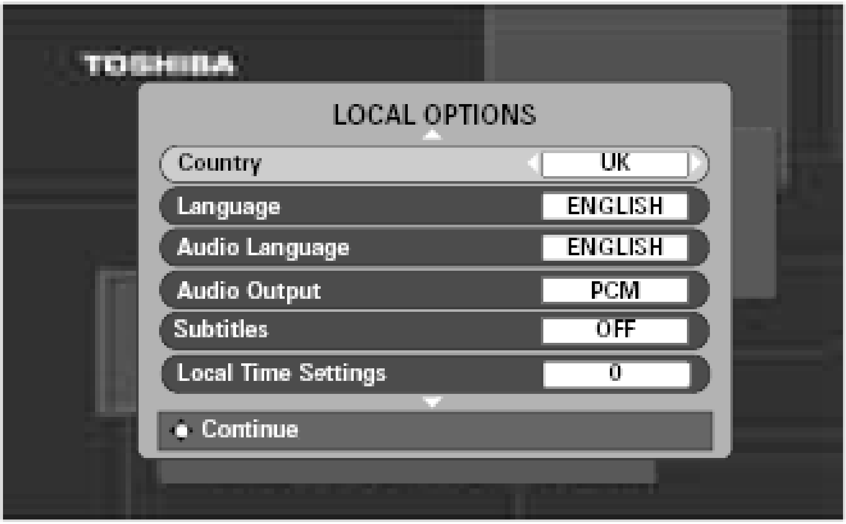
To select the Input, press ⇥ until the display shows **EXT3**. The television will automatically sense the type of signal.

Please note: Inserting the headphone plug will mute the sound from all speakers.

Tuning Digital Television for the First Time

Before switching on the television put your decoder and media recorder to **Standby** if they are connected and ensure the aerial is connected. To set up the television use the buttons on the remote control.

1. Press the ⏻ button, the **LOCAL OPTIONS** screen will appear. This screen will appear the first time that the television is switched on and each time the television is reset.



LOCAL OPTIONS

Country	UK
Language	ENGLISH
Audio Language	ENGLISH
Audio Output	PCM
Subtitles	OFF
Local Time Settings	0

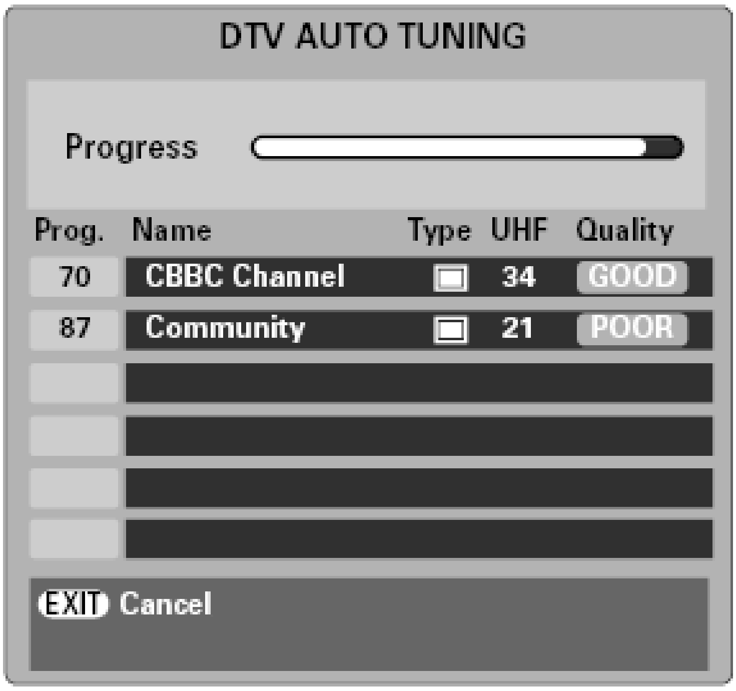
2. Press ▼ to highlight **Country** and ◀ or ▶ to select. The television will now tune the stations for your country.

3. Using ▼ highlight **Language**, then ◀ or ▶ to select.

4. Press **OK** to start tuning.

   The **DTV AUTO TUNING** screen will appear and the television will start to search for available stations. The progress bar will move along the line.

   You must allow the television to complete the search.



DTV AUTO TUNING

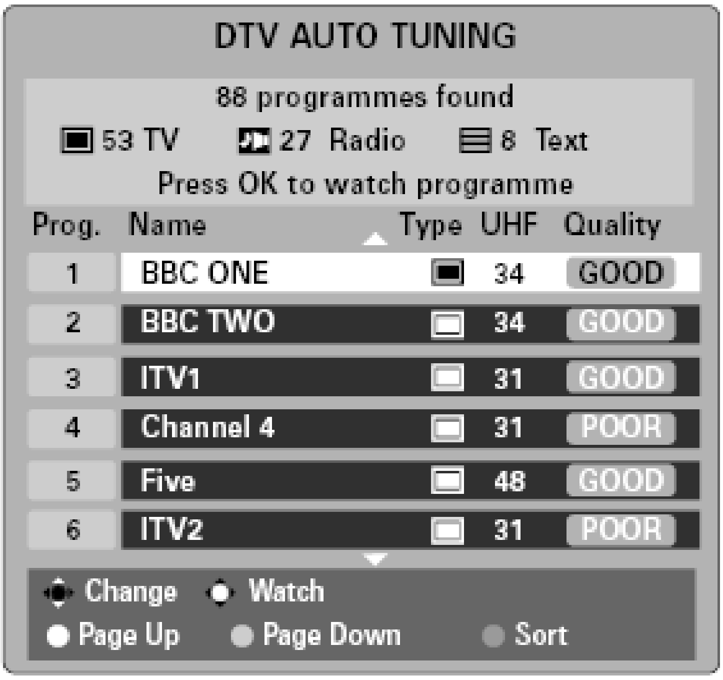
Progress

Prog.	Name	Type	UHF	Quality
70	CBBC Channel	▣	34	GOOD
87	Community	▣	21	POOR

EXIT Cancel

When the search is complete the television will automatically select programme one.

The **DTV AUTO TUNING** screen will display the total number of services found.



DTV AUTO TUNING

88 programmes found

▣ 53 TV   27 Radio   ☰ 8 Text

Press OK to watch programme

Prog.	Name	Type	UHF	Quality
1	BBC ONE	▣	34	GOOD
2	BBC TWO	▣	34	GOOD
3	ITV1	▣	31	GOOD
4	Channel 4	▣	31	POOR
5	Five	▣	48	GOOD
6	ITV2	▣	31	POOR

5 Use ▼ or ▲ to move through the list to select a programme then press **OK** to view.

NOTE: The time will be set automatically by transmission but can be advanced or decreased by up to 2 hours using **Local Time Settings**.

Digital Auto Tuning

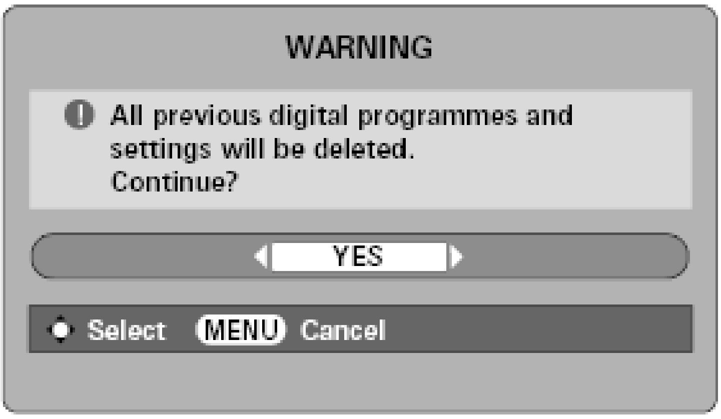
Note: As new services are broadcast it will be necessary to re-tune the television in order to view them.

Auto Tuning

Auto tuning *will completely re-tune the television and can be used to up date the channel list.* ***It is recommended that Auto tuning is run periodically to ensure that all new services are added.*** *All current programmes and settings, i.e. favourites and locked programmes, will be lost.*

1. Press **MENU**, the **SETUP MENU** will appear. Use ▼ to highlight **Auto tuning**. Press **OK**.

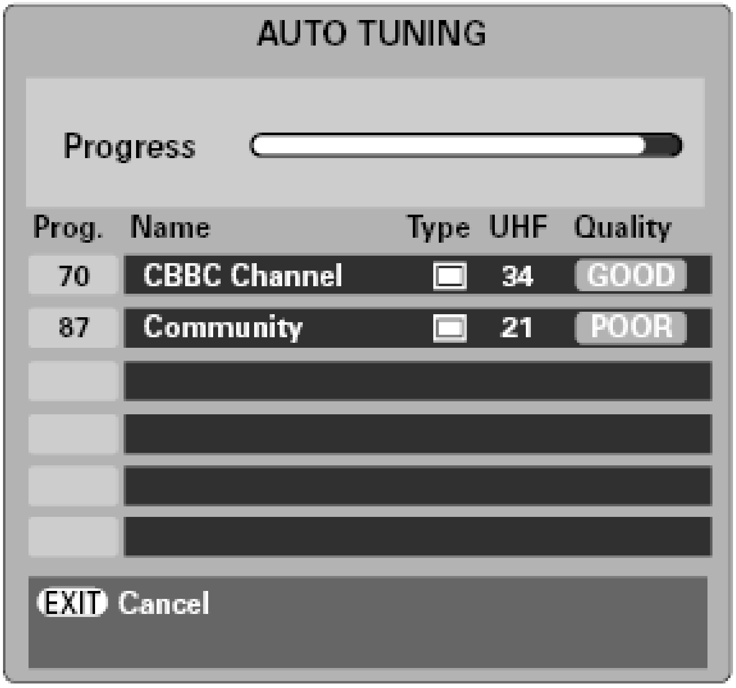
2. A screen will appear warning that previous digital programmes and settings will be deleted. Press **OK** to continue with the **Auto tuning**.



WARNING

All previous digital programmes and settings will be deleted. Continue?

The television will start to search for all available stations.



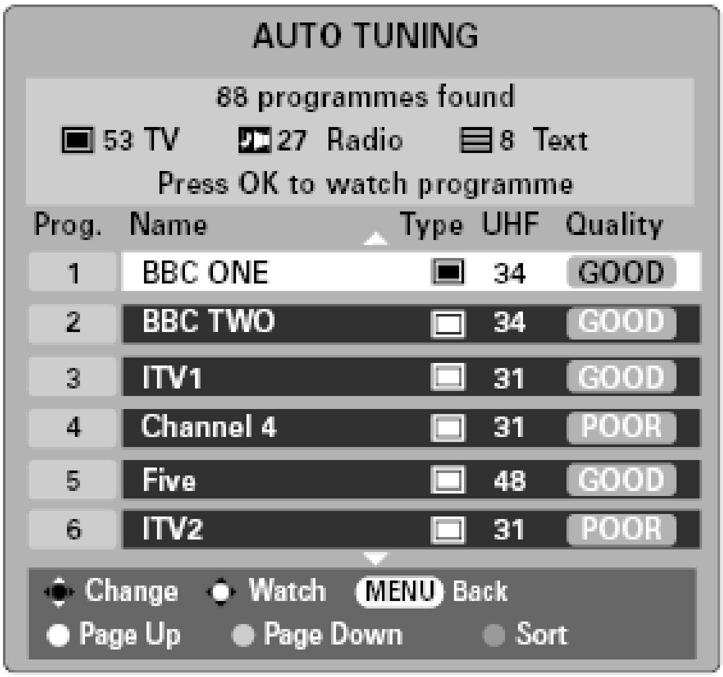
AUTO TUNING

Progress

Prog.	Name	Type	UHF	Quality
70	CBBC Channel	▭	34	GOOD
87	Community	▭	21	POOR

You must allow the television to complete the search.

When the search is complete the **Auto tuning** screen will display the total number of services found.



AUTO TUNING

88 programmes found

▣ 53 TV ♫ 27 Radio ▤ 8 Text

Press OK to watch programme

Prog.	Name	Type	UHF	Quality
1	BBC ONE	▣	34	GOOD
2	BBC TWO	▭	34	GOOD
3	ITV1	▭	31	GOOD
4	Channel 4	▭	31	POOR
5	Five	▭	48	GOOD
6	ITV2	▭	31	POOR

3 Use ▼ or ▲ to move through the list to select a programme then press **OK** to view.

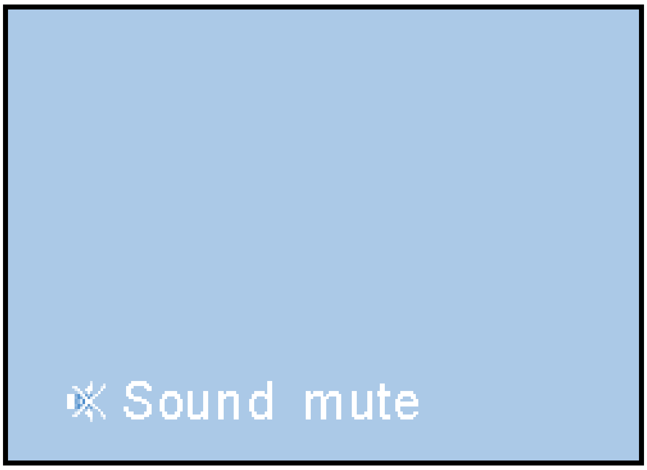
Analogue Switch-Off

This is a **digital** television which is integrated to allow the use of both **digital** and **analogue** services. However, during the lifetime of this set it is very likely that **analogue** services will be switched off to allow for more new **digital** services.

This 'switch-off' will happen in a number of phases, which will be advertised in your area well in advance. It is recommended that at each phase the television is re-tuned to ensure that existing and new **digital** services can be viewed without disruption *(see the* 'Auto tuning' *section)*.

Service Mode

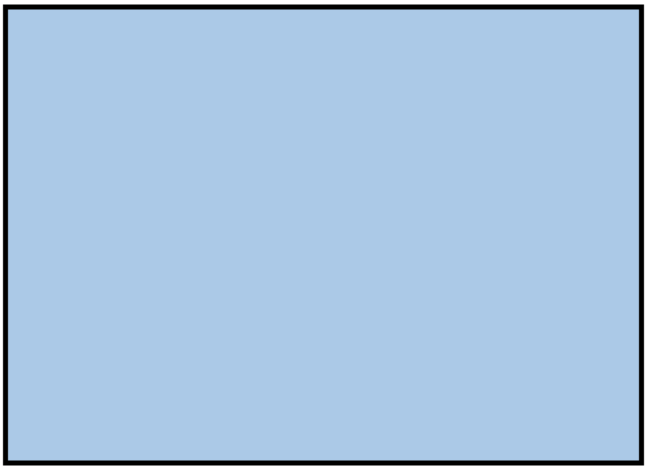
Entering to Service Mode



🔇 Sound mute

1. Set VOLUME to minimum and press 🔇 button once on remote control.

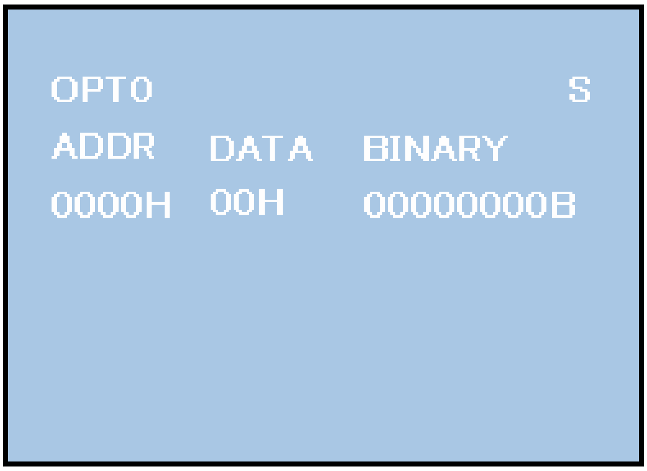
↓



2. Press 🔇 button again and hold button down.

↓

Service Mode display



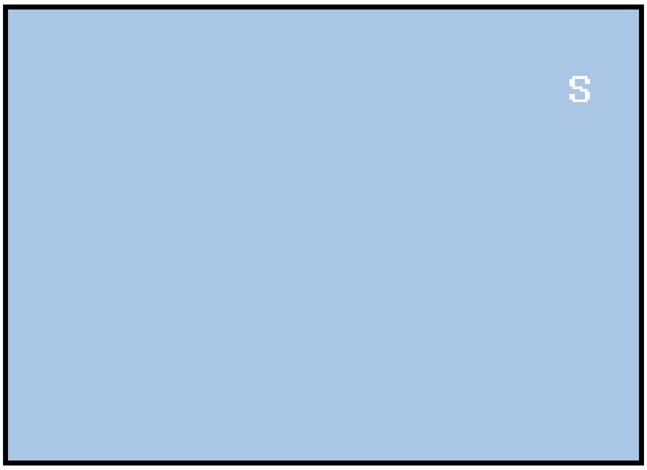
```
OPTO                          S
ADDR    DATA    BINARY
0000H   00H     00000000B
```

3. While holding the 🔇 button, press MENU button on TV set.

Displaying the Adjustment Menu

Press MENU button on TV.

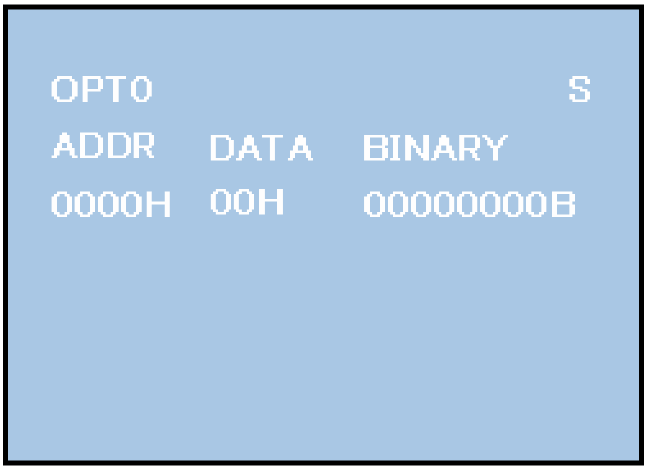
Service Mode



```
                              S
```

Press ↑ ↓ Press

Adjustment Mode



```
OPTO                S
ADDR   DATA   BINARY
0000H  00H    00000000B
```

Key Function in the Service Mode

The following key entry during display of adjustment menu provides special functions.

CAUTION: Never try to perform initialization unless you have changed the memory IC.

Test signal selection	⇥ button (on remote control)
Selection of the adjustment items	CH ▲/▼ (on TV or remote control)
Change of the data value	Volume ◿ +/- (on TV or remote control)
Adjustment menu mode ON/OFF	MENU button (on TV)
Initialization of the memory (QA02)	CALL + CH ▲ button on TV
Reset the count of operating protect circuit to "00"	CALL + CH ▼ button on TV
Turn off I2C bus communication	CALL + Volume ◿ + button on TV
"RCUT" selection	1 button
"GCUT" selection	2 button
"BCUT" selection	3 button
"CNTX" selection	4 button
"COLC" selection	5 button

"UVTT" selection	6 button
Automatic A/D Adjustment (PC, Component, Composite (PAL, NTSC))	7 button
Self diagnostic display ON/OFF	9 button

Selecting the Adjusting Item

Every pressing of CH ▲ button in the service mode changes the adjustment items in the order of table below. (▼ button for reverse order)

SETTING & ADJUSTING DATA

[SERVICE MODE]

ADJUSTING ITEMS AND DATA IN THE SERVICE MODE:

Note:

The image system data of RCUT-BDRV is different by each image format.
The PAL value is indicated in the table.
Never adjust H.POS and V.POS except PAL/WIDE mode.

Item	Name of adjustment
RCUT	R CUT OFF
GCUT	G CUT OFF
BCUT	B CUT OFF
RDRV	R DRIVE
GDRV	G DRIVE

BDRV	B DRIVE
BRTC	BRIGHTNESS CENTER
COLC	COLOR CENTER
UVTT	BASE BAND TINT
CNTX	CONTRAST MAX
VOLUX	MAX VOLUME LIMITED
PLLW0	PLL WAIT TIME
PLLW1	PLL WAIT TIME
PLLW2	PLL WAIT TIME
PLLW3	PLL WAIT TIME
PLLW4	PLL WAIT TIME
PLLW5	PLL WAIT TIME
OPT1	TV SET OPTION 1
OPT2	TV SET OPTION 2
OPT3	TV SET OPTION 3
OPT4	TV SET OPTION 4 (PANEL OPT DATA FOR VENDOR)
OPT5	TV SET OPTION 5 (PANEL OPT DATA FOR SIZE)
OPT6	TV SET OPTION 6 (HOTEL MODE)
OPT7	TV SET OPTION 7 (HOTEL MODE)
OPT8	TV SET OPTION 8 (HOTEL MODE)
TVOP	TV SET OPTION
ID	MODEL ID
BDWID	BORDER WIDTH FOR EXACT SCAN
BDHIT	BORDER HEIGHT FOR EXACT SCAN
FPLG	MASK_ENABLE (SD/HD)
FPLG2	Channel LOGO Vector Clip SW (SD)
FPLG3	Channel LOGO Vector Clip SW (HD)

Factory preset data will be loaded after setting Model ID data.
(Refer to Initialization of Memory Data of QA02 and setting data of signal board.)

Adjusting the Data

Pressing of VOLUME ◢ +/- button will change the value of data in the range from 00H to FFH. The variable range depends on the adjusting item.

I2C Bus Off

Turn off I2C communication between IC700 and IC400.

1) Press and hold the CALL button on the remote control, then press the Volume ◢ + button on the TV.

2) Display "BUS Off" OSD.

3) I2C communication turned off.

Note:

To return Bus on status, press and hold the CALL button on the remote control, then press the Volume ◢ + button on the TV again. TV will be turned off and automatically turned on, then status will be Bus On.

Setting TVOP

Enter to service mode and select menu of TVOP by pressing P▲ or P▼ during display of adjustment menu. After selecting TVOP, press ◢ + or ◢ - to set I2C check function to disable or enable as below.

TVOP	FUNCTION DESCRIPTION	1	0 (Normal)

D5 (bit5)	I2C check between IC700 and IC400. (WDT)	Disable	Enable

Setting Panel Option Data

Panel option data is subject to OP4 and OP5.
Enter to service mode and select menu of OPT4 or OPT5 by pressing P▲ or P▼ during display of adjustment menu. After selecting OPT4 or OPT5, press ◿+ or ◿- to set OPT4 or OPT5 value as table below.

Panel option data

Series		Model name	Panel vendor	OPT4 value	OPT5 value
Z3000	EU/UK Digital	37Z3030D	LPL	0xC1	0x06
		42Z3030D	LPL	0xC1	0x07
		47Z3030D	LPL	0xC6	0x08
		52Z3030D	LPL	0xC1	0x0B
			SAMSUNG	0xC6	0x0B
		57Z3030D	SAMSUNG	0xC6	0x0D
		40ZF355D	SAMSUNG	0xC6	0x0F
		46ZF355D	SAMSUNG	0xC6	0x0A
		52ZF355D	SAMSUNG	0xC6	0x0B

OP4

Ex. OPT4 value 0x05 indicates that panel vendor is AUO AMVA.

	OPT4							
	D7 (bit7)	D6 (bit6)	D5 (bit5)	D4 (bit4)	D3 (bit3)	D2 (bit2)	D1 (bit1)	D0 (bit0)

Panel vendor	LPL	-	-	-	-	0	0	0	1
	SHP	-	-	-	-	0	0	1	0
	CMO	-	-	-	-	0	0	1	1
	AUO PMVA	-	-	-	-	0	1	0	0
	AUO AMVA	-	-	-	-	0	1	0	1
	SAMSUNG	-	-	-	-	0	1	1	0
	IPS	-	-	-	-	0	1	1	1
Panel Resolution	Full HD	1	-	-	-	-	-	-	-
	WXGA	0	-	-	-	-	-	-	-
Panel 100Hz	On	-	1	-	-	-	-	-	-
	Off	-	0	-	-	-	-	-	-

OP5

Ex. OPT5 value 0x06 indicates that panel size is 37.

		OPT5							
		D7 (bit7)	**D6 (bit6)**	**D5 (bit5)**	**D4 (bit4)**	**D3 (bit3)**	**D2 (bit2)**	**D1 (bit1)**	**D0 (bit0)**
Size	37	-	-	-	-	0	1	1	0
	42	-	-	-	-	0	1	1	1
	47	-	-	-	-	1	0	0	0
	52	-	-	-	-	1	0	1	1
	57	-	-	-	-	1	1	0	1

Convert from Bit (Binary) to Hex

The table for converting from bit (D7-D0) to hex (0x**).

		BIT (Binary)			
High nibble		**D7**	**D6**	**D5**	**D4**
Low nibble		**D3**	**D2**	**D1**	**D0**
HEX	**0**	0	0	0	0
	1	0	0	0	**1**
	2	0	0	**1**	0
	3	0	0	**1**	**1**
	4	0	**1**	0	0
	5	0	**1**	0	**1**
	6	0	**1**	**1**	0
	7	0	**1**	**1**	**1**
	8	**1**	0	0	0
	9	**1**	0	0	**1**
	A	**1**	0	**1**	0
	B	**1**	0	**1**	**1**
	C	**1**	**1**	0	0
	D	**1**	**1**	0	**1**
	E	**1**	**1**	**1**	0
	F	**1**	**1**	**1**	**1**

E.g. If Bit D7-0 = 0**1**0**1** **1**0**1**0, Hex data is 0x**5A**.

Exit from Service Mode

Pressing POWER button to turn off the TV once.

Initialization of Memory Data of QA02 and Setting Data of Signal Unit

After replacing QA02 or signal board, the following initialization is required.

CAUTION: Never attempt to initialize the data unless QA02 has been replaced. Whenever using new signal board to the set, setting the Model ID data according to Panel option data.

1) Enter the service mode.

2) Select menu of ID by pressing P▲ or P▼ during display of adjustment menu in the service mode.

3) Change ID data into MODEL ID to initialize by pressing ◿ + or ◿ -, refer to table below.

4) Press and hold the CALL button on the remote control, then press the CHANNEL ▲ button on the TV.

5) Initialization progress dialog including model name and panel vendor is shown. Progress status is "WRITING".

6) Progress status is changed "OK" and power cycle (automatically). Then QA02 initialization has been completed.

7) Enter the service mode and select version check mode. Confirm if model name and model id set is correct. If not, repeat steps 1) to 6).

Note:

In case initialization by setting wrong MODEL ID is done, there is a possibility of abnormal display.

8) Set I2C check function of TVOP to enable.

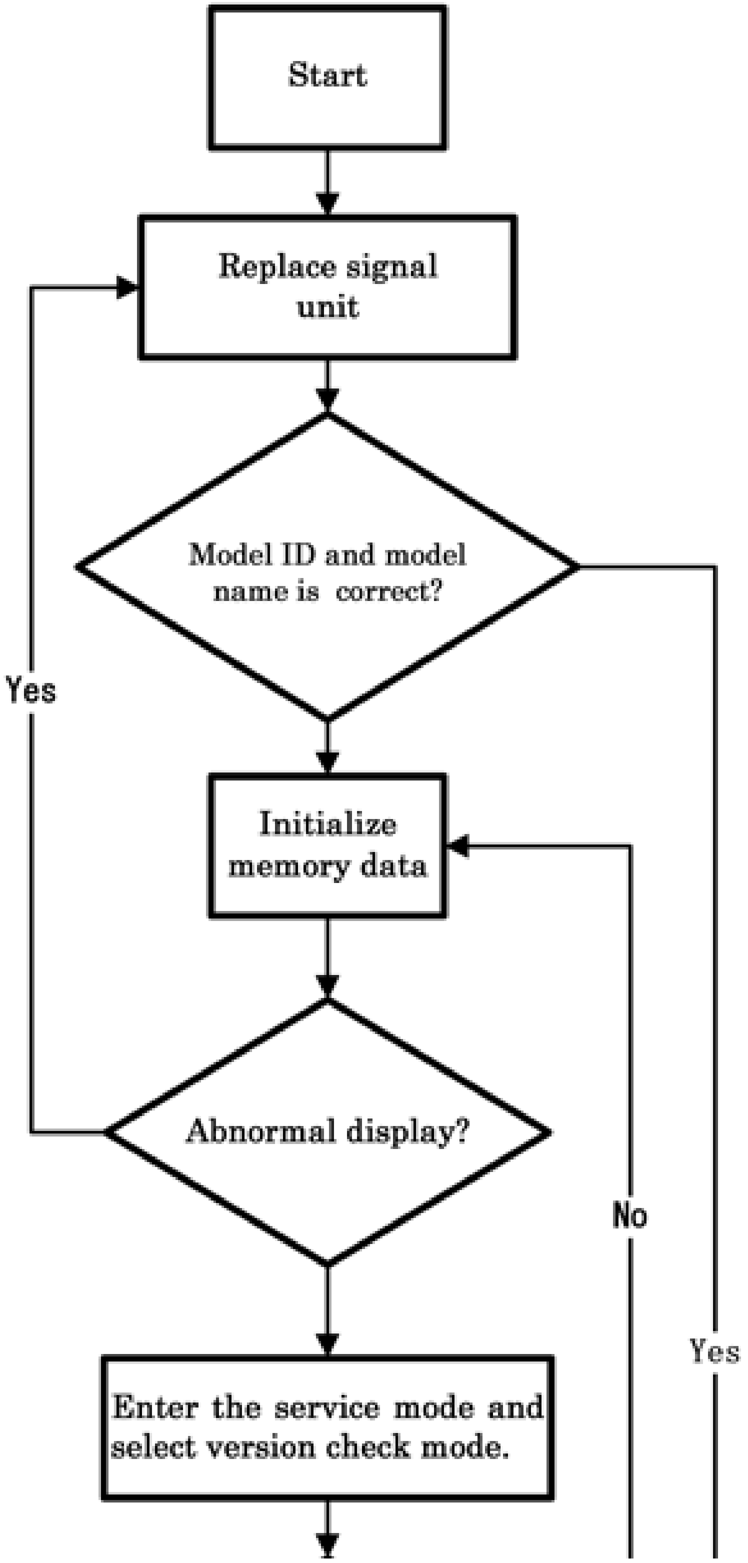
9) Check the picture carefully. If necessary, adjust any adjustment item above. Perform "Auto tune" on the owner's manual.

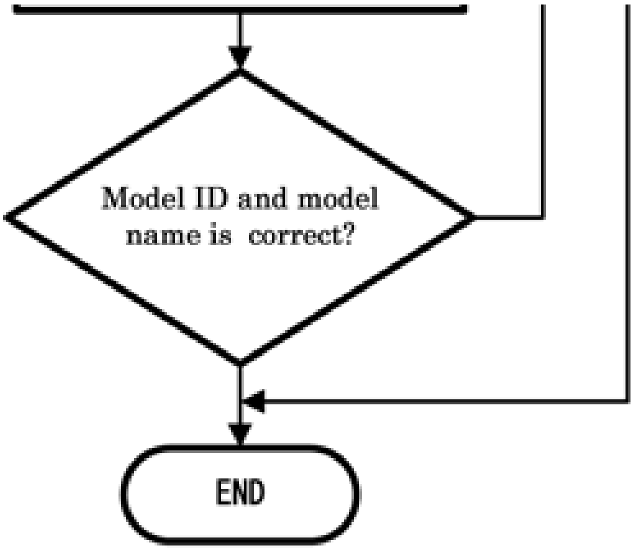
MODEL ID (HEX)	Model name	Panel vendor

0x01	37Z3030D	LPL
0x02	42Z3030D	LPL
0x03	47Z3030D	LPL
0x04	52Z3030D	LPL
0x05	57Z3030D	SAMSUNG
0x06	52Z3030D	SAMSUNG
0x07	40ZF355D	SAMSUNG
0x08	46ZF355D	SAMSUNG
0x09	52ZF355D	SAMSUNG
0x10		
0x11		
0x12		
0x13		
0x14		
0x15		
0x16		
0x17		
0x18		
0x19		
0x20		
0x21		
0x22		
0x23		
0x24		
0x25		
0x26		
0x27		
0x28		
0x29		
0x30		

0x31		
0x32		
0x33		

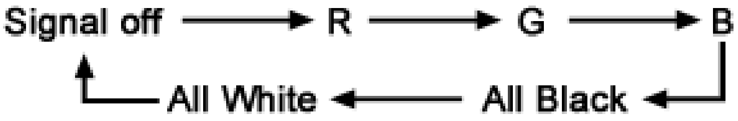
Initializing Data setting flowchart after replacing the Signal Unit


Start
Replace signal unit
Model ID and model name is correct?
Yes
Initialize memory data
Abnormal display?
No
Yes
Enter the service mode and select version check mode.


Model ID and model name is correct?
END

Test Signal Selection

Every pressing of ⊖ button on the remote control changes the built-in test patterns on screen as described below in Service Mode.


Signal off
R
G
B
All Black
All White

Picture	Signal
🟥	Red raster
🟩	Green raster

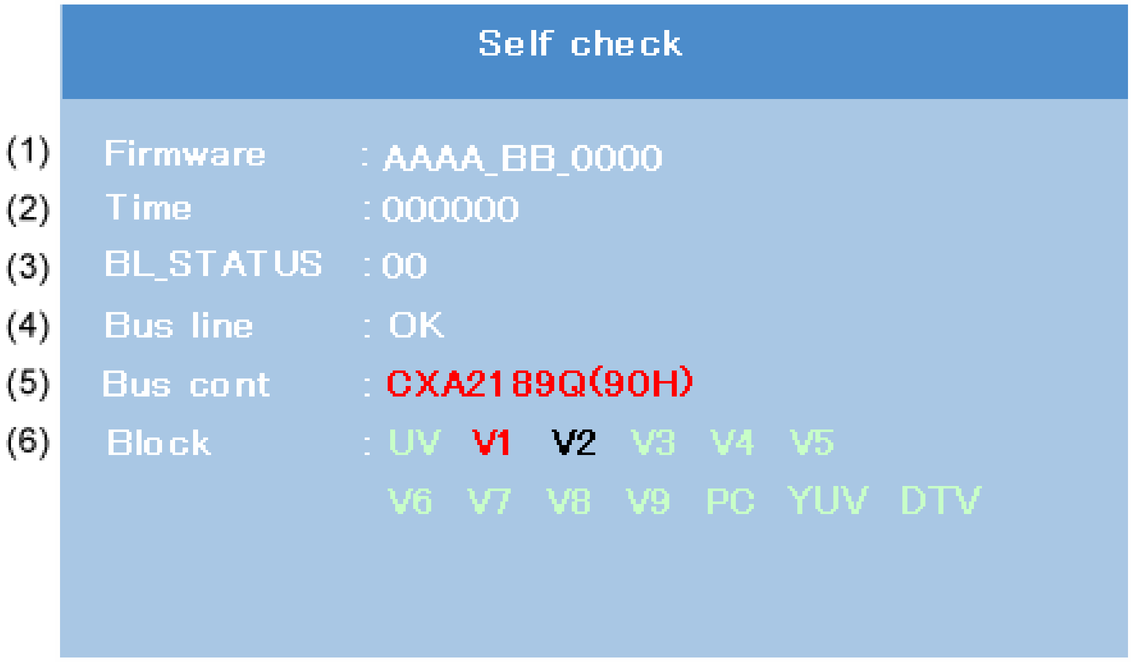
🟦	Blue raster
⬛	All Black
⬜	All White

Self Diagnostic Function

1. Press "9" button on remote control during display of adjustment menu in the service mode. The diagnosis will begin to check if interface among IC's is executed properly.

2. During diagnosis, the following displays are shown.

* Self check display and Item are subject to the models.



Self check

(1) Firmware : AAAA_BB_0000

(2) Time : 000000

(3) BL_STATUS : 00

(4) Bus line : OK

(5) Bus cont : CXA2189Q(90H)

(6) Block : UV V1 V2 V3 V4 V5
V6 V7 V8 V9 PC YUV DTV

(1) Firmware :
Version information of microprocessor
Series name (AAAA) and market area (BB) and software program version (0000)

(2) Time : Total hour of turn the TV on. (Unit : H)

(3) BL_STATUS :
Counter for saving BackLight on error.
This value is counted till 99 (Decimal).

(4) Bus line : -- "OK" is normal
SCL-GND (Red indication) : SCL-GND short circuit
SDA-GND (Red indication) : SDA-GND short circuit
SCL-SDA (Red indication) : SCL-SDA short circuit

(5) Bus cont : --- "OK" is normal.
NG is abnormal (Red indication).
When the abnormal status is detected, type name of semiconductor is indicated in red colour.

(6) Block

UV : TV reception mode
V1 - V9 : VIDEO 1-9 input mode
PC : PC mode
YUV : YUV mode
DTV : DTV mode

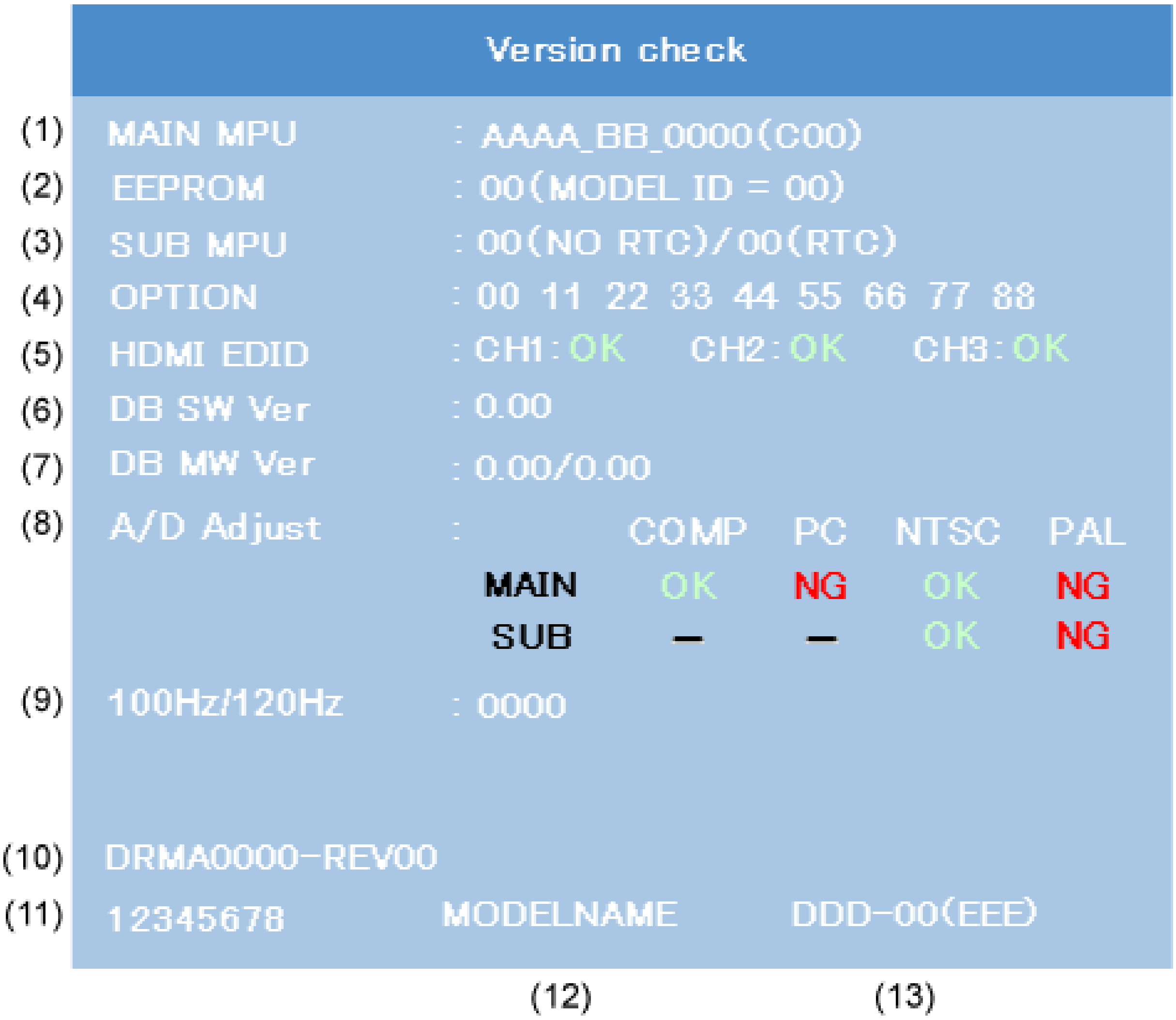
UV	RF
V1	⇥1 (SCART (FULL))
V2	⇥2 (SCART (FULL))
V3	⇥3 (Component/Composite/S)
V4	HDMI1
V5	HDMI2
V6	HDMI3
V7	--
V8	--
V9	--
PC	PC
YUV	--
DTV	DTV (Digital Only)

Version Check Mode

1. Press "9" button twice on remote control during display of adjustment menu in the service mode. The version of main MPU will be checked.

2. During Version Check, the following displays are shown.

* Version check display and Item are subject to the models.



Version check

(1) MAIN MPU : AAAA_BB_0000(C00)

(2) EEPROM : 00(MODEL ID = 00)

(3) SUB MPU : 00(NO RTC)/00(RTC)

(4) OPTION : 00 11 22 33 44 55 66 77 88

(5) HDMI EDID : CH1:OK CH2:OK CH3:OK

(6) DB SW Ver : 0.00

(7) DB MW Ver : 0.00/0.00

(8) A/D Adjust :

	COMP	PC	NTSC	PAL
MAIN	OK	NG	OK	NG
SUB	–	–	OK	NG

(9) 100Hz/120Hz : 0000

(10) DRMA0000-REV00

(11) 12345678 MODELNAME DDD-00(EEE)

(1) MAIN MPU :

Version information of microprocessor

Series name and Code Program Version (4 figure number) and OSD Version (2 figure number).

(2) EEPROM :

Version information of EEPROM : Display 1 byte data.

(3) SUB MPU :

Version information of SUB MPU : Display 1 byte data.

(4) OPTION :

Option information : Display six numbers of 1 byte data.

Left side means QF01 (for AV-LINK) micon software version.

Right side means QA01 (for CEC) micon software version. (Only CEC model)

(5) HDMI EDID :

EDID data check item. Display channel numbers depend on HDMI input numbers.

OK : EDID data is enable.

NG : EDID data is disable.

(6) SW Ver

Version information of DB software as Toshiba release. (Only digital model.)

(7) MW Ver

Left side is Application and UI version information of DB software. (Only digital model.)

Right side is Firmware (Driver) version information of DB software. (Only digital model.)

(8) A/D Adjust

A/D adjustment item.

--MAIN : It's enable only in double Window model. A/D adjustment status of main picture.
--SUB : It's enable only in double Window model. A/D adjustment status of sub picture.
--COMP : Component input
--PC : PC input
--NTSC : NTSC (60 Hz) SD signal (composite input).
--PAL : PAL (50 Hz) SD signal (composite input).
--OK : A/D adjustment set correctly.
--NG : A/D adjustment set incorrect.
-- : A/D adjustment is not needed. Because its picture format isn't used.

(9) 100 Hz FPGA Software Version

It's enable only in 100 Hz drive panel model.

(10) Memory Data Version

Version information of EEPROM.

DRMA**** means model number of EEPROM.

REV** means version of EEPROM.

(11) CD number

CD information (ascii code). : Display 4 byte data.

(12) Model Name

Model name information (ascii code). : Display 7 byte data.

(13) LCD Panel Vendor information display

The following Panel Vender (DDD) and screen size (00) and resolution (EEE) are displayed.

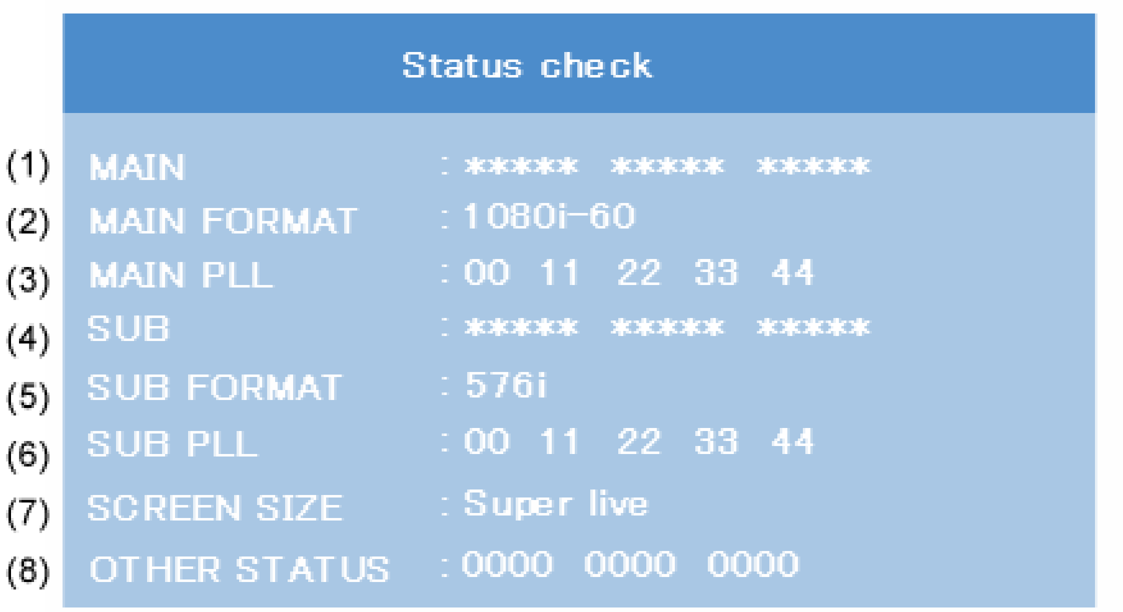
Example : AUO-32 (1080p) indicates that Vender is AUO and Screen Size is 32 inch and Full HD panel.

Status Check Mode

1. Press "9" button thrice on remote control during display of adjustment menu in the service mode. The status of this model will be checked.

2. During Status Check, the following displays are shown.

* Status check display and Item are subject to the models.



Status check

(1) MAIN : ****** ****** ******
(2) MAIN FORMAT : 1080i-60
(3) MAIN PLL : 00 11 22 33 44
(4) SUB : ****** ****** ******
(5) SUB FORMAT : 576i
(6) SUB PLL : 00 11 22 33 44
(7) SCREEN SIZE : Super live
(8) OTHER STATUS : 0000 0000 0000

(1) MAIN :
Main source information :
Display RF position number (0 - 99) on the main screen, or Input Source (EXT1/2/3/HDMI etc.)

(2) MAIN FORMAT :
Display Video and PC format information

(3) MAIN PLL :
Main PLL information : Display 1 byte data at five.

(4) SUB :
Sub source information :
Display RF position number (0 - 99) on the Sub screen, or Input Source (EXT1/2/3/HDMI etc.)
This item displays only Double window model.

(5) SUB FORMAT :

Display Video and PC format information

This item displays only Double window model.

(6) SUB PLL :

Sub PLL information : Display 1 byte data at five.

This item displays only Double window model.

(7) SCREEN SIZE :

Display the screen size as follows.

Exact Scan
Wide
Super Live 2
Cinema 2
4:3
Super Live 1
Cinema 1
Subtitle
14:9

Note:

Exact Scan is shown only for 1080p panel model with video input mode except pc.

(8) OTHER STATUS :

Other status information : Display three numbers of 2 byte data.

Setting Hotel Mode

Enter to service mode and select Hotel Mode menu by pressing P▲ or P▼.
After selecting Hotel Mode, press ◿ + to enter details setting in Hotel Mode.

To select menu, press P▲ or P▼ and press OK to enter the adjustment menu of table bellow.
To move the cursor in the adjustment, press ◿ + or ◿ -.

1. By pressing P▲ or P▼, OPT2 setting will change the value either 1 or 0 on selected items as

follows;

OP2

OPT2	FUNCTION DESCRIPTION	1	0 (Normal)
D7 (bit7)	- (no use)	-	-
D6 (bit6)	- (no use)	-	-
D5 (bit5)	FRONT Key	Disable	Enable
D4 (bit4)	All keys except input selector key (Video/TV)	Disable	Enable
D3 (bit3)	User remote control operation	Disable (Service mode and Supere User mode may possible to setting only)	Enable
D2 (bit2)	Disable SET UP MENU except language. (tuning SETUP MENU)	Disable (Display the language only)	Enable
D1 (bit1)	Disable SETUP MENU	Disable	Enable
D0 (bit0)	HOTEL Mode	On (Enable the setting of D1 from to D7)	Off (Normal)

VOLUX

Set VOLUX as following.

1) Set speaker volume to 100. (Any value is OK, but 100 is better to check sound level.)

2) Down value of VOLUX until finding suitable sound volume level.

VOLUX	FUNCTION DESCRIPTION	1	0 (Normal)
D7 (bit7) ~ D0 (bit0)	Max Limiter of Volume Control	0x00 ~ 0x7F (Normal: 0x7C)	

OP6

OPT6	FUNCTION DESCRIPTION	1	0 (Normal)
D7 (bit7)	Enable mode that POS or VIDEO program number will be appeared in forced when turn ON the main power	Enable	Disable
D6 (bit6) ~ D0 (bit0)	POS or VIDEO program number	0 ~ 127 [decimal] (Normal: 0) POS number: 0 ~ 99 VIDEO number: 100 ~ 127 (DTV = 100, VIDEO = 101, VIDEO = 102..) 100 is used as ATV when DTV option is disable.	

OP7

OPT7	FUNCTION DESCRIPTION	1	0 (Normal)
D7 (bit7)	Enable mode that speaker volume will be set in forced when turn ON the main power	Enable	Disable
D6 (bit6) ~ D0 (bit0)	Forced speaker volume	0 ~ 100 [decimal] (Normal: 0) Value above 100 is no effect.	

OP8

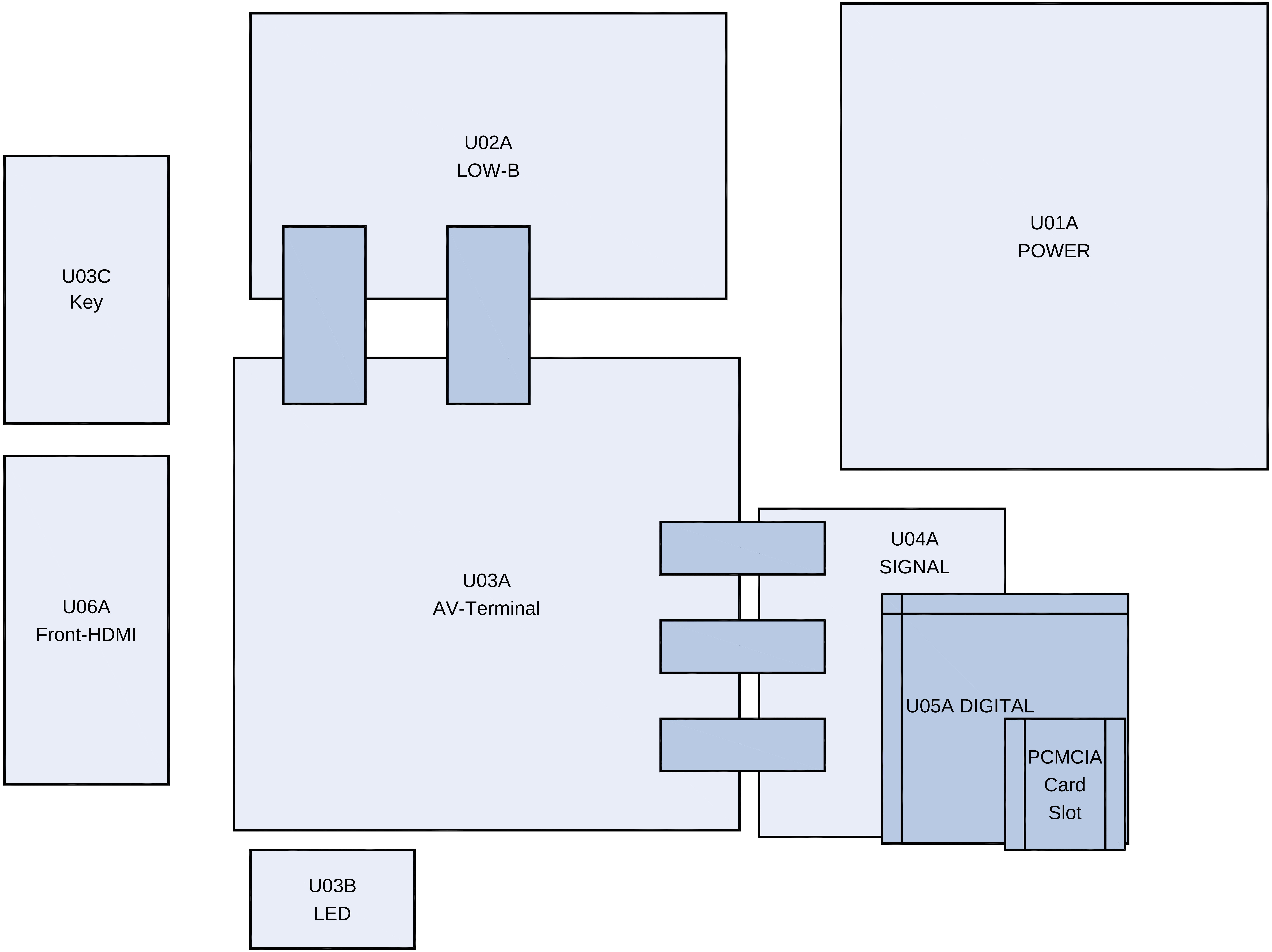
OPT8	FUNCTION DESCRIPTION	1	0 (Normal)
D7 (bit7) ~ D3 (bit3)	- (no use)	-	-
D2 (bit2)	Enable mode that picture mode will be set in forced when turn ON the main power	Enable (Display the language only)	Disable
D1 (bit1) ~ D0 (bit0)	Forced picture mode	0 [dec]: Dynamic (mode-1) (Normal) 1 [dec]: Standard (mode-2) 2 [dec]: Mild (mode-3) 3 [dec]: Memory (mode-M)	

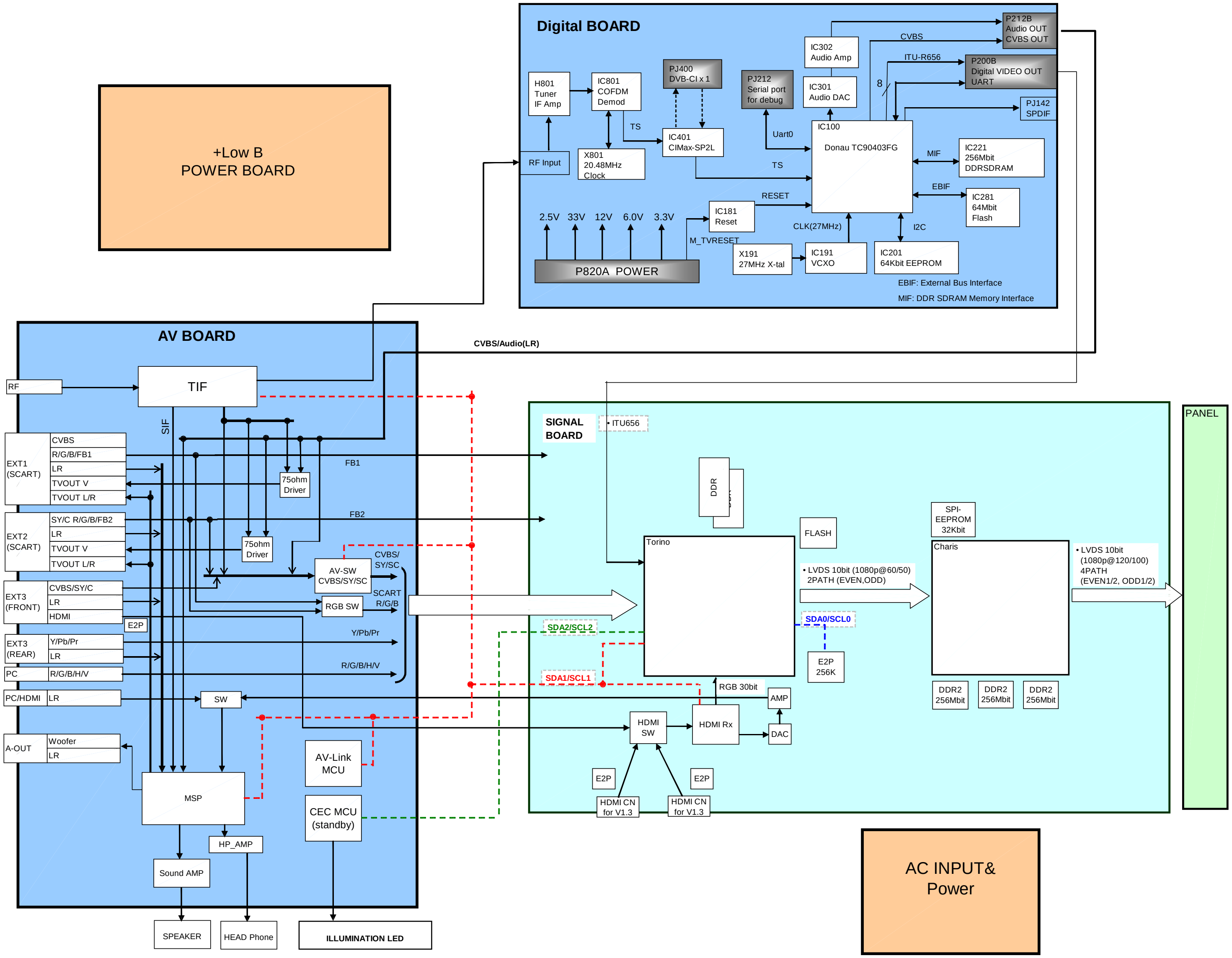
LED Indication

The Green and Red LEDs on the TV (at the bottom center of the TV) indicate the TV's status, as described below.

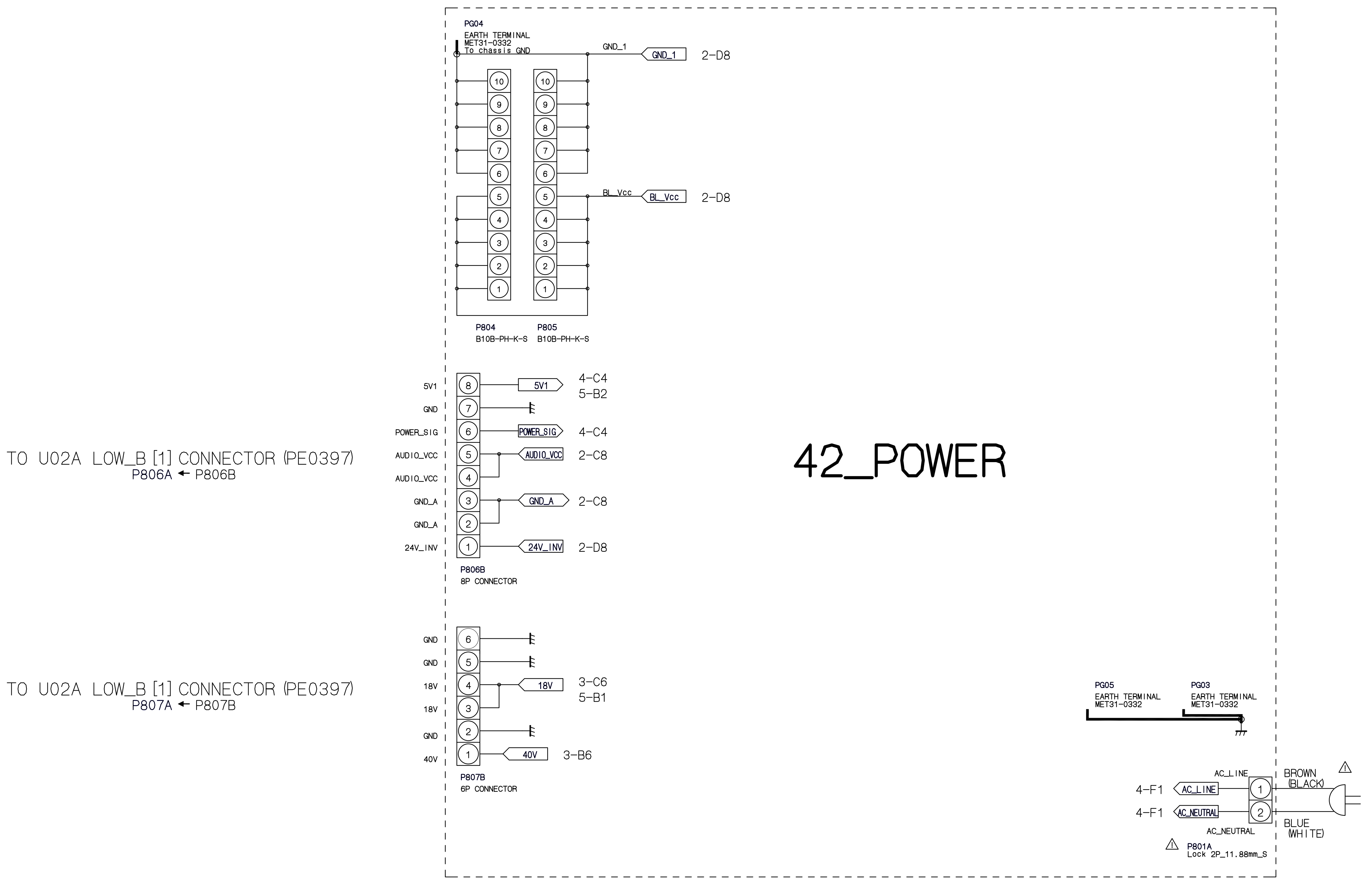
- Red ON (solid) and Green OFF = The TV power cord is plugged in.
- Green ON (solid) and Red ON = The On timer is operating.

	LED Indication	Condition	Solution
1	Green is OFF; Red blinks continuously at 0.5-second intervals.	Abnormal operation	Turn OFF the TV and unplug the power cord. Plug the power cord in again and turn ON the TV.
2	Green is OFF; Red blinks continuously at 1-second intervals.	Abnormal operation of BUS line.	Turn OFF the TV and unplug the power cord. Plug the power cord in again and turn ON the TV.

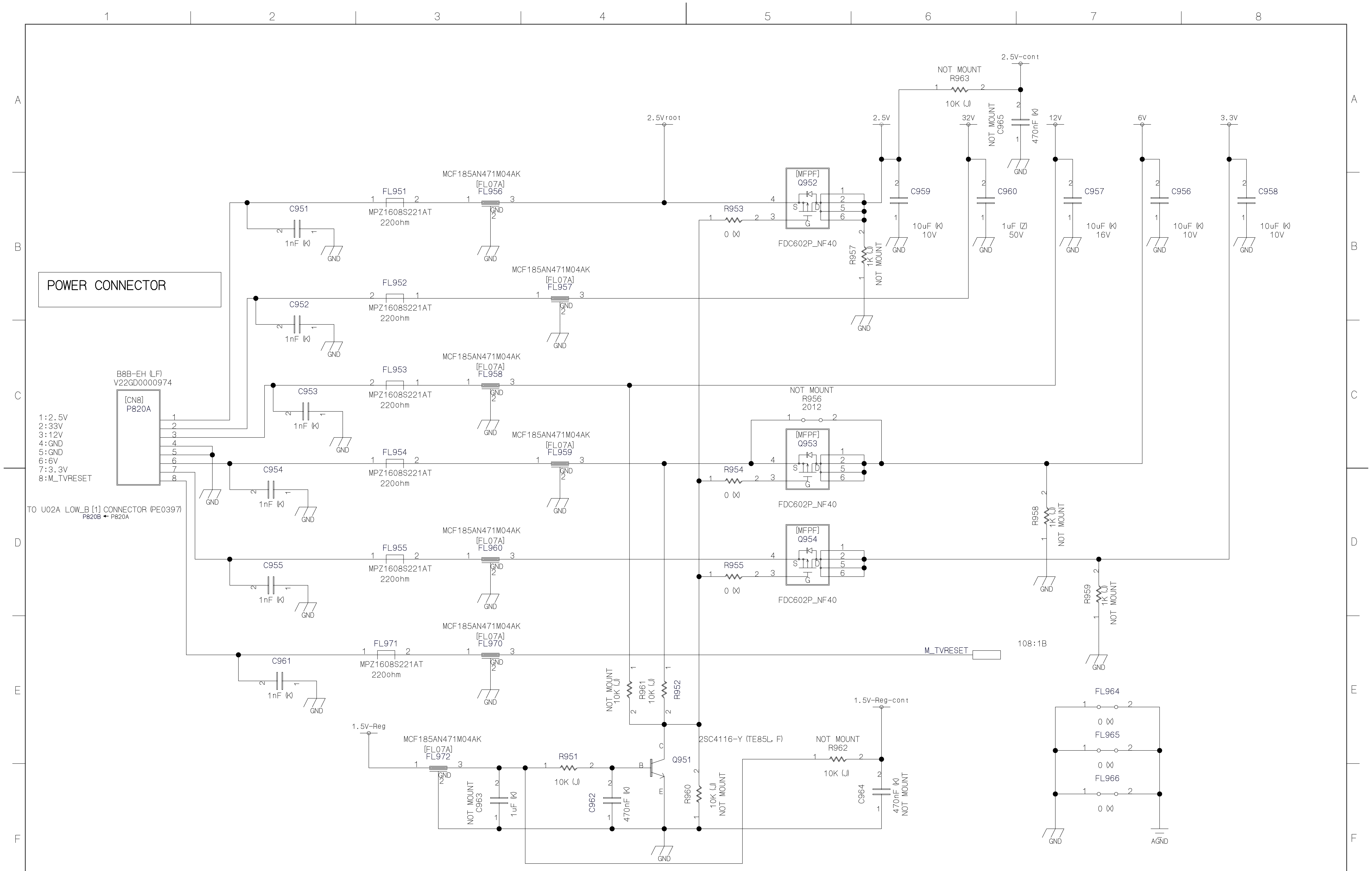

U02A
LOW-B
U01A
POWER
U03C
Key
U06A
Front-HDMI
U03A
AV-Terminal
U04A
SIGNAL
U05A DIGITAL
PCMCIA
Card
Slot
U03B
LED



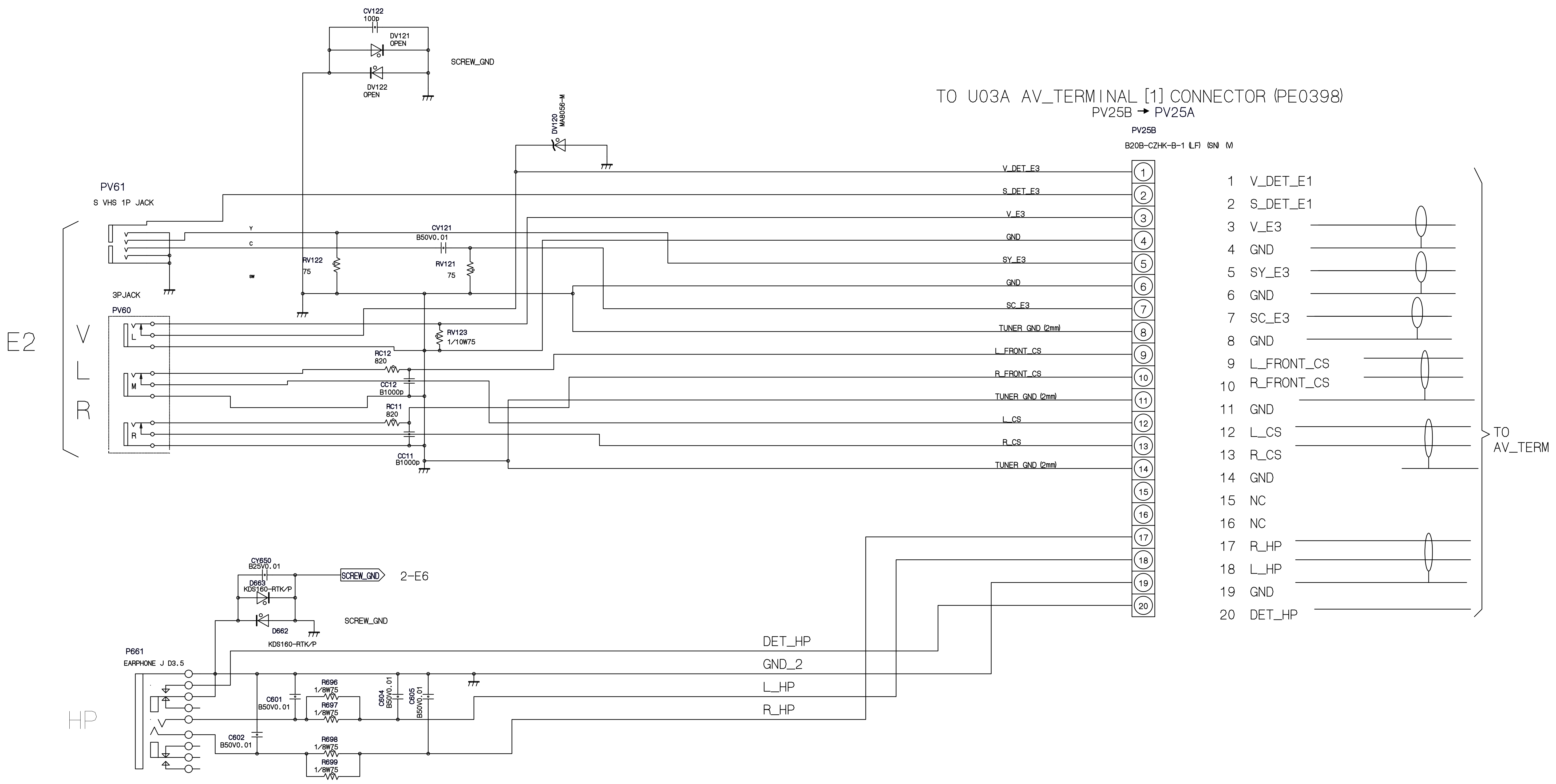
+Low B
POWER BOARD
Digital BOARD
H801
Tuner
IF Amp
IC801
COFDM
Demod
PJ400
DVB-CI x 1
PJ212
Serial port
for debug
IC302
Audio Amp
IC301
Audio DAC
P212B
Audio OUT
CVBS OUT
CVBS
ITU-R656
P200B
Digital VIDEO OUT
UART
8
PJ142
SPDIF
RF Input
X801
20.48MHz
Clock
TS
IC401
CIMax-SP2L
Uart0
IC100
Donau TC90403FG
TS
MIF
IC221
256Mbit
DDRSDRAM
EBIF
IC281
64Mbit
Flash
RESET
IC181
Reset
2.5V
33V
12V
6.0V
3.3V
M_TVRESET
CLK(27MHz)
I2C
P820A POWER
X191
27MHz X-tal
IC191
VCXO
IC201
64Kbit EEPROM
EBIF: External Bus Interface
MIF: DDR SDRAM Memory Interface
CVBS/Audio(LR)
AV BOARD
RF
TIF
SIF
EXT1
(SCART)
CVBS
R/G/B/FB1
LR
TVOUT V
TVOUT L/R
75ohm
Driver
FB1
EXT2
(SCART)
SY/C R/G/B/FB2
LR
TVOUT V
TVOUT L/R
FB2
75ohm
Driver
CVBS/
SY/SC
AV-SW
CVBS/SY/SC
EXT3
(FRONT)
CVBS/SY/C
LR
HDMI
SCART
R/G/B
RGB SW
E2P
EXT3
(REAR)
Y/Pb/Pr
LR
Y/Pb/Pr
PC
R/G/B/H/V
R/G/B/H/V
PC/HDMI
LR
SW
A-OUT
Woofer
LR
AV-Link
MCU
MSP
CEC MCU
(standby)
HP_AMP
Sound AMP
SPEAKER
HEAD Phone
ILLUMINATION LED
SIGNAL
BOARD
• ITU656
DDR
DDR
FLASH
SPI-
EEPROM
32Kbit
Torino
Charis
• LVDS 10bit (1080p@60/50)
2PATH (EVEN,ODD)
• LVDS 10bit
(1080p@120/100)
4PATH
(EVEN1/2, ODD1/2)
SDA2/SCL2
SDA0/SCL0
E2P
256K
SDA1/SCL1
RGB 30bit
AMP
HDMI
SW
HDMI Rx
DAC
DDR2
256Mbit
DDR2
256Mbit
DDR2
256Mbit
E2P
E2P
HDMI CN
for V1.3
HDMI CN
for V1.3
PANEL
AC INPUT&
Power


PG04
EARTH TERMINAL
MET31-0332
To chassis GND
GND_1
GND_1
2-D8
10
9
8
7
6
5
4
3
2
1
BL_Vcc
BL_Vcc
2-D8
P804
B10B-PH-K-S
P805
B10B-PH-K-S
TO U02A LOW_B [1] CONNECTOR (PE0397)
P806A ← P806B
5V1
GND
POWER_SIG
AUDIO_VCC
AUDIO_VCC
GND_A
GND_A
24V_INV
5V1
4-C4
5-B2
POWER_SIG
4-C4
AUDIO_VCC
2-C8
GND_A
2-C8
24V_INV
2-D8
P806B
8P CONNECTOR
42_POWER
TO U02A LOW_B [1] CONNECTOR (PE0397)
P807A ← P807B
GND
GND
18V
18V
GND
40V
18V
3-C6
5-B1
40V
3-B6
P807B
6P CONNECTOR
PG05
EARTH TERMINAL
MET31-0332
PG03
EARTH TERMINAL
MET31-0332
AC_LINE
4-F1
AC_LINE
4-F1
AC_NEUTRAL
AC_NEUTRAL
BROWN
(BLACK)
BLUE
(WHITE)
P801A
Lock 2P_11.88mm_S

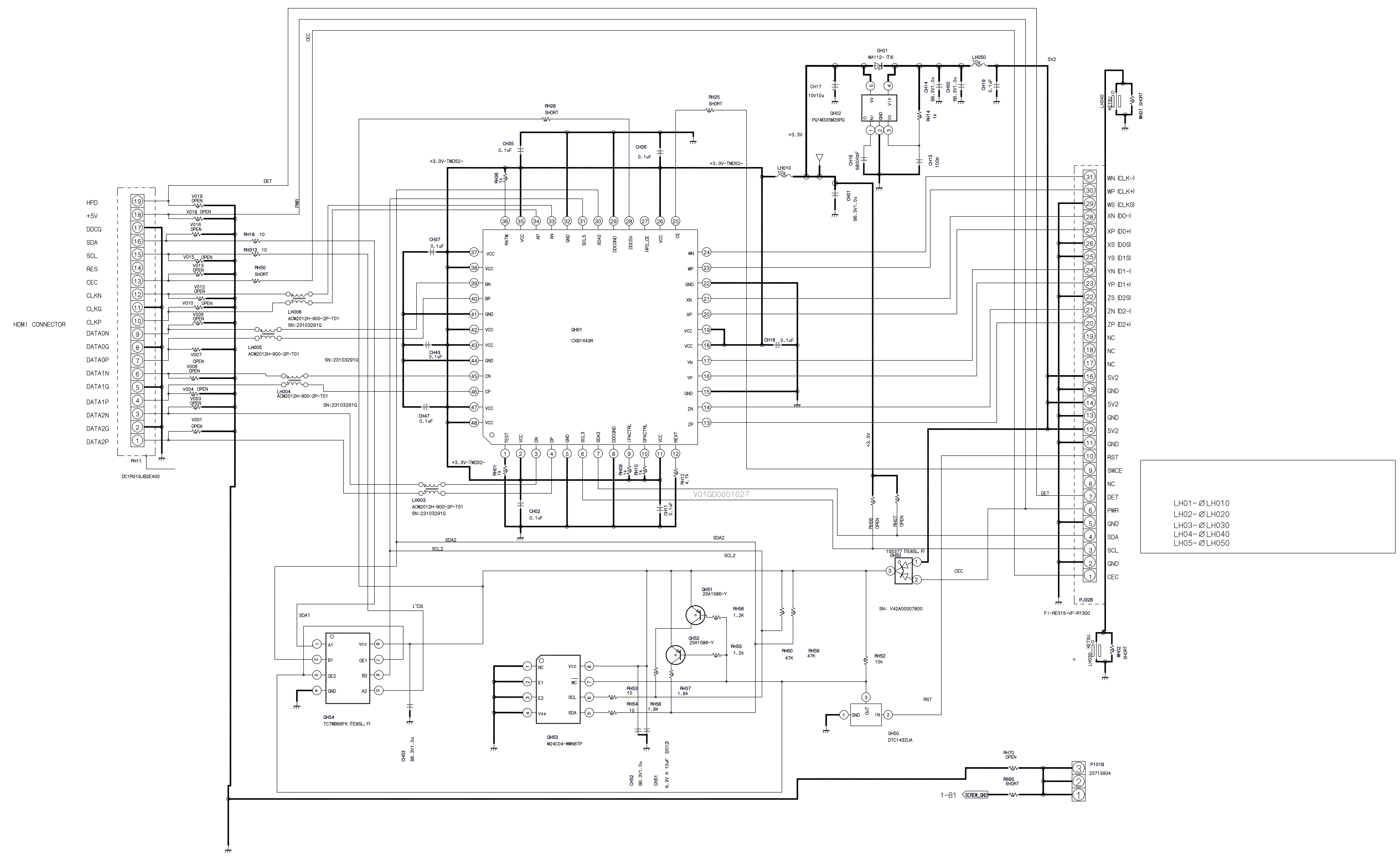
U01A POWER [1] CONNECTOR (PE0282)


1
2
3
4
5
6
7
8
A
B
C
D
E
F
POWER CONNECTOR
B8B-EH (LF)
V22GD0000974
[CN8]
P820A
1:2.5V
2:33V
3:12V
4:GND
5:GND
6:6V
7:3.3V
8:M_TVRESET
TO U02A LOW_B [1] CONNECTOR (PE0397)
P820B ← P820A
FL951
FL952
FL953
FL954
FL955
FL971
MPZ1608S221AT
220ohm
MCF185AN471M04AK
[FL07A]
FL956
FL957
FL958
FL959
FL960
FL970
FL972
C951
C952
C953
C954
C955
C961
1nF (K)
GND
2.5Vroot
R953
R954
R955
0 (X)
[MFPF]
Q952
Q953
Q954
FDC602P_NF40
S
G
D
R957
1K (J)
NOT MOUNT
NOT MOUNT
R963
10K (J)
2.5V-cont
C965
470nF (K)
2.5V
32V
12V
6V
3.3V
C959
C960
C957
C956
C958
10uF (K)
10V
1uF (Z)
50V
10uF (K)
16V
NOT MOUNT
R956
2012
R958
1K (J)
R959
1K (J)
M_TVRESET
108:1B
R961
10K (J)
R952
1.5V-Reg
C963
1uF (K)
R951
10K (J)
C962
470nF (K)
Q951
2SC4116-Y (TE85L, F)
B
C
E
R960
10K (J)
1.5V-Reg-cont
R962
10K (J)
C964
470nF (K)
FL964
FL965
FL966
0 (X)
AGND

U05A DIGITAL [901] POWER CONNECTOR (PE0284)

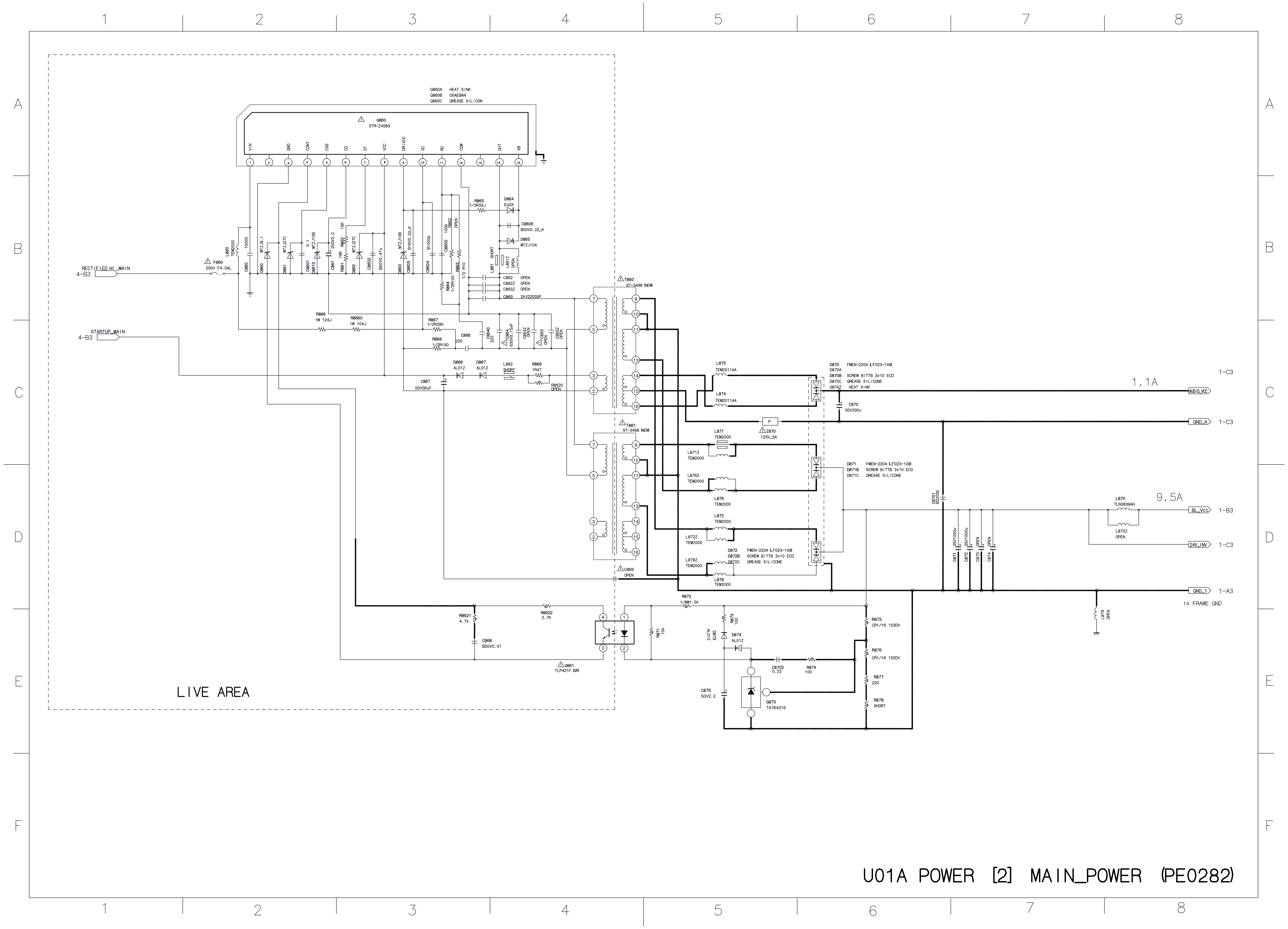

CV122
100p
DV121
OPEN
DV122
OPEN
SCREW_GND
DV120
MA8056-M
TO U03A AV_TERMINAL [1] CONNECTOR (PE0398)
PV25B → PV25A
PV25B
B20B-CZHK-B-1 (LF) (SN) (M)
PV61
S VHS 1P JACK
Y
C
SW
CV121
B50V0.01
RV122
75
RV121
75
3PJACK
PV60
E2
V
L
R
L
M
R
RV123
1/10W75
RC12
820
CC12
B1000p
RC11
820
CC11
B1000p
V_DET_E3
S_DET_E3
V_E3
GND
SY_E3
GND
SC_E3
TUNER GND (2mm)
L_FRONT_CS
R_FRONT_CS
TUNER GND (2mm)
L_CS
R_CS
TUNER GND (2mm)
1 V_DET_E1
2 S_DET_E1
3 V_E3
4 GND
5 SY_E3
6 GND
7 SC_E3
8 GND
9 L_FRONT_CS
10 R_FRONT_CS
11 GND
12 L_CS
13 R_CS
14 GND
15 NC
16 NC
17 R_HP
18 L_HP
19 GND
20 DET_HP
TO
AV_TERM
CY650
B25V0.01
SCREW_GND
2-E6
D663
KDS160-RTK/P
D662
KDS160-RTK/P
SCREW_GND
P661
EARPHONE J D3.5
HP
C601
B50V0.01
C602
B50V0.01
R696
1/8W75
R697
1/8W75
R698
1/8W75
R699
1/8W75
C604
B50V0.01
C605
B50V0.01
DET_HP
GND_2
L_HP
R_HP

U06A FRONT_HDMI [1] FRONT-AV (PE0400)

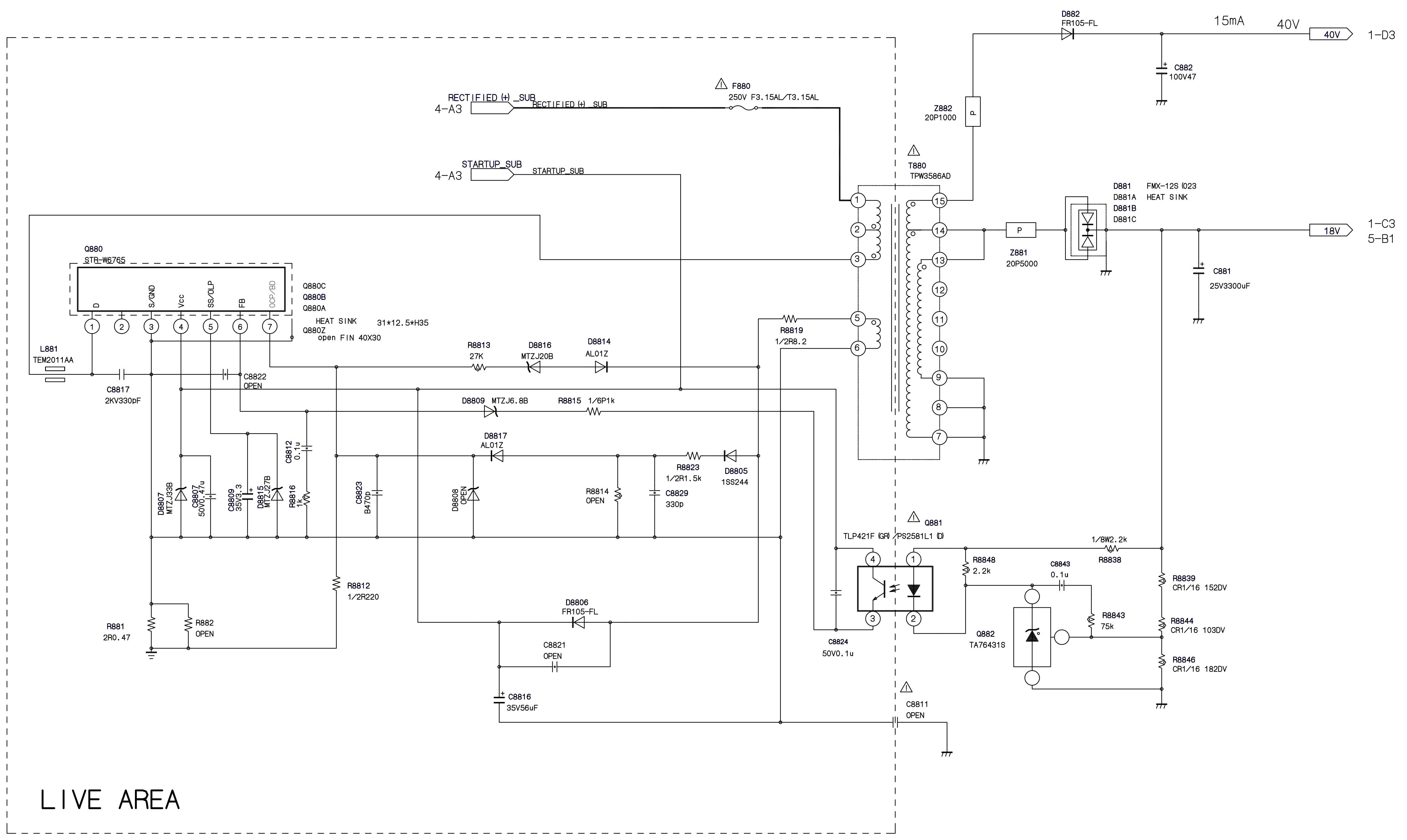

HDMI CONNECTOR
HPD
+5V
DDCG
SDA
SCL
RES
CEC
CLKN
CLKG
CLKP
DATA0N
DATA0G
DATA0P
DATA1N
DATA1G
DATA1P
DATA2N
DATA2G
DATA2P
PH11
DC1R019JB2E400
DET
PWR
CEC
V019 OPEN
V018 OPEN
V016 OPEN
V015 OPEN
V013 OPEN
V012 OPEN
V010 OPEN
V009 OPEN
V007 OPEN
V006 OPEN
V004 OPEN
V003 OPEN
V001 OPEN
RH18 10
RH312 10
RH50 SHORT
LH006
ACM2012H-900-2P-T01
SN:23103291Q
LH005
ACM2012H-900-2P-T01
SN:23103291Q
LH004
ACM2012H-900-2P-T01
SN:23103291Q
LH003
ACM2012H-900-2P-T01
SN:23103291Q
QH01
CXB1443R
+3.3V-TMDS2-
RH28 SHORT
RH25 SHORT
CH35 0.1uF
CH26 0.1uF
CH37 0.1uF
CH43 0.1uF
CH47 0.1uF
CH18 0.1uF
CH02 0.1uF
CH11 0.1uF
RH06
RH01 1k
RH09
RH10
RH12 4.7k
V01GD0001027
CH01 MA112-(TX)
LH050 10k
5V2
CH17 10V10u
QH02 PQ1M335M2SPQ
CH14 B6.3V1.0u
CH30 B6.3V1.0u
CH19 0.1uF
RH14 1k
CH16 6800PF
CH15 100k
LH010 10k
+3.3V
CH01 B6.3V1.0u
LH040
KETSU
WH01 SHORT
WN (CLK-)
WP (CLK+)
WS (CLKS)
XN (D0-)
XP (D0+)
XS (D0S)
YS (D1S)
YN (D1-)
YP (D1+)
ZS (D2S)
ZN (D2-)
ZP (D2+)
NC
NC
NC
5V2
GND
5V2
GND
5V2
GND
RST
SWCE
NC
DET
PWR
GND
SDA
SCL
GND
CEC
PJ32B
FI-RE31S-VF-R1300
LH020
KETSU
WH02 SHORT
SDA2
SCL2
SDA1
SCL1
QH54
TC7W666FK (TE85L,F)
CH53 B6.3V1.0u
QH53
M24C04-WMN6TP
QH51 2SA1586-Y
QH52 2SA1586-Y
RH56 1.2K
RH55 1.2k
RH53 10
RH54 10
RH58 1.8k
RH57 1.8k
CH52 B6.3V1.0u
CH51 6.3V K 10uF (2012)
RH60 47K
RH59 47K
RH62 10k
1SS377 (TE85L,F)
DH50
SN: V42A00007800
RH36 OPEN
RH37 OPEN
QH50
DTC143ZUA
RST
RH70 OPEN
R695 SHORT
P101B
23713934
1-B1
SCREW_GND

LH01-ØLH010
LH02-ØLH020
LH03-ØLH030
LH04-ØLH040
LH05-ØLH050

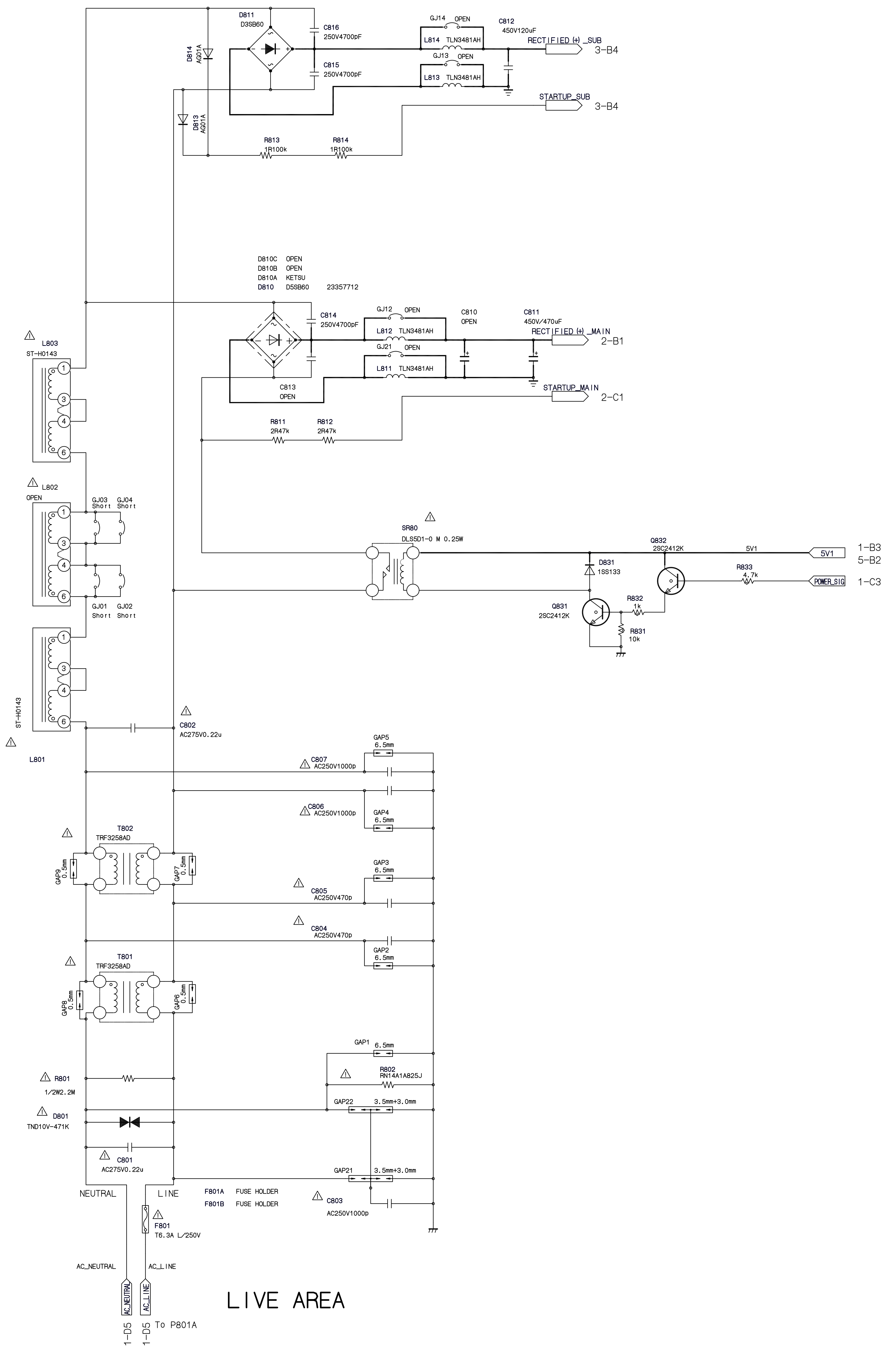
U06A FRONT_HDMI [2] HDMI #0 (TMDS2_SW) (PE0400)


1
2
3
4
5
6
7
8
A
B
C
D
E
F
LIVE AREA
Q860A HEAT SINK
Q860B OSAEBAN
Q860C GREASE SILICON
Q860
STR-Z4569
VIN
GND
CONT
CSS
CD
DT
VCC
DRIVCC
OC
RC
COM
OUT
VB
RECTIFIED (+) _MAIN
4-B3
STARTUP_MAIN
4-B3
F860
250V F4.0AL
L860
TEM2000
C860
10000
D860
MTZJ9.1
D861
MTZJ27C
C8601
0.1
D8610
MTZJ10B
C861
200V2.2
R860
15K
R861
18K
D862
MTZJ27C
C8602
B50V0.47u
D863
MTZJ10B
C8603
B16V0.22_K
C8604
B1000p
C8605
100p
R862
OPEN
R863
1/2 R10
R864
1/2R100
R865
1/2R33J
D864
EU2A
C8606
B50V0.22_K
D865
MTZJ10A
L861 SHORT
L861Z
OPEN
C862 OPEN
C862Z OPEN
C863Z OPEN
C863 2kV2200pF
R866
1W 124J
R8660
1W 104J
R867
1/2R390
R868
1/2R150
C866
220
C8640
220
C864
630V0.15uF
C864Z
OPEN
C865
OPEN
C865Z
OPEN
D866
AL01Z
D867
AL01Z
L862
SHORT
R869
1R47
R8620
OPEN
C867
50V56uF
T860
ST-3496 (NEW)
T861
ST-3496 (NEW)
L875
TEM2011AA
L874
TEM2011AA
D870 FMEN-220A (LF023-108)
D870A
D870B SCREW BITTB 3x10 ECO
D870C GREASE SILICONE
Q87AZ HEAT SINK
C870
50V330u
1.1A
AUDIO_VCC
1-C3
P
Z870
125V_5A
GND_A
1-C3
L871
TEM2000
L871Z
TEM2000
L876Z
TEM2000
L876
TEM2000
D871 FMEN-220A (LF023-108)
D871B SCREW BITTB 3x10 ECO
D871C GREASE SILICONE
C8701
B2200p
L870
TLN3639AH
9.5A
BL_Vcc
1-B3
L870Z
OPEN
24V_INV
1-C3
L872
TEM2000
L872Z
TEM2000
L878Z
TEM2000
D872 FMEN-220A (LF023-108)
D872B SCREW BITTB 3x10 ECO
D872C GREASE SILICONE
L878
TEM2000
C869
OPEN
C871
35V1000u
C872
35V1000u
C873
OPEN
C874
OPEN
GND_1
1-A3
to FRAME GND
L879
OPEN
R8621
4.7k
C868
B50V0.01
R8622
2.7K
Q861
TLP421F (GR)
R872
1/8W1.5k
R871
10k
R873
100
D873
AL01Z
D874
AL01Z
C8702
0.22
R874
100
C875
50V2.2
Q870
TA76431S
R875
CR1/16 153DV
R876
CR1/16 152DV
R877
220
R878
SHORT

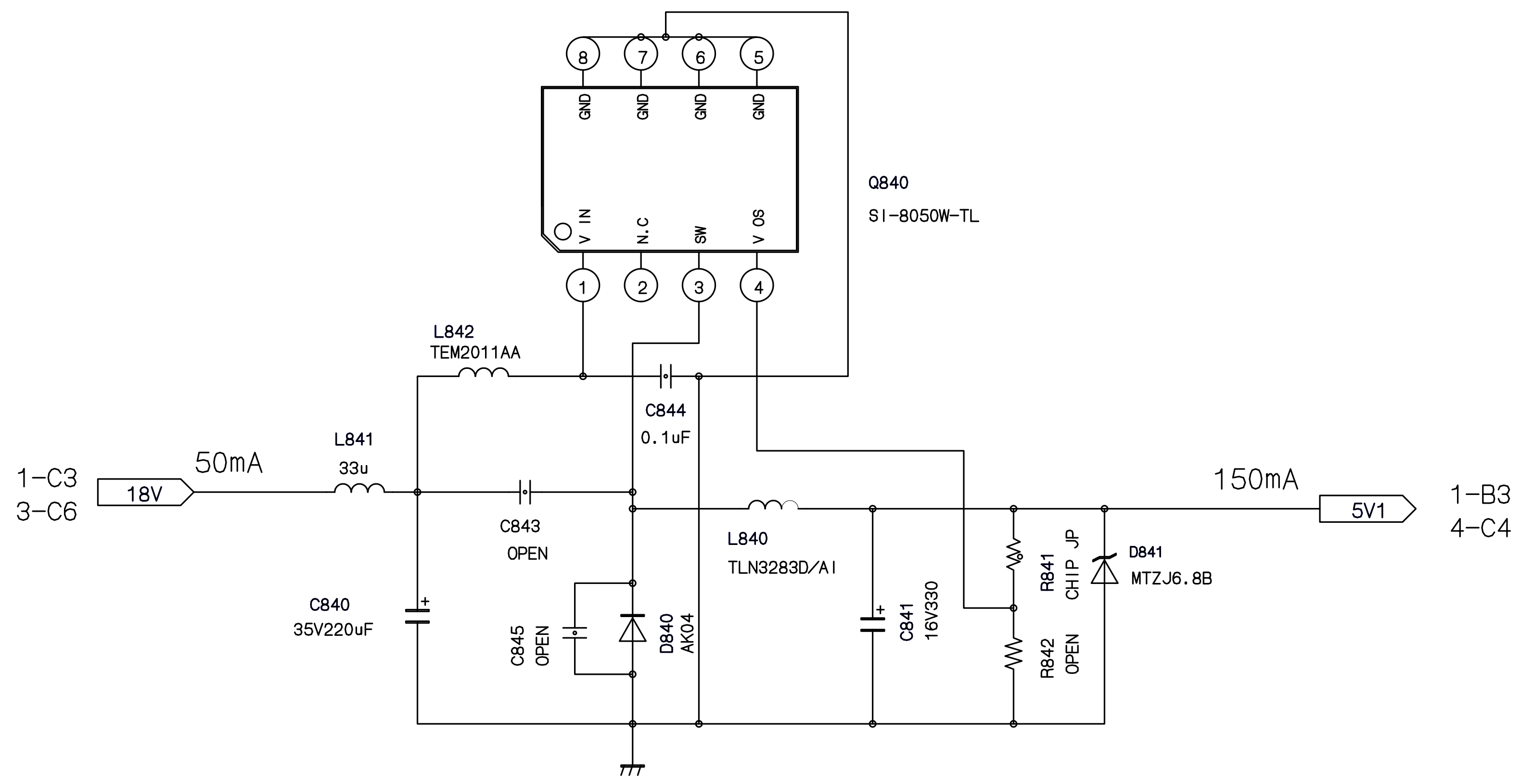
U01A POWER [2] MAIN_POWER (PE0282)


1
2
3
4
5
6
7
8
A
B
C
D
E
F
LIVE AREA
RECTIFIED (+) _SUB
4-A3
RECTIFIED (+) _SUB
F880
250V F3.15AL/T3.15AL
STARTUP_SUB
4-A3
STARTUP_SUB
T880
TPW3586AD
Z882
20P1000
P
D882
FR105-FL
15mA
40V
40V
1-D3
C882
100V47
D881 FMX-12S (023
D881A HEAT SINK
D881B
D881C
Z881
20P5000
P
18V
1-C3
5-B1
C881
25V3300uF
Q880
STR-W6765
D
S/GND
Vcc
SS/OLP
FB
OCP/BD
Q880C
Q880B
Q880A
HEAT SINK 31*12.5*H35
Q880Z
open FIN 40X30
L881
TEM2011AA
C8817
2KV330pF
C8822
OPEN
R8813
27K
D8816
MTZJ20B
D8814
AL01Z
R8819
1/2R8.2
D8809 MTZJ6.8B
R8815 1/6P1k
D8817
AL01Z
R8823
1/2R1.5k
D8805
1SS244
D8807
MTZJ33B
C8807
50V0.47u
C8809
35V3.3
D8815
MTZJ27B
R8816
1k
C8812
0.1u
C8823
B470p
D8808
OPEN
R8814
OPEN
C8829
330p
R8812
1/2R220
R881
2R0.47
R882
OPEN
D8806
FR105-FL
C8821
OPEN
C8816
35V56uF
Q881
TLP421F (GR) /PS2581L1 (D)
C8824
50V0.1u
R8848
2.2k
C8843
0.1u
1/8W2.2k
R8838
R8839
CR1/16 152DV
R8843
75k
Q882
TA76431S
R8844
CR1/16 103DV
R8846
CR1/16 182DV
C8811
OPEN

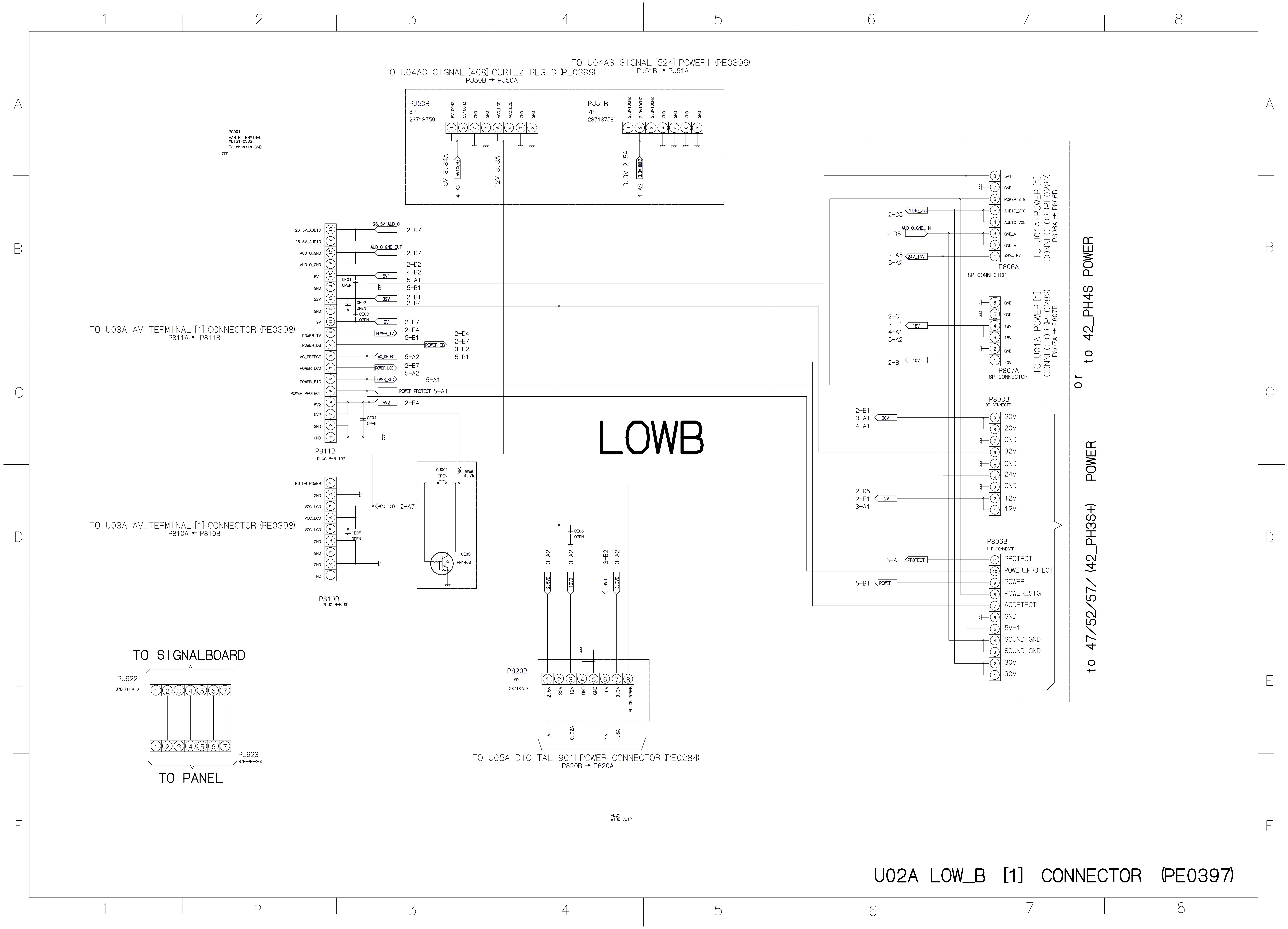
U01A POWER [3] SUB_POWER (PE0282)


D811
D3SB60
C816
250V4700pF
C815
250V4700pF
D814
AG01A
D813
AG01A
GJ14 OPEN
L814 TLN3481AH
GJ13 OPEN
L813 TLN3481AH
C812
450V120uF
RECTIFIED (+) _SUB
3-B4
STARTUP_SUB
3-B4
R813
1R100k
R814
1R100k
D810C OPEN
D810B OPEN
D810A KETSU
D810 D5SB60 23357712
C814
250V4700pF
C813
OPEN
GJ12 OPEN
L812 TLN3481AH
GJ21 OPEN
L811 TLN3481AH
C810
OPEN
C811
450V/470uF
RECTIFIED (+) _MAIN
2-B1
STARTUP_MAIN
2-C1
R811
2R47k
R812
2R47k
L803
ST-H0143
L802
OPEN
GJ03 Short
GJ04 Short
GJ01 Short
GJ02 Short
ST-H0143
L801
SR80
DLS5D1-0 M 0.25W
D831
1SS133
Q831
2SC2412K
Q832
2SC2412K
R832
1k
R831
10k
R833
4.7k
5V1
5V1
1-B3
5-B2
POWER_SIG
1-C3
C802
AC275V0.22u
GAP5
6.5mm
C807
AC250V1000p
C806
AC250V1000p
GAP4
6.5mm
T802
TRF3258AD
GAP9
0.5mm
GAP7
0.5mm
GAP3
6.5mm
C805
AC250V470p
C804
AC250V470p
GAP2
6.5mm
T801
TRF3258AD
GAP8
0.5mm
GAP6
0.5mm
GAP1 6.5mm
R802
RN14A1A825J
R801
1/2W2.2M
D801
TND10V-471K
GAP22 3.5mm+3.0mm
C801
AC275V0.22u
GAP21 3.5mm+3.0mm
NEUTRAL
LINE
F801A FUSE HOLDER
F801B FUSE HOLDER
C803
AC250V1000p
F801
T6.3A L/250V
AC_NEUTRAL
AC_LINE
AC_NEUTRAL
AC_LINE
1-D5
1-D5 To P801A
LIVE AREA

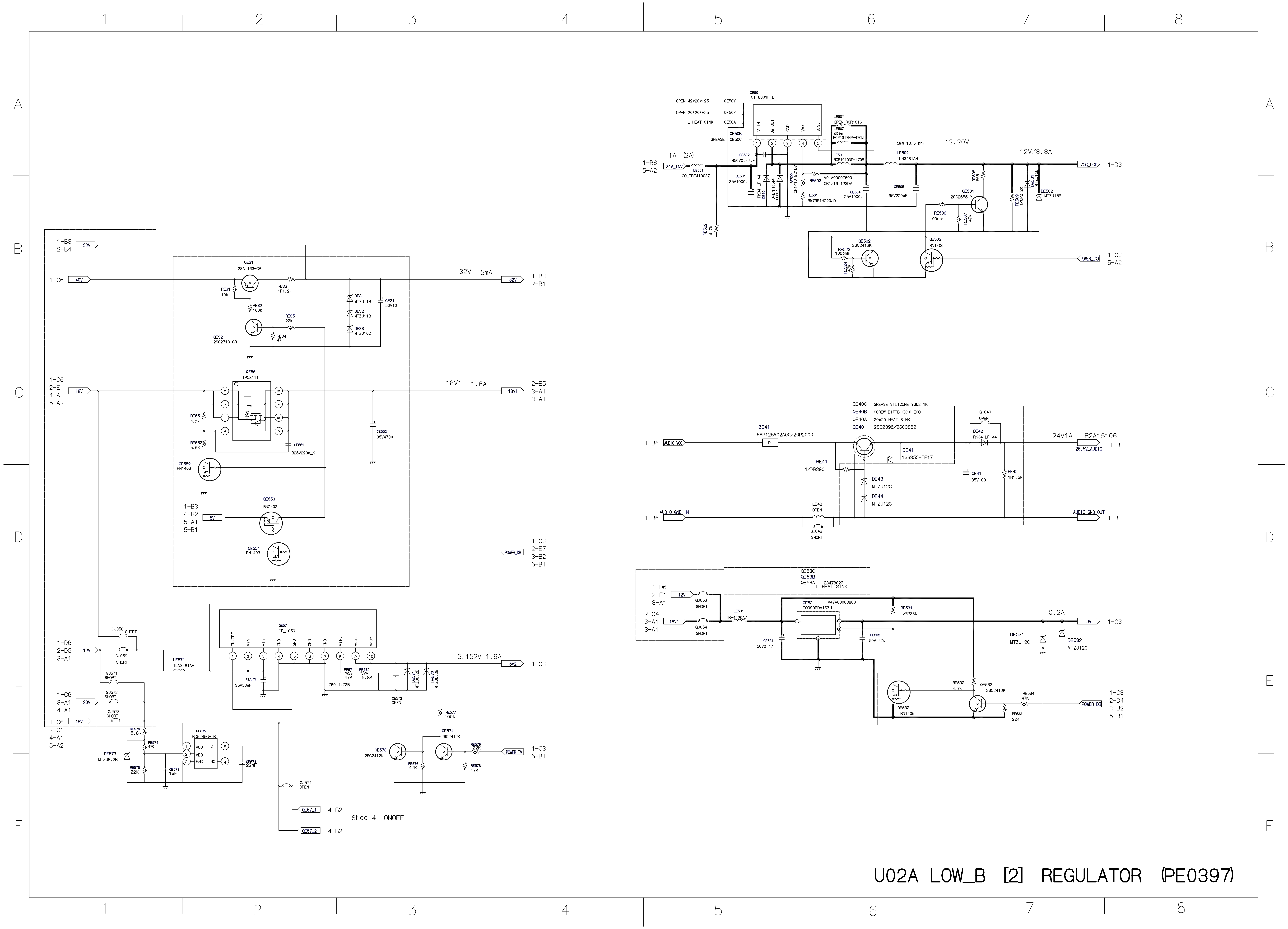
U01A POWER [4] AC-INPUT (PE0282)


GND
GND
GND
GND
8
7
6
5
V IN
N.C
SW
V OS
1
2
3
4
Q840
SI-8050W-TL
L842
TEM2011AA
C844
0.1uF
1-C3
3-C6
18V
50mA
L841
33u
C843
OPEN
C840
35V220uF
C845
OPEN
D840
AK04
L840
TLN3283D/AI
C841
16V330
R841
CHIP JP
R842
OPEN
D841
MTZJ6.8B
150mA
5V1
1-B3
4-C4

U01A POWER [5] STANDBY 5V1 (PE0282)

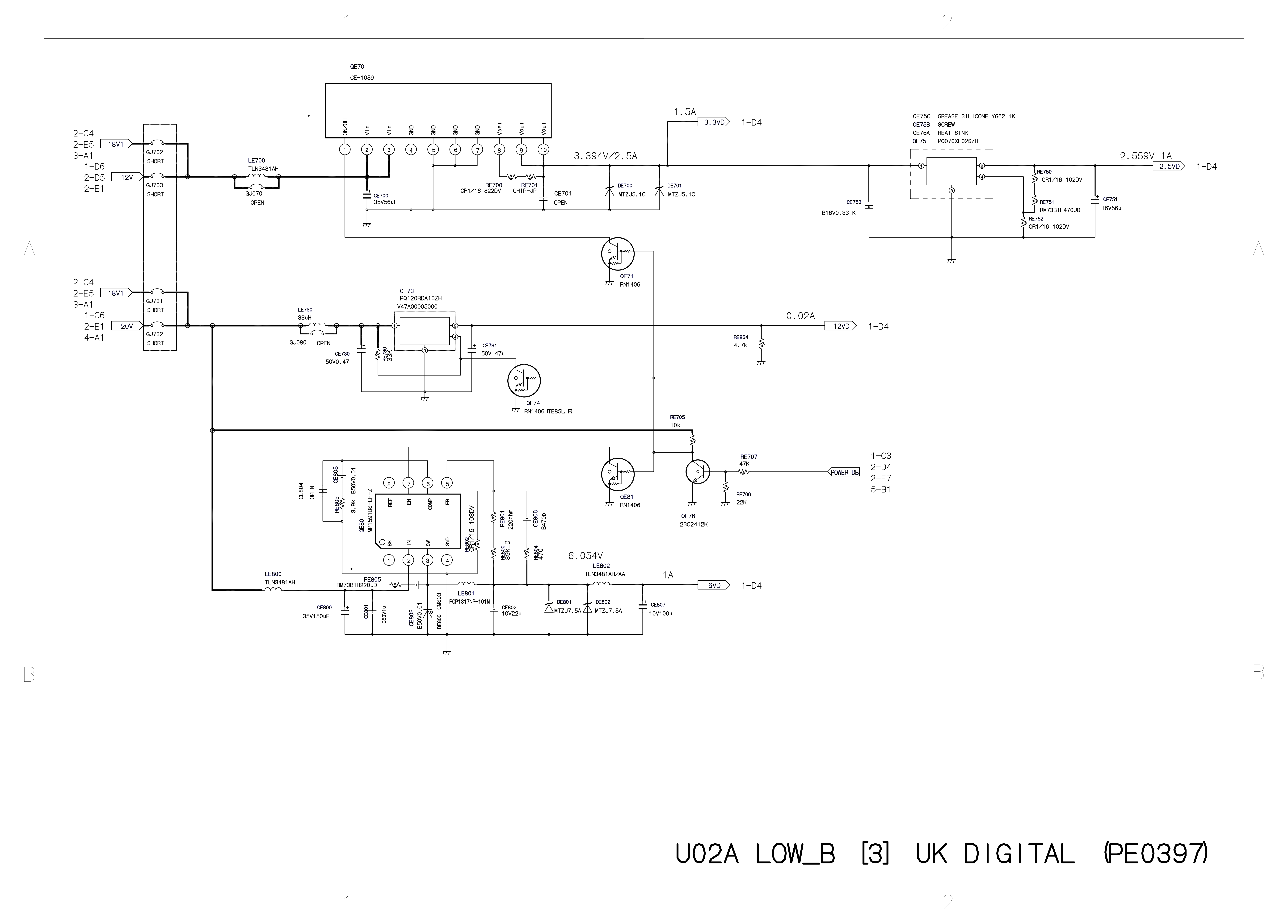

LOWB
TO U04AS SIGNAL [408] CORTEZ REG 3 (PE0399)
PJ50B → PJ50A
TO U04AS SIGNAL [524] POWER1 (PE0399)
PJ51B → PJ51A
PJ50B
8P
23713759
PJ51B
7P
23713758
5V 3.34A
12V 3.3A
3.3V 2.5A
4-A2
PG001
EARTH TERMINAL
To chassis GND
TO U03A AV_TERMINAL [1] CONNECTOR (PE0398)
P811A ← P811B
P811B
PLUG B-B 19P
26.5V_AUDIO
AUDIO_GND
AUDIO_GND_OUT
5V1
GND
32V
9V
POWER_TV
POWER_DB
AC_DETECT
POWER_LCD
POWER_SIG
POWER_PROTECT
5V2
CE01 OPEN
CE02 OPEN
CE03 OPEN
CE04 OPEN
2-C7
2-D7
2-D2
4-B2
5-A1
5-B1
2-B1
2-B4
2-E7
2-E4
2-D4
3-B2
5-A2
2-B7
TO U03A AV_TERMINAL [1] CONNECTOR (PE0398)
P810A ← P810B
P810B
PLUG B-B 9P
EU_DB_POWER
VCC_LCD
NC
CE05 OPEN
2-A7
GJ001 OPEN
RE05 4.7k
QE05 RN1403
CE06 OPEN
3-A2
3-B2
2.5VD
12VD
6VD
3.3VD
P820B
8P
23713759
2.5V
32V
12V
GND
6V
3.3V
EU_DB_POWER
1A
0.02A
1.5A
TO U05A DIGITAL [901] POWER CONNECTOR (PE0284)
P820B → P820A
TO SIGNALBOARD
PJ922
B7B-PH-K-S
PJ923
B7B-PH-K-S
TO PANEL
PL21
WIRE CLIP
TO U01A POWER [1] CONNECTOR (PE0282)
P806A → P806B
P806A
8P CONNECTOR
5V1
GND
POWER_SIG
AUDIO_VCC
GND_A
24V_INV
2-C5
2-D5
AUDIO_GND_IN
2-A5
5-A2
TO U01A POWER [1] CONNECTOR (PE0282)
P807A → P807B
P807A
6P CONNECTOR
18V
40V
2-C1
2-E1
4-A1
2-B1
or to 42_PH4S POWER
P803B
9P CONNECTR
20V
GND
32V
24V
12V
2-E1
3-A1
4-A1
2-D5
P806B
11P CONNECTR
PROTECT
POWER_PROTECT
POWER
POWER_SIG
ACDETECT
GND
5V-1
SOUND GND
30V
5-A1
5-B1
to 47/52/57/ (42_PH3S+) POWER
1 2 3 4 5 6 7 8
A B C D E F

U02A LOW_B [1] CONNECTOR (PE0397)



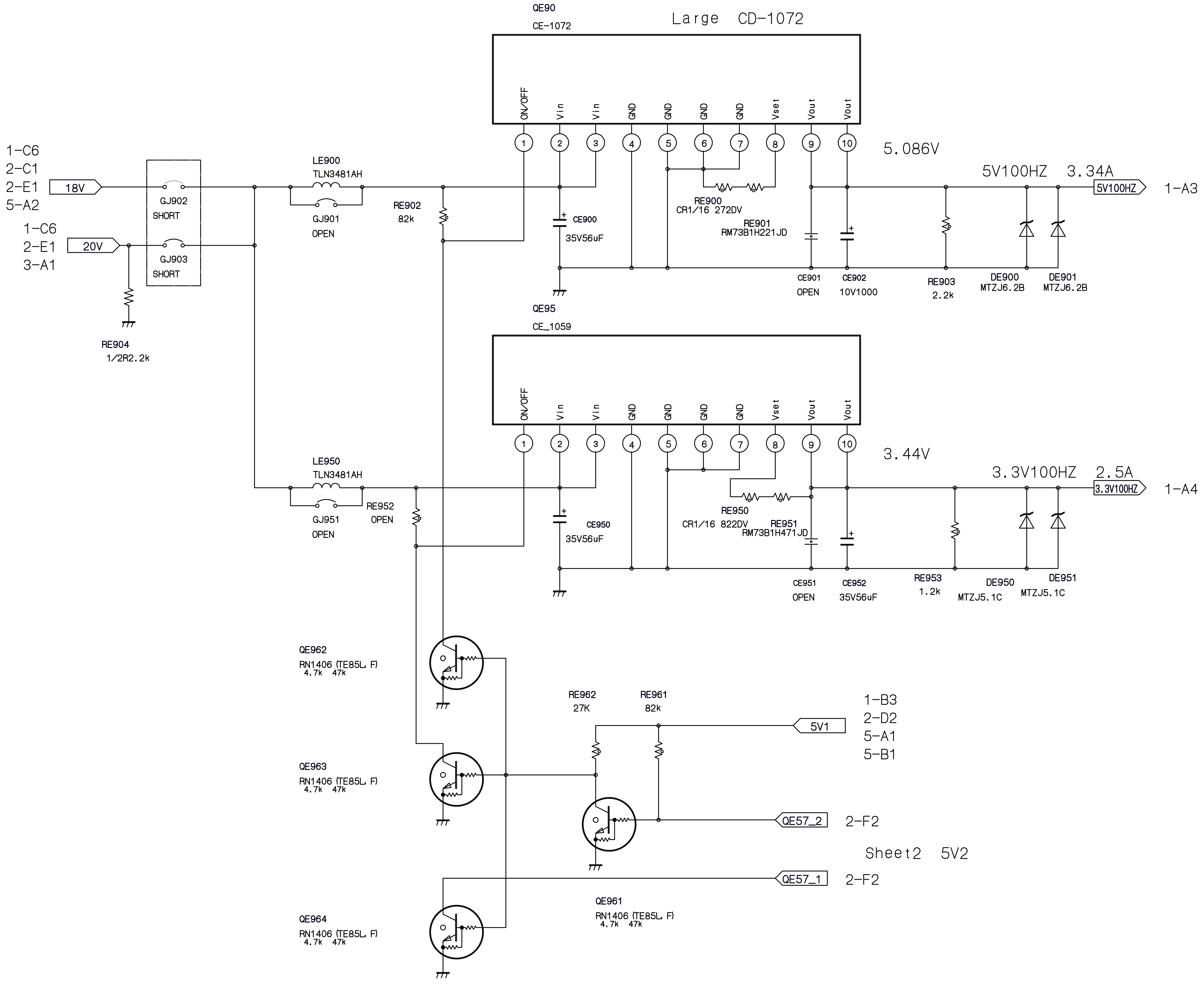
24V_INV
VCC_LCD
12.20V
12V/3.3A
1A (2A)
QE50 SI-8001FFE
LE502 TLN3481AH
QE501 2SC2655-Y
QE502 2SC2412K
QE503 RN1406
POWER_LCD
32V
40V
32V 5mA
QE31 2SA1163-GR
QE32 2SC2713-GR
DE31 MTZJ11B
DE32 MTZJ11B
DE33 MTZJ10C
18V
18V1
18V1 1.6A
QE55 TPC8111
QE552 RN1403
QE553 RN2403
QE554 RN1403
5V1
POWER_DB
12V
20V
QE57 CE_1059
LE571 TLN3481AH
5.152V 1.9A
5V2
QE572 BD5245G-TR
QE573 2SC2412K
QE574 2SC2412K
POWER_TV
QE57_1
QE57_2
Sheet4 ONOFF
AUDIO_VCC
ZE41 SMP125M02A00/20P2000
QE40 2SD2396/2SC3852
DE41 1SS355-TE17
DE42 RK34 LF-A4
DE43 MTZJ12C
DE44 MTZJ12C
24V1A R2A15106
26.5V_AUDIO
AUDIO_GND_IN
AUDIO_GND_OUT
QE53 PQ090RDA1SZH
LE531 TRF4520AZ
0.2A
9V
DE531 MTZJ12C
DE532 MTZJ12C
QE532 RN1406
QE533 2SC2412K
POWER_DB

U02A LOW_B [2] REGULATOR (PE0397)



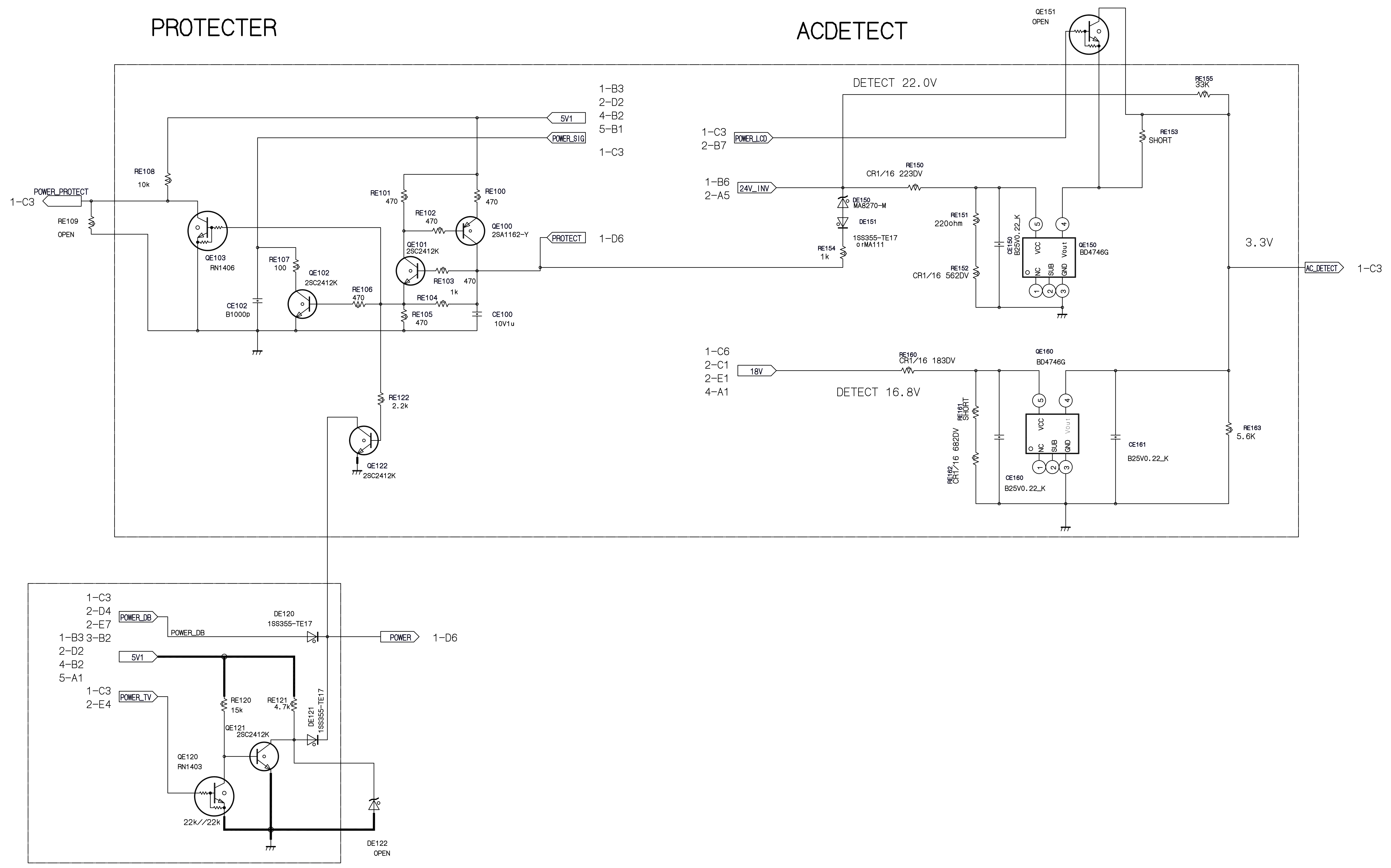
QE70
CE-1059
ON/OFF
Vin
Vin
GND
GND
GND
GND
Vset
Vout
Vout
1.5A
3.3VD
1-D4
3.394V/2.5A
2-C4
2-E5
3-A1
1-D6
2-D5
2-E1
18V1
12V
GJ702
SHORT
GJ703
SHORT
LE700
TLN3481AH
GJ070
OPEN
CE700
35V56uF
RE700
CR1/16 822DV
RE701
CHIP-JP
CE701
OPEN
DE700
MTZJ5.1C
DE701
MTZJ5.1C
QE75C GREASE SILICONE YG62 1K
QE75B SCREW
QE75A HEAT SINK
QE75 PQ070XF02SZH
2.559V 1A
2.5VD
1-D4
RE750
CR1/16 102DV
RE751
RM73B1H470JD
RE752
CR1/16 102DV
CE750
B16V0.33_K
CE751
16V56uF
QE71
RN1406
2-C4
2-E5
3-A1
1-C6
2-E1
4-A1
18V1
20V
GJ731
SHORT
GJ732
SHORT
LE730
33uH
GJ080
OPEN
QE73
PQ120RDA1SZH
V47A00005000
CE730
50V0.47
RE730
33K
CE731
50V 47u
0.02A
12VD
1-D4
RE864
4.7k
QE74
RN1406 (TE85L, F)
RE705
10k
RE707
47K
POWER_DB
1-C3
2-D4
2-E7
5-B1
RE706
22K
QE76
2SC2412K
QE81
RN1406
CE804
OPEN
CE805
B50V0.01
RE803
3.9k
QE80
MP1591DS-LF-Z
REF
EN
COMP
FB
BS
IN
SW
GND
RE802
CR1/16 103DV
RE801
220ohm
RE800
33K_D
CE806
B470p
RE804
470
6.054V
LE802
TLN3481AH/AA
1A
6VD
1-D4
LE800
TLN3481AH
RE805
RM73B1H220JD
CE800
35V150uF
CE801
B50V1u
CE803
B50V0.01
DE800
CMS03
LE801
RCP1317NP-101M
CE802
10V22u
DE801
MTZJ7.5A
DE802
MTZJ7.5A
CE807
10V100u
1
2
A
B

U02A LOW_B [3] UK DIGITAL (PE0397)

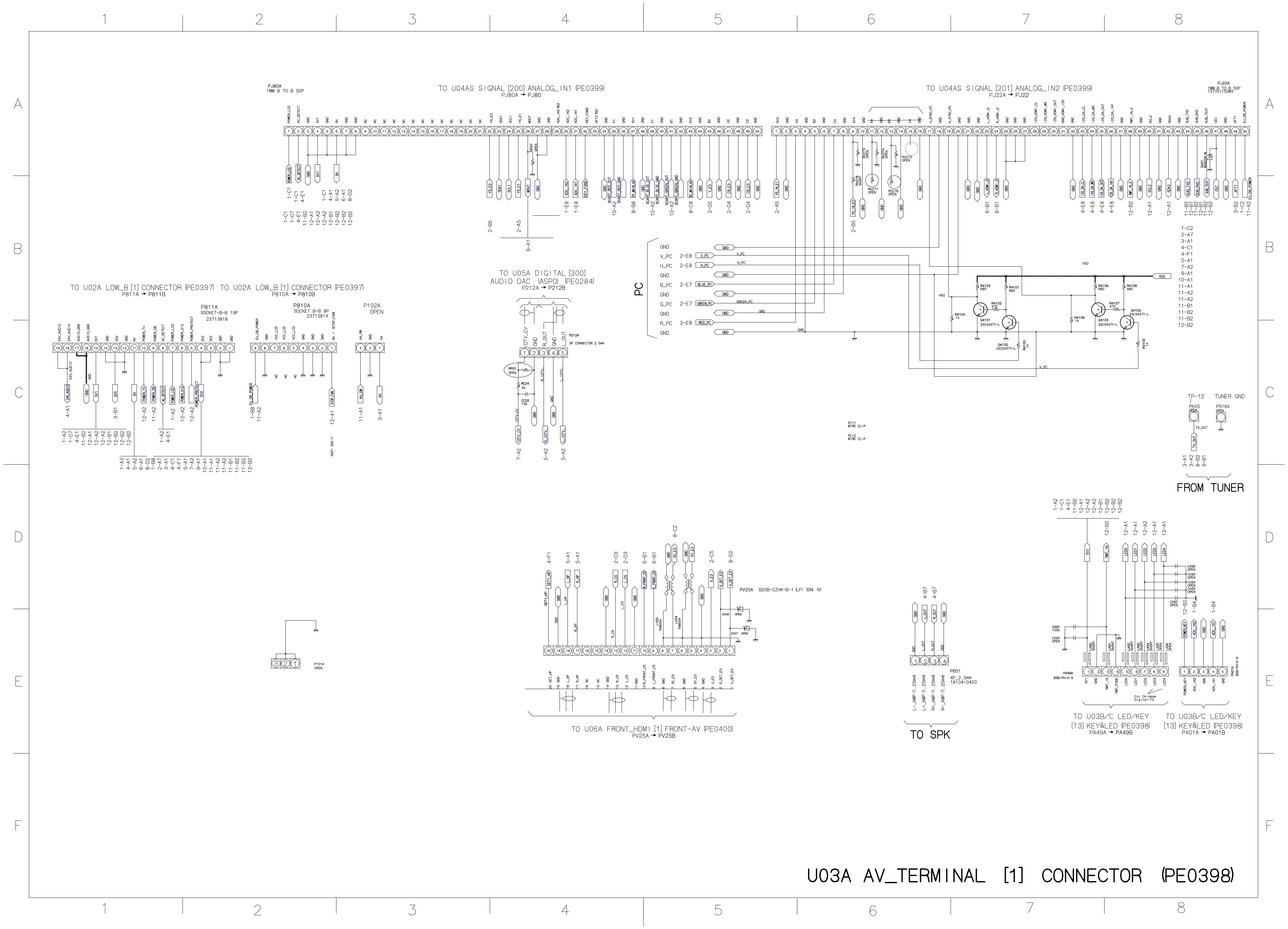

QE90
CE-1072
Large CD-1072
ON/OFF
Vin
Vin
GND
GND
GND
GND
Vset
Vout
Vout
1
2
3
4
5
6
7
8
9
10
5.086V
5V100HZ 3.34A
5V100HZ
1-A3
1-C6
2-C1
2-E1
5-A2
18V
GJ902
SHORT
1-C6
2-E1
3-A1
20V
GJ903
SHORT
RE904
1/2R2.2k
LE900
TLN3481AH
GJ901
OPEN
RE902
82k
CE900
35V56uF
RE900
CR1/16 272DV
RE901
RM73B1H221JD
CE901
OPEN
CE902
10V1000
RE903
2.2k
DE900
MTZJ6.2B
DE901
MTZJ6.2B
QE95
CE_1059
3.44V
3.3V100HZ 2.5A
3.3V100HZ
1-A4
LE950
TLN3481AH
GJ951
OPEN
RE952
OPEN
CE950
35V56uF
RE950
CR1/16 822DV
RE951
RM73B1H471JD
CE951
OPEN
CE952
35V56uF
RE953
1.2k
DE950
MTZJ5.1C
DE951
MTZJ5.1C
QE962
RN1406 (TE85L, F)
4.7k 47k
QE963
RN1406 (TE85L, F)
4.7k 47k
QE964
RN1406 (TE85L, F)
4.7k 47k
RE962
27K
RE961
82k
5V1
1-B3
2-D2
5-A1
5-B1
QE57_2
2-F2
Sheet2 5V2
QE57_1
2-F2
QE961
RN1406 (TE85L, F)
4.7k 47k

U02A LOW_B [4] for SIG100HZ (PE0397)

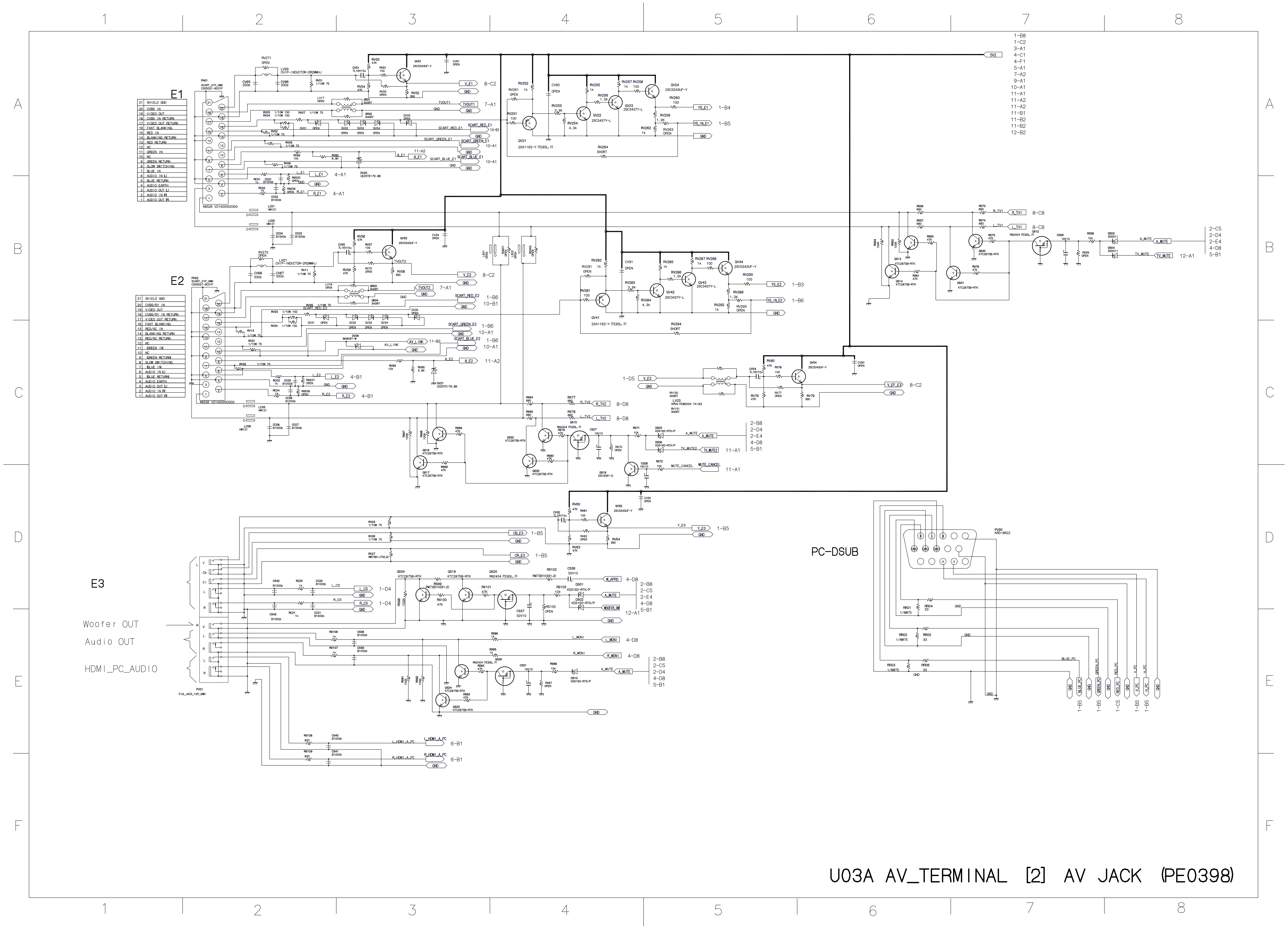
1
2
A
B
1
2
A
B


1
2
A
B
PROTECTER
ACDETECT
1-B3
2-D2
4-B2
5-B1
5V1
POWER_SIG
1-C3
POWER_PROTECT
1-C3
RE108
10k
RE109
OPEN
QE103
RN1406
RE107
100
CE102
B1000p
QE102
2SC2412K
RE106
470
RE101
470
RE102
470
RE100
470
QE101
2SC2412K
QE100
2SA1162-Y
PROTECT
1-D6
RE103
1k
470
RE104
RE105
470
CE100
10V1u
RE122
2.2k
QE122
2SC2412K
QE151
OPEN
DETECT 22.0V
RE155
33K
1-C3
2-B7
POWER_LCD
RE153
SHORT
1-B6
2-A5
24V_INV
RE150
CR1/16 223DV
DE150
MA8270-M
DE151
1SS355-TE17
orMA111
RE154
1k
RE151
220ohm
RE152
CR1/16 562DV
CE150
B25V0.22_K
VCC
Vout
NC
SUB
GND
QE150
BD4746G
3.3V
AC_DETECT
1-C3
1-C6
2-C1
2-E1
4-A1
18V
RE160
CR1/16 183DV
DETECT 16.8V
QE160
BD4746G
RE161
SHORT
RE162
CR1/16 682DV
CE160
B25V0.22_K
CE161
B25V0.22_K
RE163
5.6K
1-C3
2-D4
2-E7
1-B3 3-B2
2-D2
4-B2
5-A1
1-C3
2-E4
POWER_DB
POWER_DB
DE120
1SS355-TE17
POWER
1-D6
5V1
POWER_TV
RE120
15k
RE121
4.7k
DE121
1SS355-TE17
QE121
2SC2412K
QE120
RN1403
22k//22k
DE122
OPEN

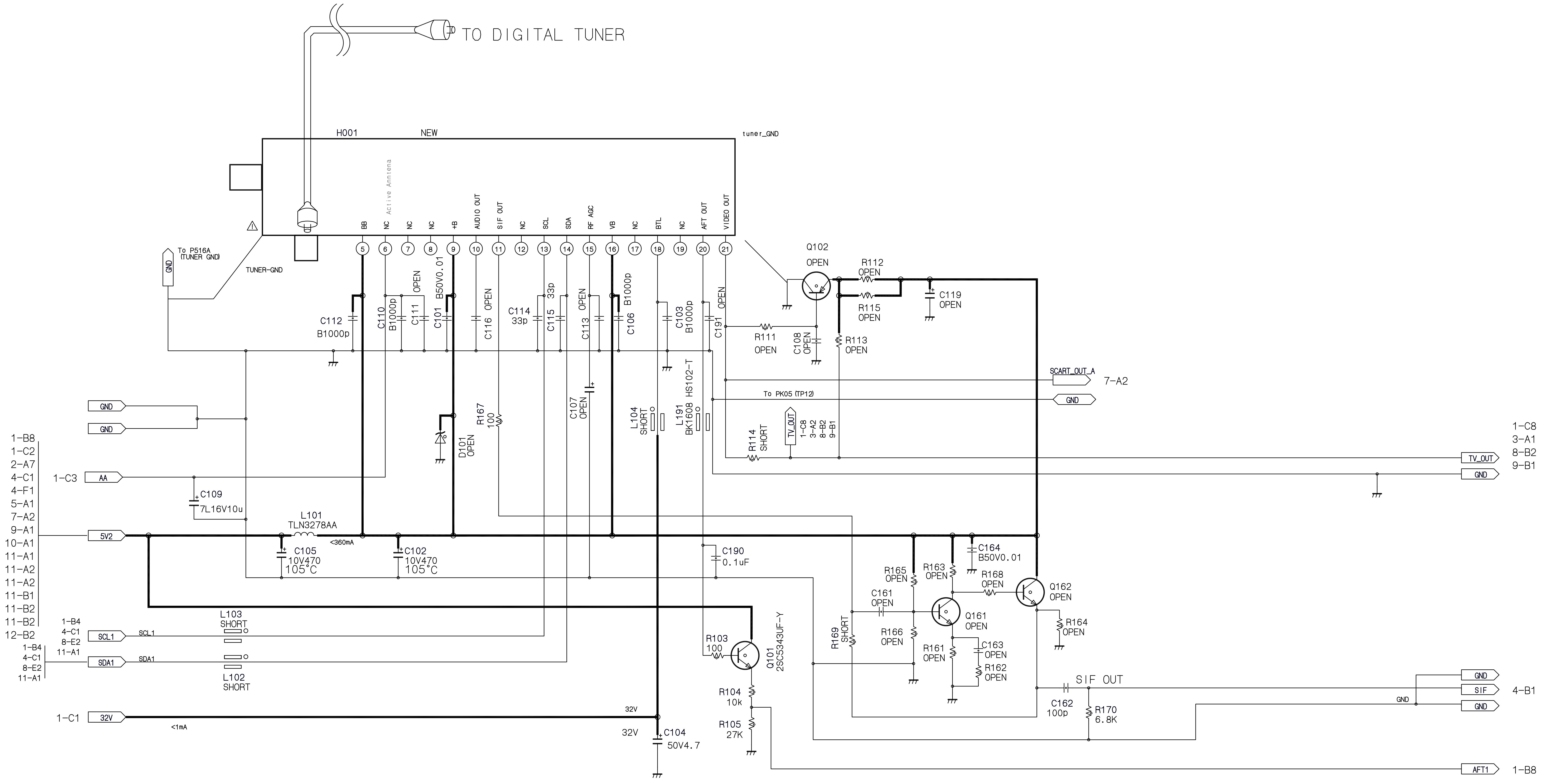
U02A LOW_B [5] Protect, ACDetect, Pow (PE0397)


TO U04AS SIGNAL [200] ANALOG_IN1 (PE0399)
PJ80A → PJ80
PJ80A
1MM B TO B 50P
TO U04AS SIGNAL [201] ANALOG_IN2 (PE0399)
PJ22A → PJ22
PJ22A
1MM B TO B 50P
TO U02A LOW_B [1] CONNECTOR (PE0397)
P811A → P811B
TO U02A LOW_B [1] CONNECTOR (PE0397)
P810A → P810B
P811A
SOCKET-B-B 19P
23713818
P810A
SOCKET B-B 9P
23713814
P102A
OPEN
TO U05A DIGITAL [300]
AUDIO DAC (ASPD) (PE0284)
P212A → P212B
PC
GND
V_PC
H_PC
GND
B_PC
GND
G_PC
GND
R_PC
GND
R4103
R4101
R4108
R4106
R4104
R4109
R4107
R4102
Q4101
Q4100
Q4103
Q4102
2SC3437Y-L
TP-12
TUNER GND
PK05
P516A
FROM TUNER
PV25A B20B-CZHK-B-1 (LF) (SN) M
TO U06A FRONT_HDMI [1] FRONT-AV (PE0400)
PV25A → PV25B
P101A
OPEN
P601
4P_2.5mm
18104-0420
L_AMP (1.25mm)
L+_AMP (1.25mm)
R+_AMP (1.25mm)
R-_AMP (1.25mm)
TO SPK
PA49A
PA01A
For Chinese Digital-TV
TO U03B/C LED/KEY
[13] KEY&LED (PE0398)
PA49A → PA49B
TO U03B/C LED/KEY
[13] KEY&LED (PE0398)
PA01A → PA01B
1
2
3
4
5
6
7
8
A
B
C
D
E
F

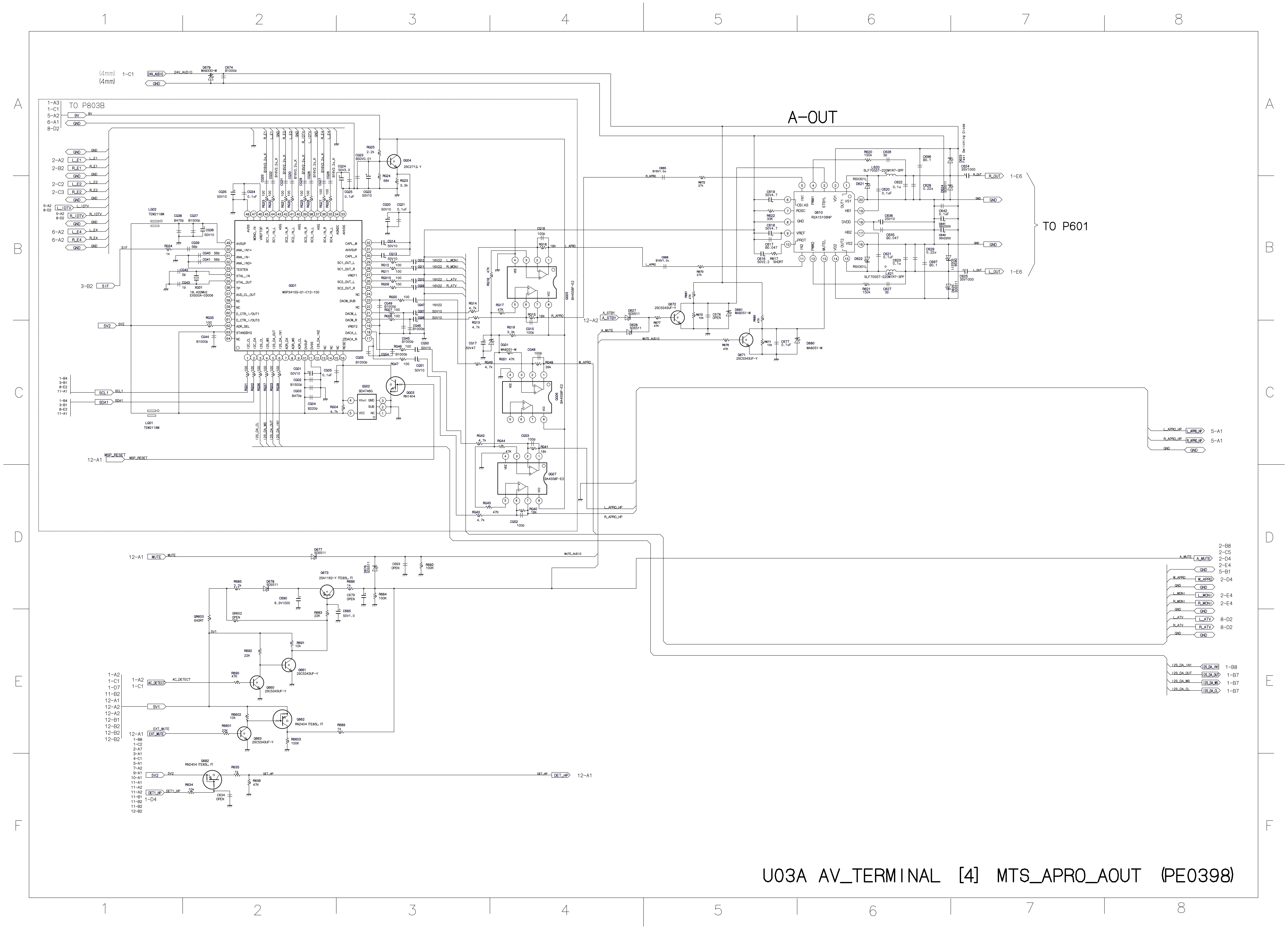
U03A AV_TERMINAL [1] CONNECTOR (PE0398)


E1
E2
E3
PC-DSUB
Woofer OUT
Audio OUT
HDMI_PC_AUDIO

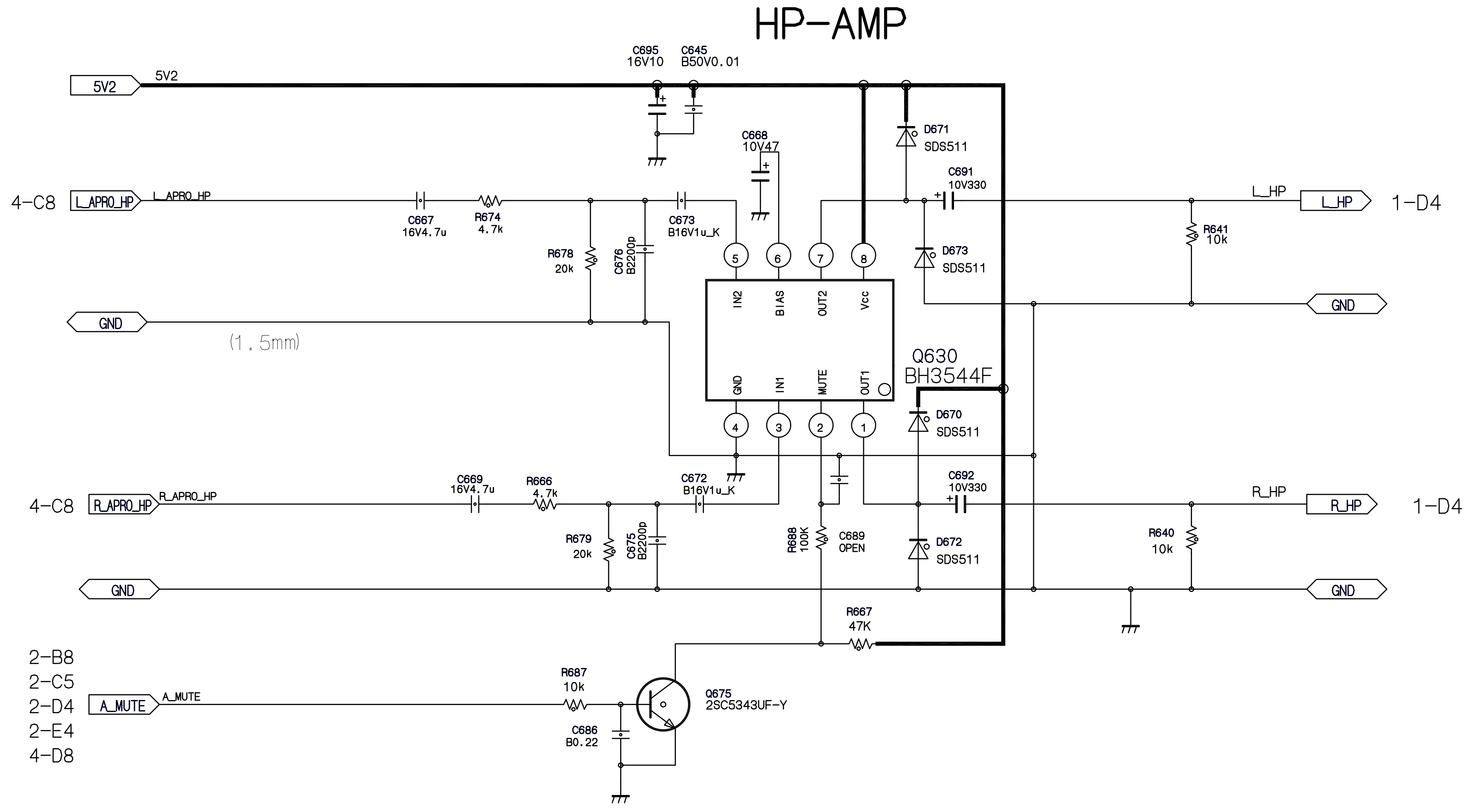
U03A AV_TERMINAL [2] AV JACK (PE0398)

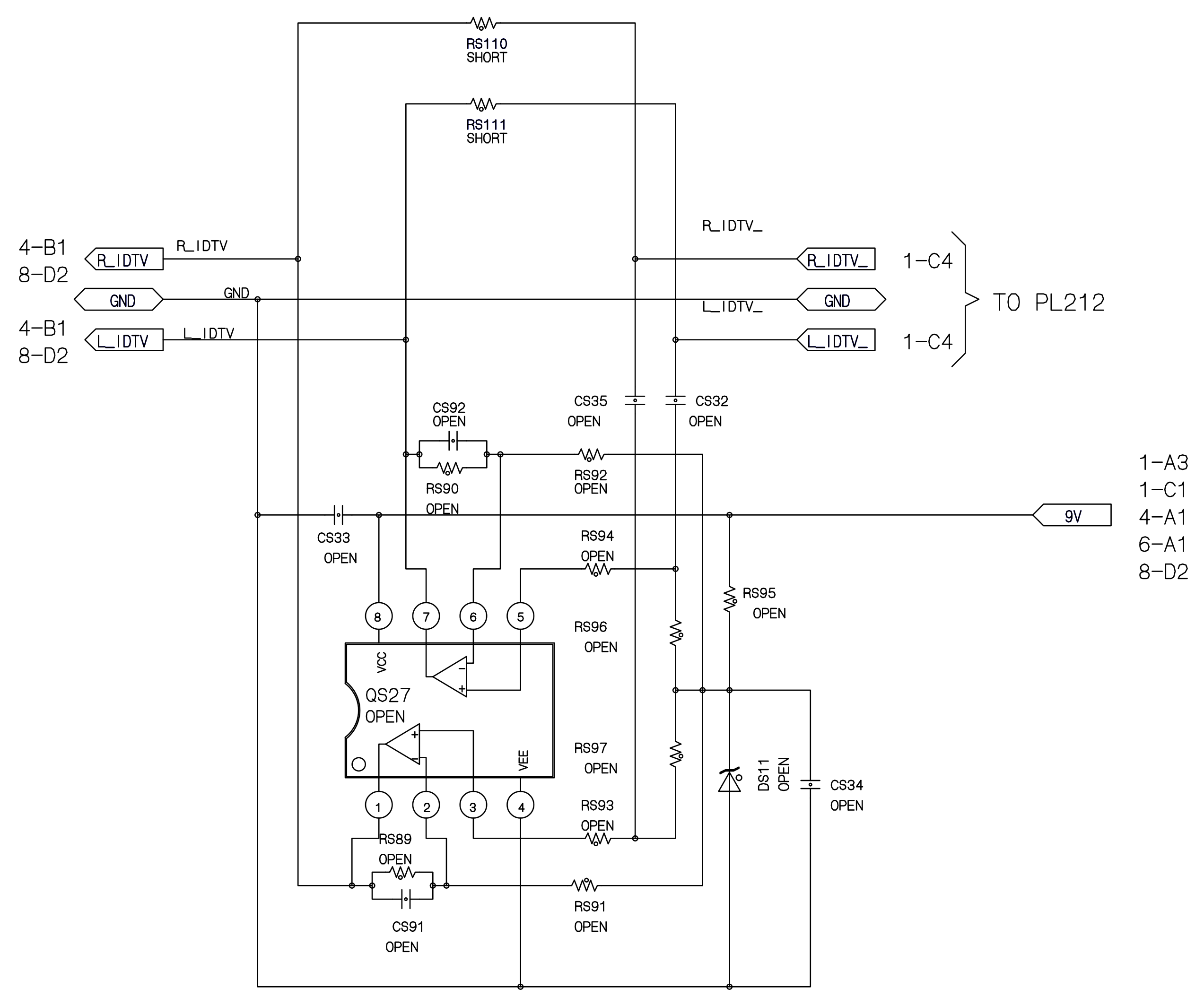

TO DIGITAL TUNER
H001
NEW
tuner_GND
Active Anntena
BB
NC
NC
NC
+B
AUDIO OUT
SIF OUT
NC
SCL
SDA
RF AGC
VB
NC
BTL
NC
AFT OUT
VIDEO OUT
5
6
7
8
9
10
11
12
13
14
15
16
17
18
19
20
21
GND
To P516A
(TUNER GND)
TUNER-GND
C112
B1000p
C110
B1000p
C111
OPEN
C101
B50V0.01
C116
OPEN
C114
33p
C115
33p
C113
OPEN
C106
B1000p
C103
B1000p
C191
OPEN
Q102
OPEN
R112
OPEN
R115
OPEN
C119
OPEN
R111
OPEN
C108
OPEN
R113
OPEN
SCART_OUT_A
7-A2
To PK05 (TP12)
GND
D101
OPEN
R167
100
C107
OPEN
L104
SHORT
L191
BK1608
HS102-T
R114
SHORT
TV_OUT
1-C8
3-A2
8-B2
9-B1
1-C8
3-A1
8-B2
9-B1
TV_OUT
GND
GND
GND
1-B8
1-C2
2-A7
4-C1
4-F1
5-A1
7-A2
9-A1
10-A1
11-A1
11-A2
11-A2
11-B1
11-B2
11-B2
12-B2
1-C3
AA
C109
7L16V10u
5V2
L101
TLN3278AA
<360mA
C105
10V470
105°C
C102
10V470
105°C
C190
0.1uF
C164
B50V0.01
R165
OPEN
R163
OPEN
R168
OPEN
Q162
OPEN
C161
OPEN
Q161
OPEN
R169
SHORT
R166
OPEN
R161
OPEN
C163
OPEN
R162
OPEN
R164
OPEN
1-B4
4-C1
8-E2
11-A1
SCL1
SCL1
L103
SHORT
1-B4
4-C1
8-E2
11-A1
SDA1
SDA1
L102
SHORT
R103
100
Q101
2SC5343UF-Y
R104
10k
R105
27K
SIF OUT
C162
100p
R170
6.8K
GND
GND
SIF
GND
4-B1
1-C1
32V
<1mA
32V
32V
C104
50V4.7
AFT1
1-B8

U03A AV_TERMINAL [3] TUNER (PE0398)


A-OUT
TO P803B
TO P601
Q601
MSP3410G-Q1-C12-100
Q610
R2A15106NP
Q605
BA4558F-E2
Q606
BA4558F-E2
Q607
BA4558F-E2
24V_AUDIO
GND
9V
SIF
5V2
SCL1
SDA1
MSP_RESET
MUTE
A_MUTE
A_STBY
AC_DETECT
5V1
EXT_MUTE
DET1_HP
DET_HP
R_OUT
L_OUT
L_APRO_HP
R_APRO_HP
W_APRO
L_MON1
R_MON1
L_ATV
R_ATV
I2S_DA_IN1
I2S_DA_OUT
I2S_DA_WS
I2S_DA_CL
1
2
3
4
5
6
7
8
A
B
C
D
E
F

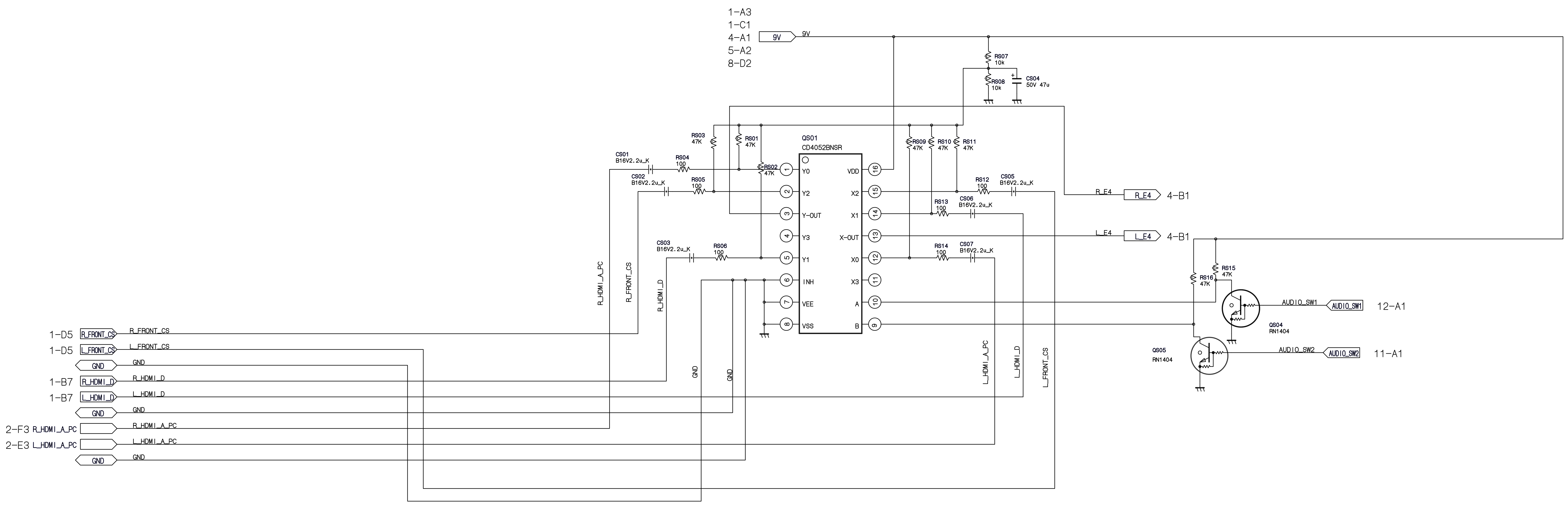
U03A AV_TERMINAL [4] MTS_APRO_AOUT (PE0398)


HP-AMP
5V2
5V2
C695
16V10
C645
B50V0.01
C668
10V47
D671
SDS511
C691
10V330
4-C8
L_APRO_HP
L_APRO_HP
C667
16V4.7u
R674
4.7k
C673
B16V1u_K
R678
20k
C676
B2200p
D673
SDS511
L_HP
L_HP
1-D4
R641
10k
GND
GND
(1.5mm)
IN2
BIAS
OUT2
Vcc
GND
IN1
MUTE
OUT1
Q630
BH3544F
D670
SDS511
4-C8
R_APRO_HP
R_APRO_HP
C669
16V4.7u
R666
4.7k
C672
B16V1u_K
C692
10V330
R_HP
R_HP
1-D4
R679
20k
C675
B2200p
R688
100K
C689
OPEN
D672
SDS511
R640
10k
GND
GND
R667
47K
2-B8
2-C5
2-D4
2-E4
4-D8
A_MUTE
A_MUTE
R687
10k
C686
B0.22
Q675
2SC5343UF-Y


RS110
SHORT
RS111
SHORT
4-B1
8-D2
R_IDTV
R_IDTV
GND
GND
4-B1
8-D2
L_IDTV
L_IDTV
R_IDTV_
R_IDTV_
1-C4
GND
L_IDTV_
L_IDTV_
1-C4
TO PL212
CS92
OPEN
RS90
OPEN
CS35
OPEN
CS32
OPEN
RS92
OPEN
CS33
OPEN
9V
1-A3
1-C1
4-A1
6-A1
8-D2
RS94
OPEN
RS95
OPEN
RS96
OPEN
VCC
QS27
OPEN
VEE
RS97
OPEN
DS11
OPEN
CS34
OPEN
RS93
OPEN
RS89
OPEN
CS91
OPEN
RS91
OPEN

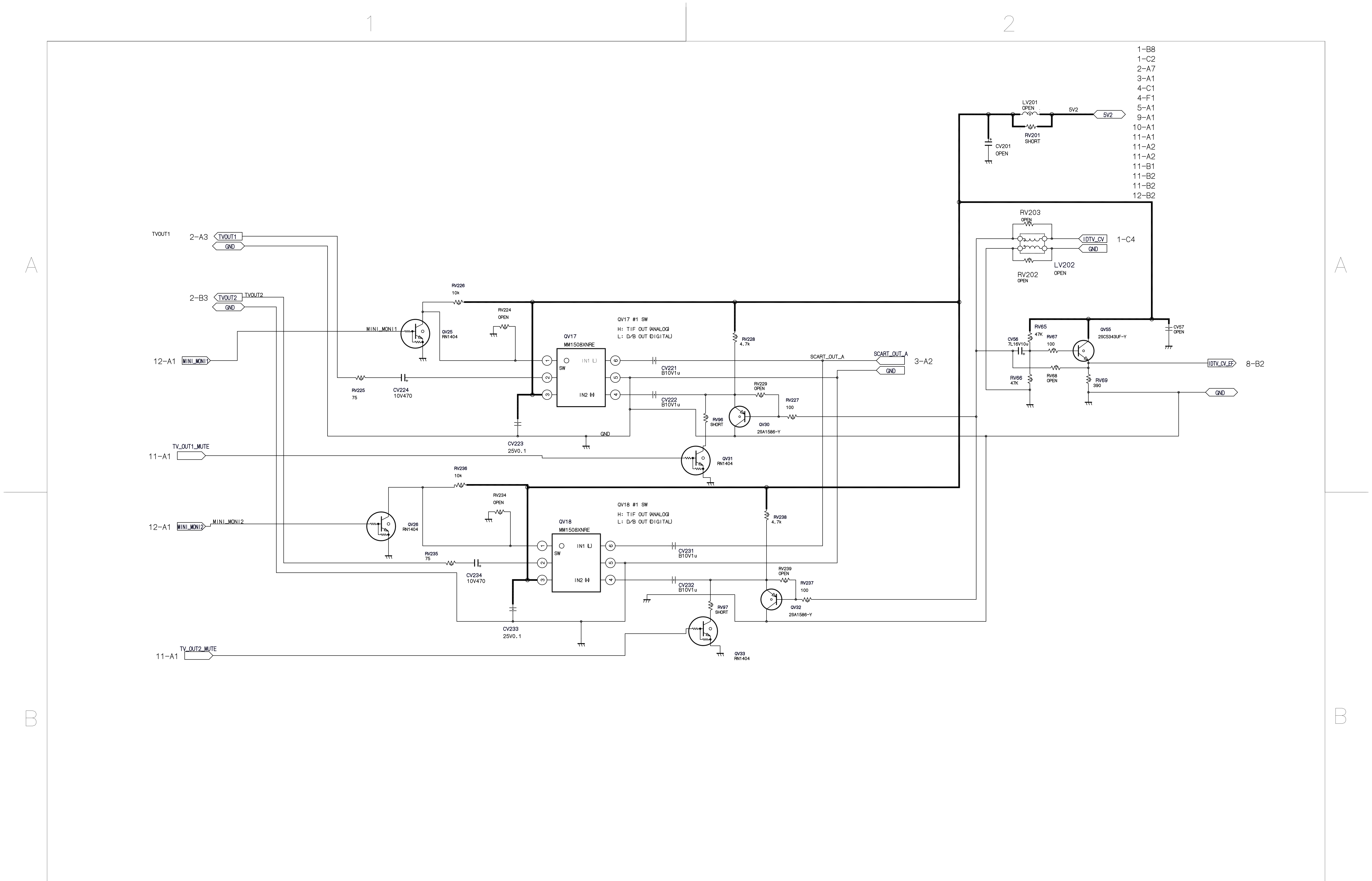
U03A AV_TERMINAL [5] EU_DIGITAL_RECEIVE CIRCUIT HEADPHONE_AMP (PE0398)

A A

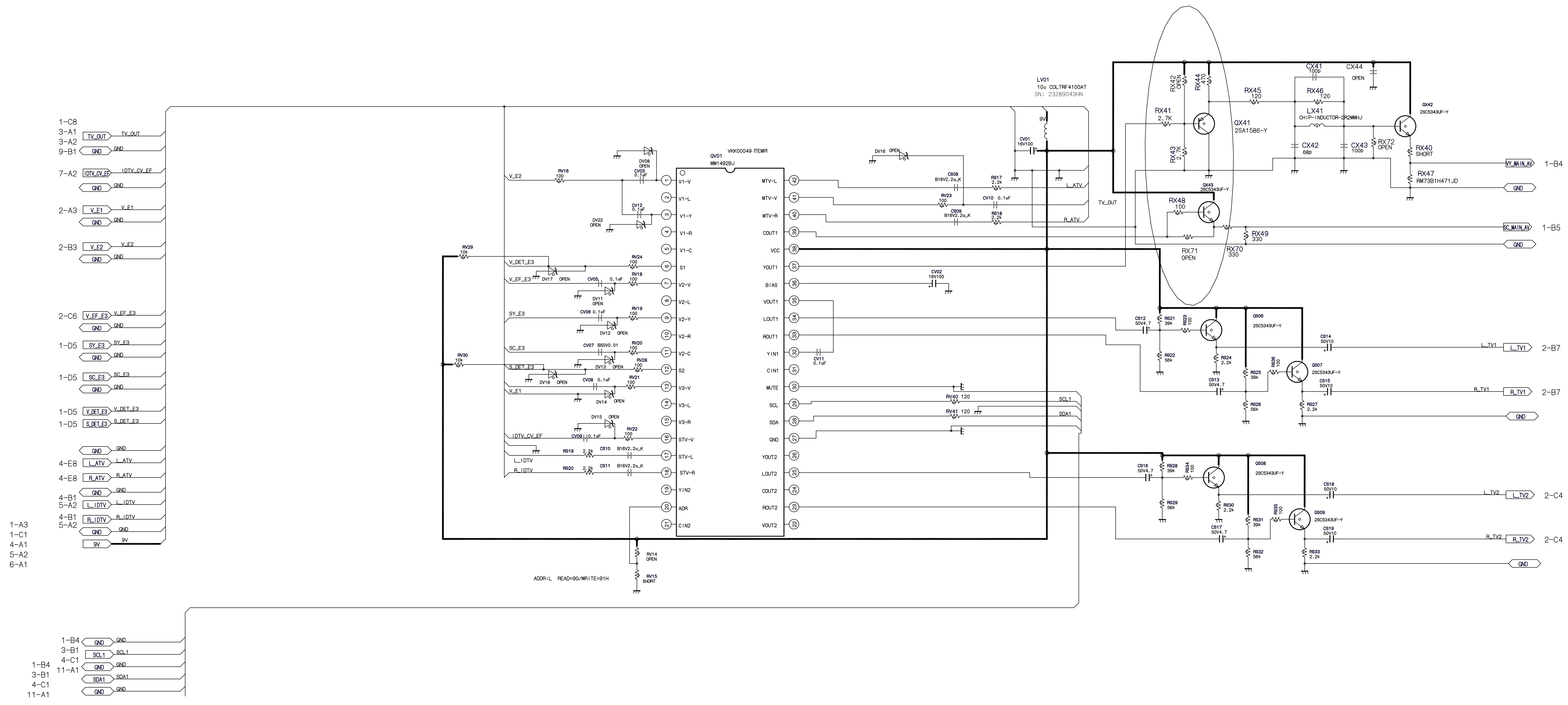

1-A3
1-C1
4-A1
5-A2
8-D2
9V
9V
RS07
10k
RS08
10k
CS04
50V 47u
QS01
CD4052BNSR
RS03
47K
RS01
47K
RS02
47K
CS01
B16V2.2u_K
RS04
100
CS02
B16V2.2u_K
RS05
100
CS03
B16V2.2u_K
RS06
100
Y0
Y2
Y-OUT
Y3
Y1
INH
VEE
VSS
VDD
X2
X1
X-OUT
X0
X3
A
B
1
2
3
4
5
6
7
8
16
15
14
13
12
11
10
9
RS09
47K
RS10
47K
RS11
47K
RS12
100
CS05
B16V2.2u_K
RS13
100
CS06
B16V2.2u_K
RS14
100
CS07
B16V2.2u_K
R_E4
R_E4
4-B1
L_E4
L_E4
4-B1
RS15
47K
RS16
47K
QS04
RN1404
QS05
RN1404
AUDIO_SW1
AUDIO_SW1
12-A1
AUDIO_SW2
AUDIO_SW2
11-A1
R_HDMI_A_PC
R_FRONT_CS
R_HDMI_D
GND
GND
L_HDMI_A_PC
L_HDMI_D
L_FRONT_CS
1-D5
R_FRONT_CS
R_FRONT_CS
1-D5
L_FRONT_CS
L_FRONT_CS
GND
GND
1-B7
R_HDMI_D
R_HDMI_D
1-B7
L_HDMI_D
L_HDMI_D
GND
GND
2-F3 R_HDMI_A_PC
R_HDMI_A_PC
2-E3 L_HDMI_A_PC
L_HDMI_A_PC
GND
GND

B B

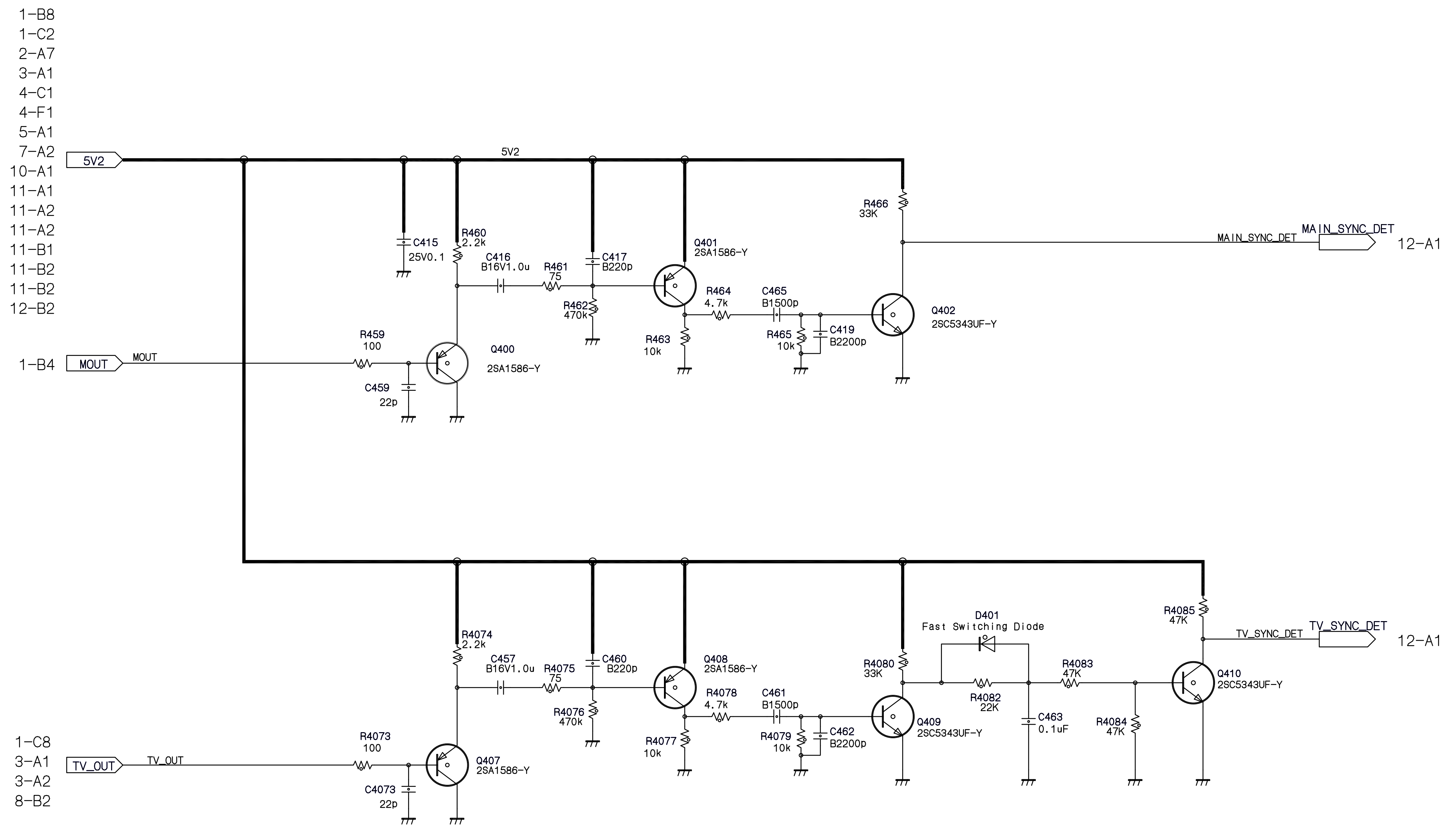
U03A AV_TERMINAL [6] AUDIO_SW (PE0398)


1
2
A
B
1-B8
1-C2
2-A7
3-A1
4-C1
4-F1
5-A1
9-A1
10-A1
11-A1
11-A2
11-A2
11-B1
11-B2
11-B2
12-B2
LV201
OPEN
5V2
5V2
RV201
SHORT
CV201
OPEN
RV203
OPEN
IDTV_CV
GND
1-C4
LV202
OPEN
RV202
OPEN
TVOUT1
2-A3
TVOUT1
GND
2-B3
TVOUT2
TVOUT2
GND
RV226
10k
RV224
OPEN
MINI_MONI1
QV25
RN1404
12-A1
MINI_MONI1
QV17
MM1508XNRE
QV17 #1 SW
H: TIF OUT (ANALOG)
L: D/B OUT (DIGITAL)
SW
IN1 (L)
IN2 (H)
RV225
75
CV224
10V470
CV221
B10V1u
CV222
B10V1u
CV223
25V0.1
GND
RV228
4.7k
SCART_OUT_A
SCART_OUT_A
GND
3-A2
RV229
OPEN
RV227
100
RV96
SHORT
QV30
2SA1586-Y
TV_OUT1_MUTE
11-A1
QV31
RN1404
RV65
47K
QV55
2SC5343UF-Y
CV57
OPEN
CV56
7L16V10u
RV67
100
IDTV_CV_EF
8-B2
RV66
47K
RV68
OPEN
RV69
390
GND
RV236
10k
RV234
OPEN
12-A1
MINI_MONI2
MINI_MONI2
QV26
RN1404
QV18
MM1508XNRE
QV18 #1 SW
H: TIF OUT (ANALOG)
L: D/B OUT (DIGITAL)
SW
IN1 (L)
IN2 (H)
RV238
4.7k
RV235
75
CV234
10V470
CV231
B10V1u
CV232
B10V1u
RV239
OPEN
RV237
100
QV32
2SA1586-Y
RV97
SHORT
CV233
25V0.1
QV33
RN1404
11-A1
TV_OUT2_MUTE

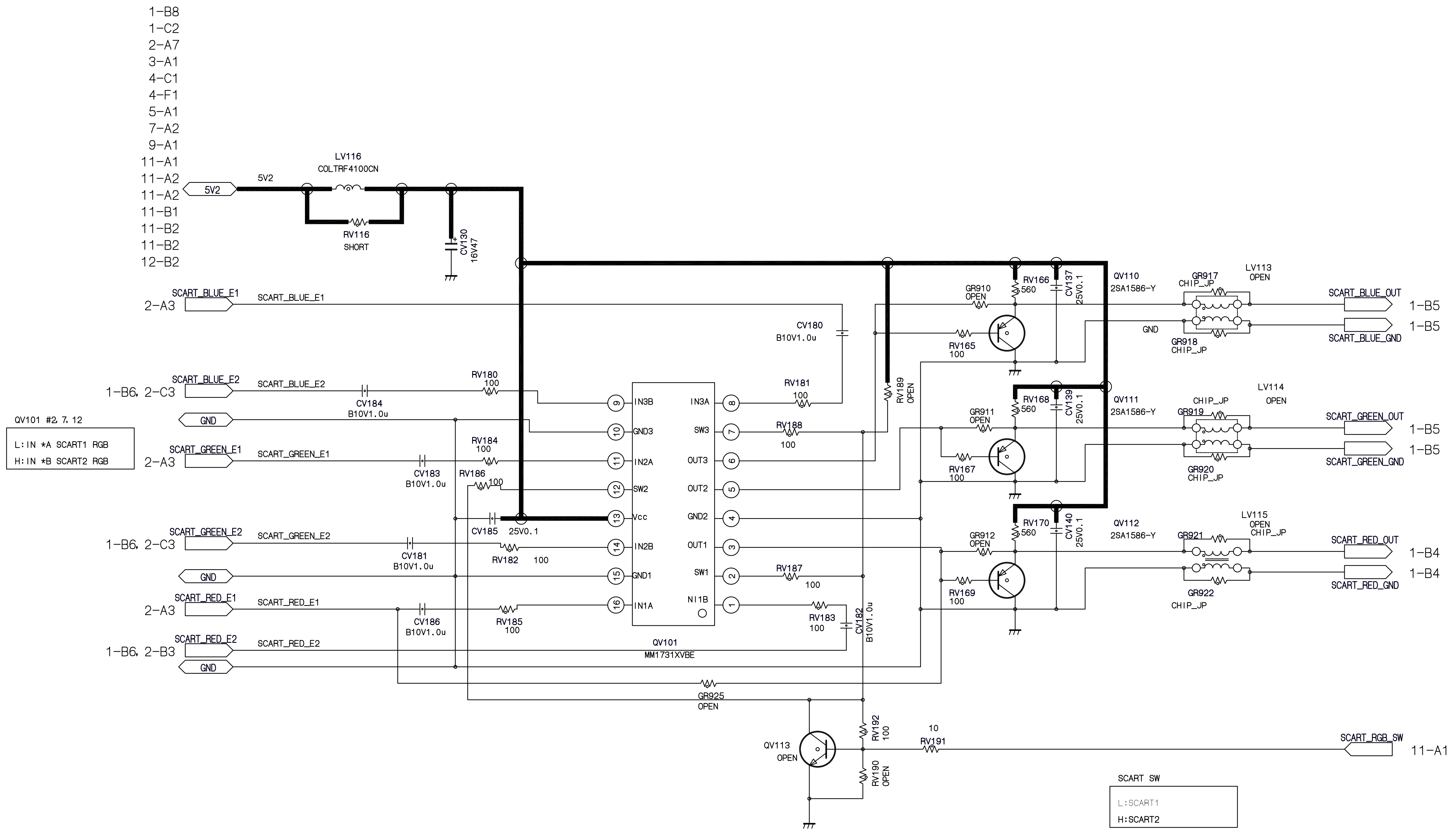
U03A AV_TERMINAL [7] TV-OUT&MONI-OUT (PE0398)


1-C8
3-A1
3-A2
9-B1
TV_OUT
GND
7-A2
IDTV_CV_EF
2-A3
V_E1
2-B3
V_E2
2-C6
V_EF_E3
1-D5
SY_E3
SC_E3
V_DET_E3
S_DET_E3
4-E8
L_ATV
R_ATV
4-B1
5-A2
L_IDTV
R_IDTV
1-A3
1-C1
4-A1
6-A1
9V
1-B4
3-B1
4-C1
11-A1
SCL1
SDA1
QV01
MM1492BJ
ADR-L READ=90/WRITE=91H
LV01
RX41
2.7K
RX42
RX43
RX44
RX45
120
RX46
120
CX41
CX44
OPEN
LX41
CHIP-INDUCTOR-SR2M44J
QX41
2SA1586-Y
CX42
68p
CX43
100p
RX72
OPEN
RX40
SHORT
RX47
RX48
100
RX49
330
RX71
OPEN
RX70
330
QX42
2SC5343UF-Y
QX43
2SC5343UF-Y
Y_MAIN_AV
1-B4
SC_MAIN_AV
1-B5
GND
L_TV1
R_TV1
2-B7
L_TV2
R_TV2
2-C4
Q608
Q607
Q609
2SC5343UF-Y

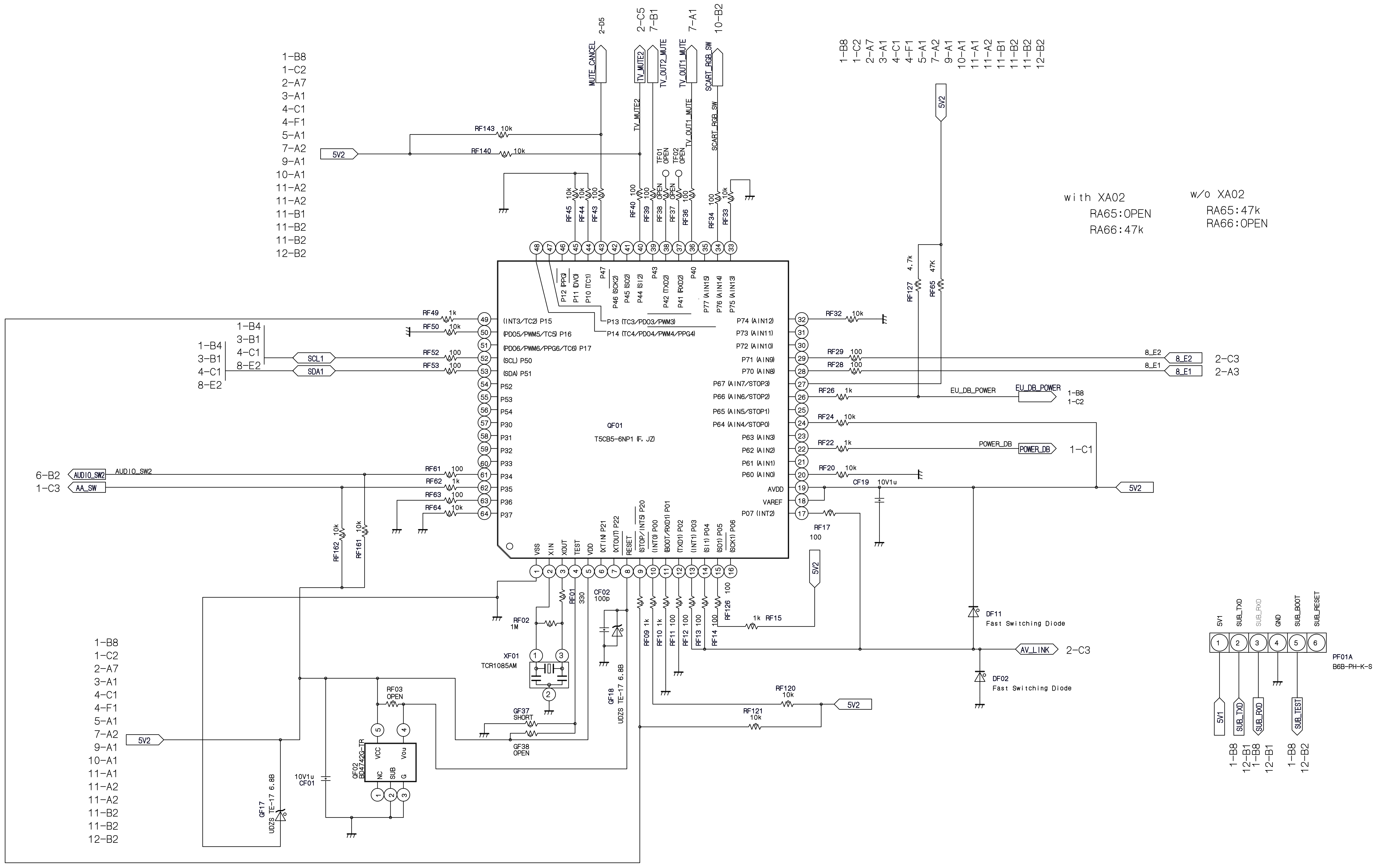
U03A AV_TERMINAL [8] AV_SW (PE0398)


1-B8
1-C2
2-A7
3-A1
4-C1
4-F1
5-A1
7-A2
10-A1
11-A1
11-A2
11-A2
11-B1
11-B2
11-B2
12-B2
5V2
5V2
C415
25V0.1
R460
2.2k
C416
B16V1.0u
R461
75
R462
470k
C417
B220p
Q401
2SA1586-Y
R464
4.7k
R463
10k
C465
B1500p
R465
10k
C419
B2200p
Q402
2SC5343UF-Y
R466
33K
MAIN_SYNC_DET
MAIN_SYNC_DET
12-A1
1-B4
MOUT
MOUT
R459
100
C459
22p
Q400
2SA1586-Y
R4074
2.2k
C457
B16V1.0u
R4075
75
R4076
470k
C460
B220p
Q408
2SA1586-Y
R4078
4.7k
R4077
10k
C461
B1500p
R4079
10k
C462
B2200p
R4080
33K
Q409
2SC5343UF-Y
D401
Fast Switching Diode
R4082
22K
C463
0.1uF
R4083
47K
R4084
47K
R4085
47K
Q410
2SC5343UF-Y
TV_SYNC_DET
TV_SYNC_DET
12-A1
1-C8
3-A1
3-A2
8-B2
TV_OUT
TV_OUT
R4073
100
C4073
22p
Q407
2SA1586-Y
1
2
A
A
B
B
1
2

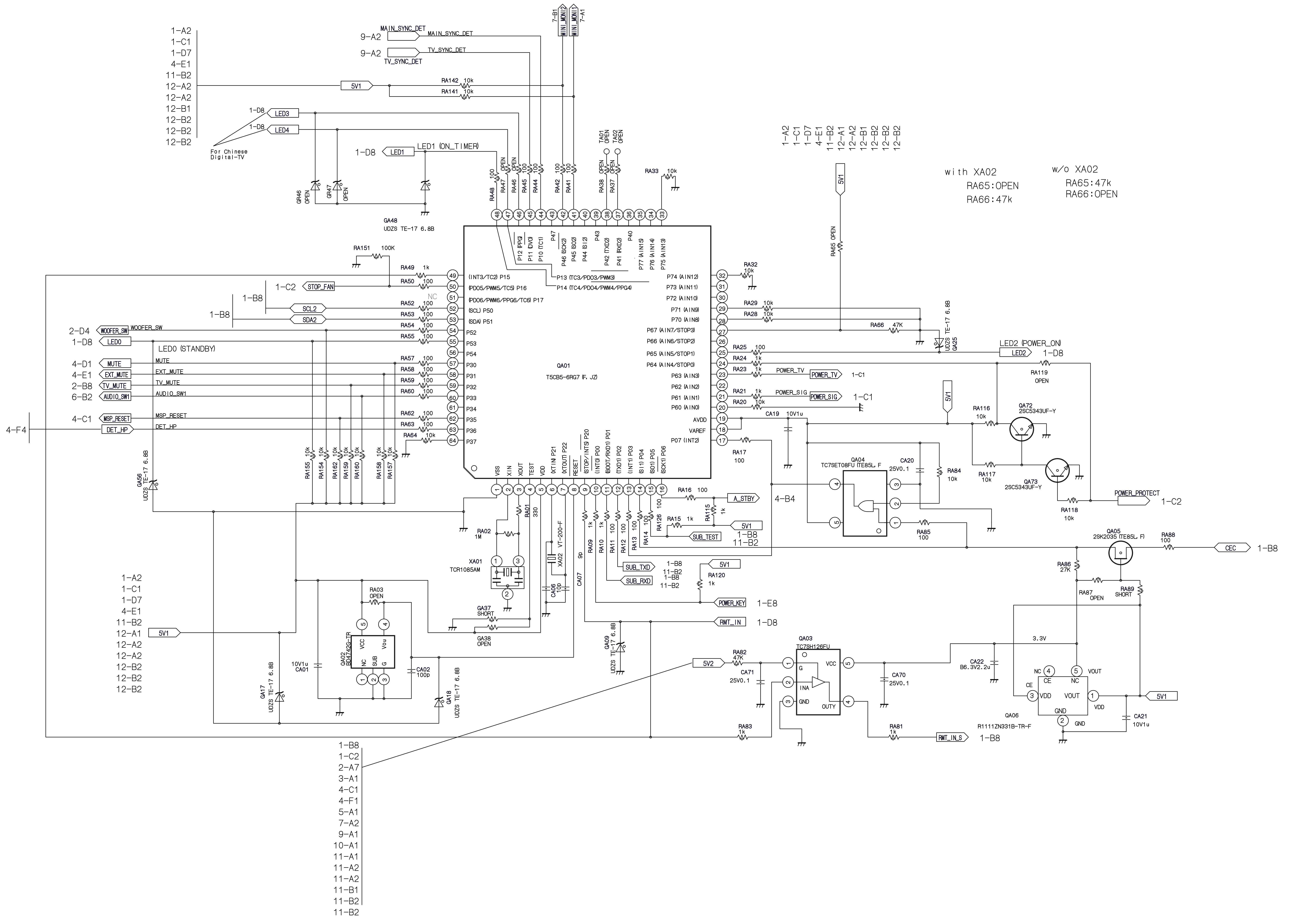
U03A AV_TERMINAL [9] SYNC_DET (PE0398)


1
2
A
B
1-B8
1-C2
2-A7
3-A1
4-C1
4-F1
5-A1
7-A2
9-A1
11-A1
11-A2
11-A2
11-B1
11-B2
11-B2
12-B2
5V2
5V2
LV116
COLTRF4100CN
RV116
SHORT
CV130
16V47
SCART_BLUE_E1
2-A3
SCART_BLUE_E1
SCART_BLUE_E2
1-B6, 2-C3
SCART_BLUE_E2
CV184
B10V1.0u
RV180
100
GND
QV101 #2, 7, 12
L:IN *A SCART1 RGB
H:IN *B SCART2 RGB
SCART_GREEN_E1
2-A3
SCART_GREEN_E1
CV183
B10V1.0u
RV184
100
RV186
100
CV185
25V0.1
SCART_GREEN_E2
1-B6, 2-C3
SCART_GREEN_E2
CV181
B10V1.0u
RV182
100
GND
SCART_RED_E1
2-A3
SCART_RED_E1
CV186
B10V1.0u
RV185
100
SCART_RED_E2
1-B6, 2-B3
SCART_RED_E2
GND
IN3B
GND3
IN2A
SW2
Vcc
IN2B
GND1
IN1A
IN3A
SW3
OUT3
OUT2
GND2
OUT1
SW1
NI1B
QV101
MM1731XVBE
GR925
OPEN
CV180
B10V1.0u
RV181
100
RV188
100
RV187
100
RV183
100
CV182
B10V1.0u
RV189
OPEN
GR910
OPEN
RV166
560
CV137
25V0.1
RV165
100
QV110
2SA1586-Y
GR911
OPEN
RV168
560
CV139
25V0.1
RV167
100
QV111
2SA1586-Y
GR912
OPEN
RV170
560
CV140
25V0.1
RV169
100
QV112
2SA1586-Y
GR917
CHIP_JP
LV113
OPEN
GND
GR918
CHIP_JP
SCART_BLUE_OUT
1-B5
SCART_BLUE_GND
1-B5
CHIP_JP
LV114
OPEN
GR919
GR920
CHIP_JP
SCART_GREEN_OUT
1-B5
SCART_GREEN_GND
1-B5
LV115
OPEN
CHIP_JP
GR921
GR922
CHIP_JP
SCART_RED_OUT
1-B4
SCART_RED_GND
1-B4
QV113
OPEN
RV192
100
RV190
OPEN
10
RV191
SCART_RGB_SW
11-A1
SCART SW
L:SCART1
H:SCART2

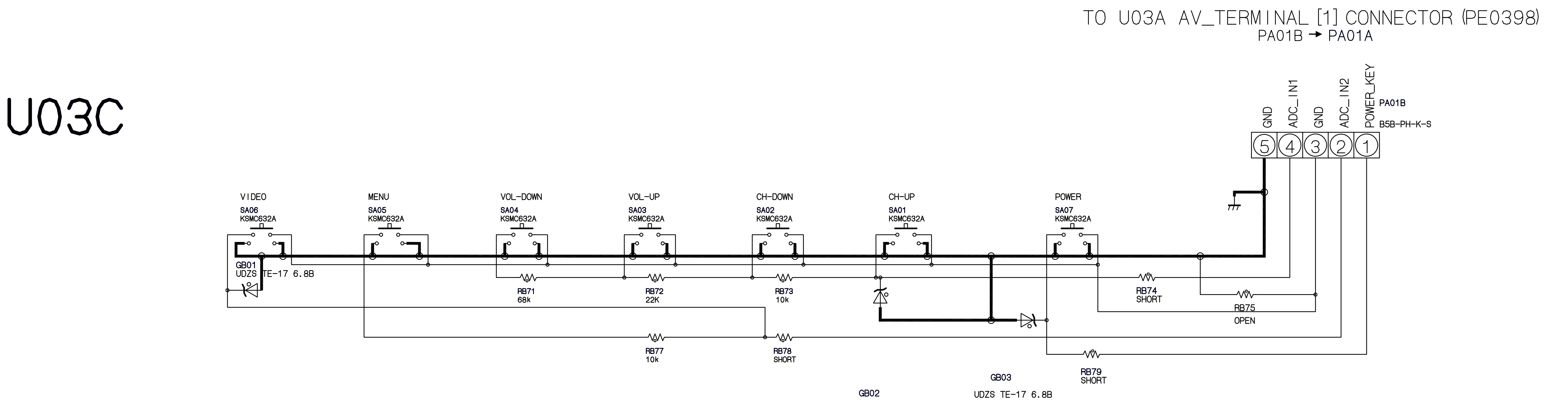
U03A AV_TERMINAL [10] SCART SW (PE0398)

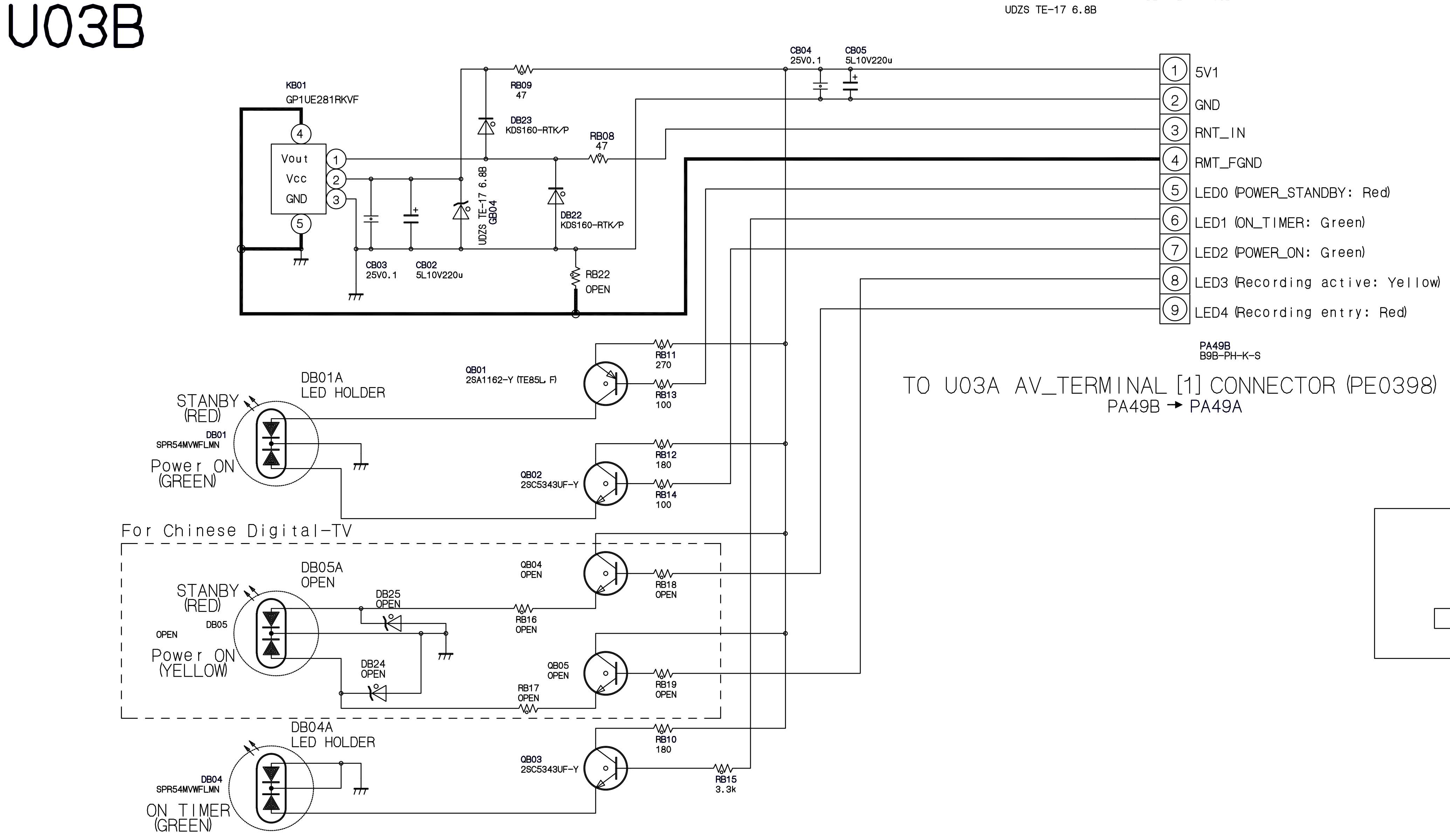

QF01
T5CB5-6NP1 (F, JZ)
MUTE_CANCEL 2-D5
TV_MUTE2 2-C5 7-B1
TV_OUT2_MUTE
TV_OUT1_MUTE 7-A1
SCART_RGB_SW 10-B2
RF143 10k
RF140 10k
5V2
RF45 10k
RF44 10k
RF43 100
RF40 100
RF39 100
RF38 OPEN
RF37 OPEN
RF36 100
RF34 100
RF33 10k
TF01 OPEN
TF02 OPEN
RF49 1k
RF50 10k
RF52 100
RF53 100
SCL1
SDA1
1-B4 3-B1 4-C1 8-E2
AUDIO_SW2 6-B2
AA_SW 1-C3
RF61 100
RF62 1k
RF63 100
RF64 10k
RF162 10k
RF161 10k
RF32 10k
RF29 100
RF28 100
8_E2 2-C3
8_E1 2-A3
RF26 1k
EU_DB_POWER 1-B8 1-C2
RF24 10k
RF22 1k
POWER_DB 1-C1
RF20 10k
CF19 10V1u
RF17 100
RF127 4.7k
RF65 47K
with XA02
RA65:OPEN
RA66:47k
w/o XA02
RA65:47k
RA66:OPEN
1-B8 1-C2 2-A7 3-A1 4-C1 4-F1 5-A1 7-A2 9-A1 10-A1 11-A1 11-A2 11-B1 11-B2 11-B2 12-B2
RF01 330
CF02 100p
RF02 1M
XF01 TCR1085AM
RF09 1k
RF10 1k
RF11 100
RF12 100
RF13 100
RF14 100
RF126 100
RF15 1k
RF120 10k
RF121 10k
GF18 UDZS TE-17 6.8B
GF17 UDZS TE-17 6.8B
GF37 SHORT
GF38 OPEN
RF03 OPEN
QF02 BD4742G-TR
VCC Vou NC SUB G
10V1u CF01
DF11 Fast Switching Diode
DF02 Fast Switching Diode
AV_LINK 2-C3
PF01A B6B-PH-K-S
5V1 SUB_TXD SUB_RXD GND SUB_BOOT SUB_RESET
5V1 SUB_TXD SUB_RXD SUB_TEST
1-B8 12-B1 1-B8 12-B1 1-B8 12-B2

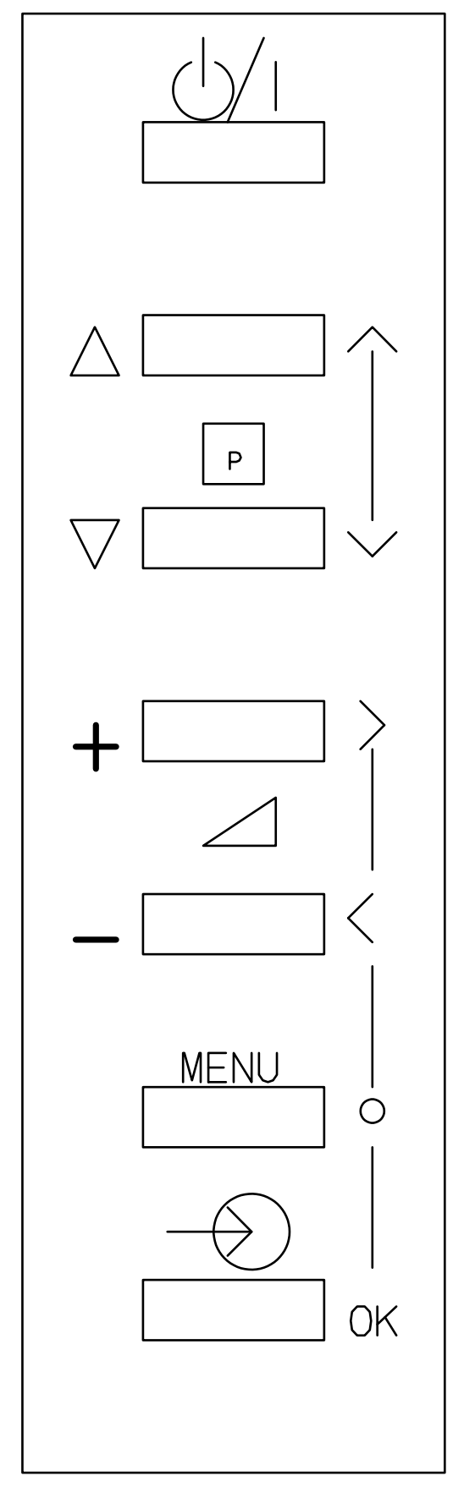
U03A AV_TERMINAL [11] AV-Link Micoro GPIO for Scart (PE0398)

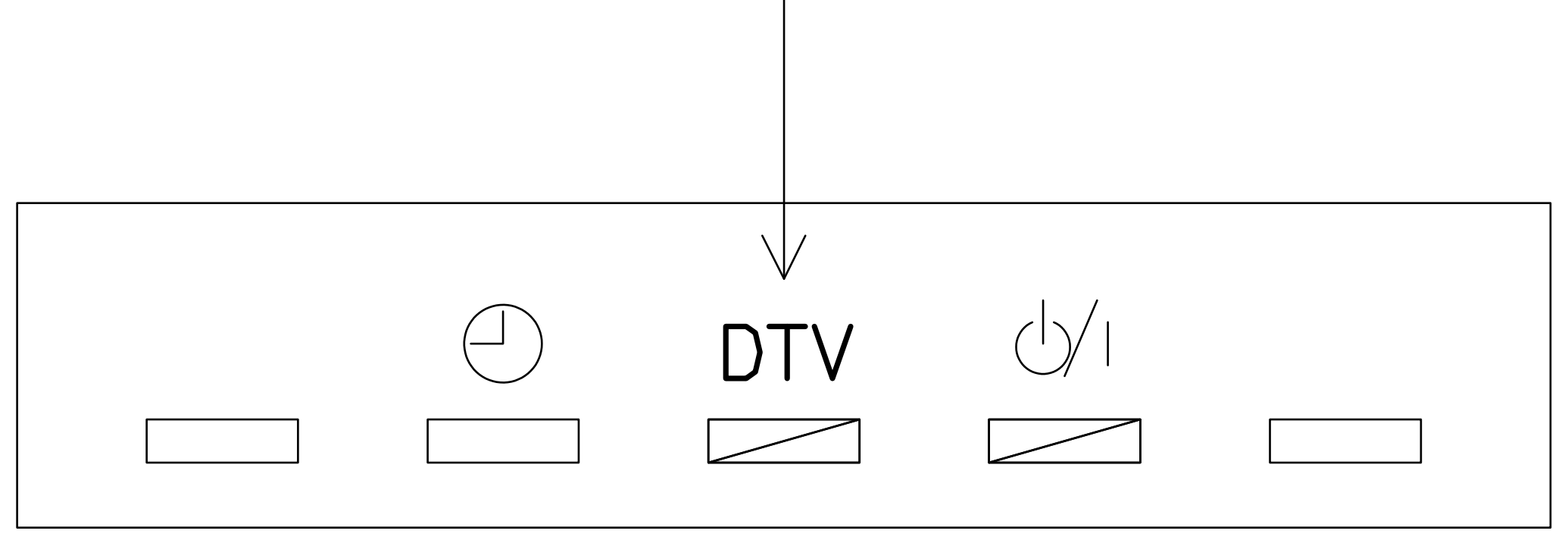

1
2
A
B
QA01
TSCB5-6RG7 (F, JZ)
MAIN_SYNC_DET
TV_SYNC_DET
9-A2
5V1
RA142 10k
RA141 10k
LED3
LED4
1-D8
For Chinese Digital-TV
LED1
LED1 (ON_TIMER)
GA46 OPEN
GA47 OPEN
GA48 UDZS TE-17 6.8B
RA151 100K
RA49 1k
RA50 100
1-C2
STOP_FAN
1-B8
SCL2
SDA2
RA52 100
RA53 100
RA54 100
RA55 100
2-D4
WOOFER_SW
LED0
LED0 (STANDBY)
4-D1
MUTE
4-E1
EXT_MUTE
2-B8
TV_MUTE
6-B2
AUDIO_SW1
4-C1
MSP_RESET
4-F4
DET_HP
RA57 100
RA58 100
RA59 100
RA60 100
RA62 100
RA63 100
RA64 10k
RA155 10k
RA154 10k
RA162 10k
RA159 10k
RA160 10k
RA158 10k
RA157 10k
GA56 UDZS TE-17 6.8B
(INT3/TC2) P15
(PDO5/PWM5/TC5) P16
(PDO6/PWM6/PPG6/TC6) P17
(SCL) P50
(SDA) P51
P52
P53
P54
P55
P30
P31
P32
P33
P34
P35
P36
P37
P13 (TC3/PDO3/PWM3)
P14 (TC4/PDO4/PWM4/PPG4)
P74 (AIN12)
P73 (AIN11)
P72 (AIN10)
P71 (AIN9)
P70 (AIN8)
P67 (AIN7/STOP3)
P66 (AIN6/STOP2)
P65 (AIN5/STOP1)
P64 (AIN4/STOP0)
P63 (AIN3)
P62 (AIN2)
P61 (AIN1)
P60 (AIN0)
AVDD
VAREF
P07 (INT2)
VSS
XIN
XOUT
TEST
VDD
(XTIN) P21
(XTOUT) P22
RESET
(STOP/INT5) P20
(INT0) P00
(BOOT/RXD0) P01
(TXD0) P02
(RXD1) P03
(SI1) P04
(SO1) P05
(SCK1) P06
P12 (PPG3)
P11 (INT4)
P10 (TC1)
P47
P46 (SCK0)
P45 (SO0)
P44 (SI0)
P43
P42 (TXD0)
P41 (RXD0)
P40
P77 (AIN15)
P76 (AIN14)
P75 (AIN13)
RA48 100
RA47 OPEN
RA46 OPEN
RA45 100
RA44 100
RA42 100
RA41 100
RA38 OPEN
RA37 OPEN
TA01 OPEN
TA02 OPEN
RA33 10k
7-B1
7-A1
MINI_MON1
MINI_MON2
RA32 10k
RA29 10k
RA28 10k
RA66 47K
RA65 OPEN
GA25 UDZS TE-17 6.8B
RA25 100
RA24 1k
RA23 1k
RA21 1k
RA20 10k
POWER_TV
POWER_SIG
1-C1
LED2 (POWER_ON)
LED2
RA119 OPEN
CA19 10V1u
RA17 100
with XA02
RA65:OPEN
RA66:47k
w/o XA02
RA65:47k
RA66:OPEN
RA116 10k
QA72 2SC5343UF-Y
RA117 10k
QA73 2SC5343UF-Y
RA118 10k
POWER_PROTECT
QA04 TC7SET08FU (TE85L, F)
CA20 25V0.1
RA84 10k
RA85 100
QA05 2SK2035 (TE85L, F)
RA88 100
CEC
RA86 27K
RA87 OPEN
RA89 SHORT
RA16 100
A_STBY
4-B4
RA115 1k
RA15 1k
SUB_TEST
11-B2
RA02 1M
RA01 330
XA01 TCR1085AM
XA02 VT-200-F
CA06 100
CA07 9p
RA09 1k
RA10 1k
RA11 100
RA12 100
RA13 100
RA14 100
RA126 1k
SUB_TXD
SUB_RXD
RA120 1k
POWER_KEY
1-E8
RMT_IN
GA37 SHORT
GA38 OPEN
RA03 OPEN
QA02 BD4742F-TR
VCC
Vou
NC
SUB
G
10V1u CA01
CA02 100p
GA17 UDZS TE-17 6.8B
GA18 UDZS TE-17 6.8B
GA09 UDZS TE-17 6.8B
5V2
RA82 47K
CA71 25V0.1
QA03 TC7SH126FU
INA
GND
OUTY
CA70 25V0.1
RA83 1k
RA81 1k
RMT_IN_S
CA22 B6.3V2.2u
3.3V
QA06 R1111N331B-TR-F
CE
VDD
VOUT
CA21 10V1u
1-A2
1-C1
1-D7
4-E1
11-B2
12-A1
12-A2
12-B1
12-B2
2-A7
3-A1
4-C1
4-F1
5-A1
7-A2
9-A1
10-A1
11-A1
11-A2
11-B1

U03A AV_TERMINAL [12] STANDBY MICRO with HDMI CEC (PE0398)

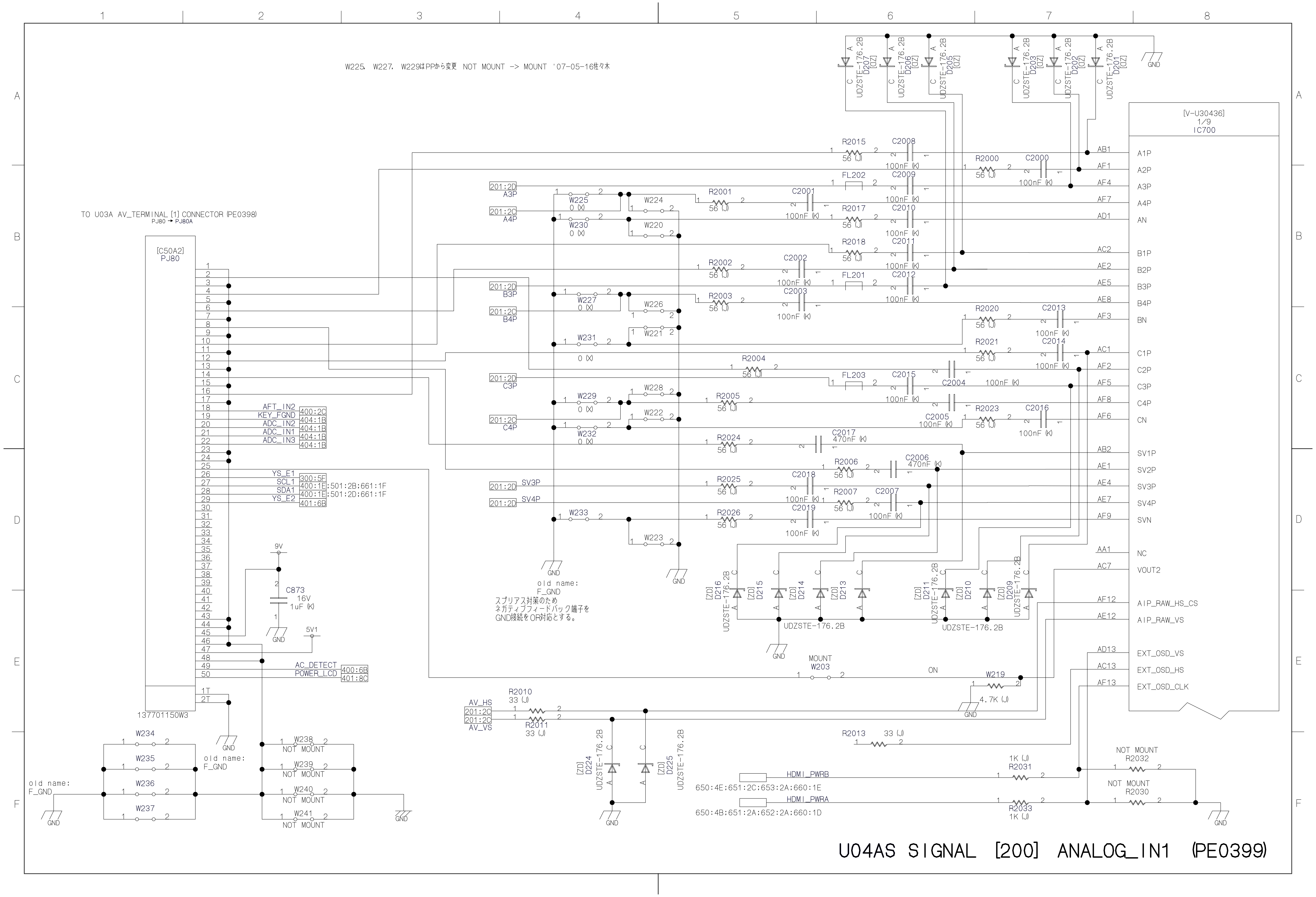

U03C
TO U03A AV_TERMINAL [1] CONNECTOR (PE0398)
PA01B → PA01A
GND
ADC_IN1
GND
ADC_IN2
POWER_KEY
PA01B
B5B-PH-K-S
VIDEO
SA06
KSMC632A
MENU
SA05
KSMC632A
VOL-DOWN
SA04
KSMC632A
VOL-UP
SA03
KSMC632A
CH-DOWN
SA02
KSMC632A
CH-UP
SA01
KSMC632A
POWER
SA07
KSMC632A
GB01
UDZS TE-17 6.8B
RB71
68k
RB72
22K
RB73
10k
RB74
SHORT
RB75
OPEN
RB77
10k
RB78
SHORT
RB79
SHORT
GB02
UDZS TE-17 6.8B
GB03
UDZS TE-17 6.8B


U03B
KB01
GP1UE281RKVF
Vout
Vcc
GND
RB09
47
DB23
KDS160-RTK/P
RB08
47
UDZS TE-17 6.8B
GB04
DB22
KDS160-RTK/P
CB03
25V0.1
CB02
5L10V220u
RB22
OPEN
CB04
25V0.1
CB05
5L10V220u
5V1
GND
RNT_IN
RMT_FGND
LED0 (POWER_STANDBY: Red)
LED1 (ON_TIMER: Green)
LED2 (POWER_ON: Green)
LED3 (Recording active: Yellow)
LED4 (Recording entry: Red)
PA49B
B9B-PH-K-S
TO U03A AV_TERMINAL [1] CONNECTOR (PE0398)
PA49B → PA49A
QB01
2SA1162-Y (TE85L, F)
RB11
270
RB13
100
DB01A
LED HOLDER
STANBY
(RED)
DB01
SPR54MVWFLMN
Power ON
(GREEN)
QB02
2SC5343UF-Y
RB12
180
RB14
100
For Chinese Digital-TV
DB05A
OPEN
STANBY
(RED)
DB05
OPEN
Power ON
(YELLOW)
DB25
OPEN
DB24
OPEN
QB04
OPEN
RB16
OPEN
RB18
OPEN
QB05
OPEN
RB17
OPEN
RB19
OPEN
DB04A
LED HOLDER
DB04
SPR54MVWFLMN
ON TIMER
(GREEN)
QB03
2SC5343UF-Y
RB10
180
RB15
3.3k

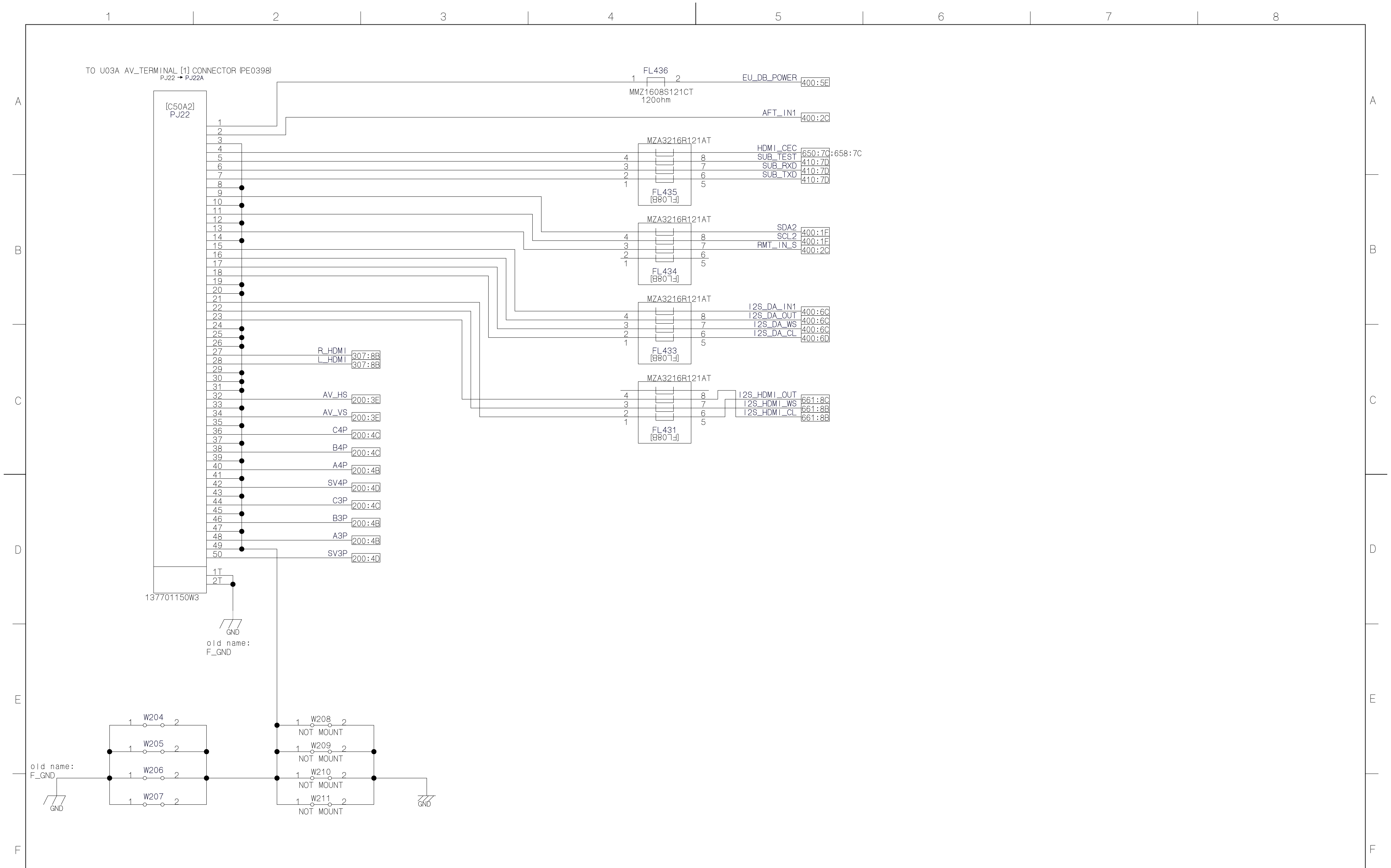



For Chinese Digital-TV
DTV

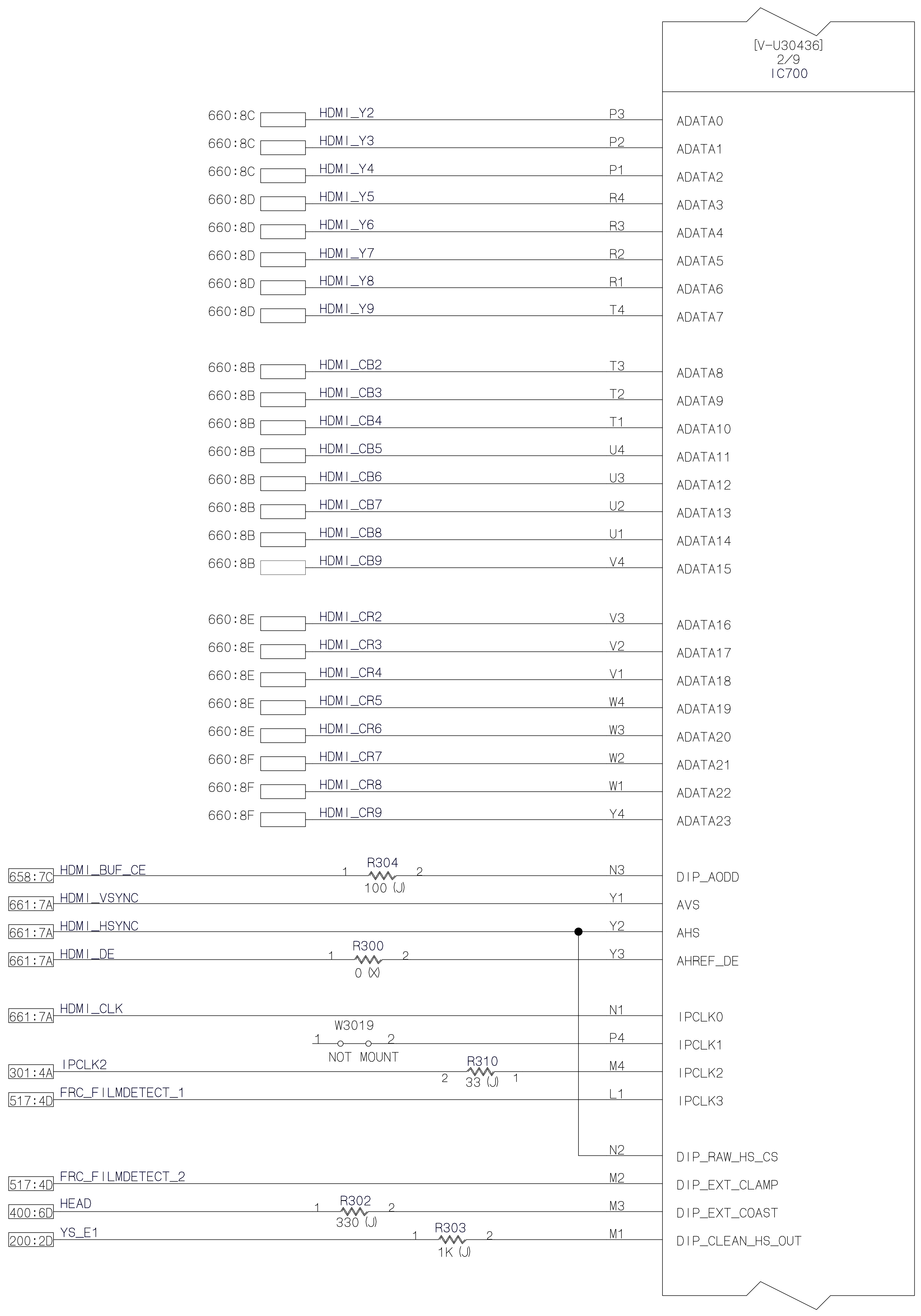
U03B/C LED/KEY [13] KEY & LED (PE0398)


W225, W227, W229はPPから変更 NOT MOUNT -> MOUNT '07-05-16佐々木
TO U03A AV_TERMINAL [1] CONNECTOR (PE0398)
PJ80 → PJ80A
[C50A2]
PJ80
137701150W3
AFT_IN2 400:2C
KEY_FGND 404:1B
ADC_IN2 404:1B
ADC_IN1 404:1B
ADC_IN3 404:1B
YS_E1 300:5F
SCL1 400:1E:501:2B:661:1F
SDA1 400:1E:501:2D:661:1F
YS_E2 401:6B
AC_DETECT 400:6B
POWER_LCD 401:8C
9V
C873
16V
1uF (K)
5V1
GND
old name:
F_GND
スプリアス対策のため
ネガティブフィードバック端子を
GND接続をOR対応とする。
201:2D A3P
201:2C A4P
201:2D B3P
201:2C B4P
201:2D C3P
201:2C C4P
201:2D SV3P
201:2D SV4P
W225 0 (K)
W230 0 (K)
W224
W220
W227 0 (K)
W226
W221
W231 0 (K)
W229 0 (K)
W228
W222
W232 0 (K)
W233
W223
R2015 56 (J)
R2001 56 (J)
R2017 56 (J)
R2018 56 (J)
R2002 56 (J)
R2003 56 (J)
R2000 56 (J)
R2020 56 (J)
R2021 56 (J)
R2004 56 (J)
R2005 56 (J)
R2023 56 (J)
R2024 56 (J)
R2006 56 (J)
R2025 56 (J)
R2007 56 (J)
R2026 56 (J)
FL202
FL201
FL203
C2008 100nF (K)
C2009 100nF (K)
C2010 100nF (K)
C2011 100nF (K)
C2012 100nF (K)
C2001 100nF (K)
C2002 100nF (K)
C2003 100nF (K)
C2000 100nF (K)
C2013 100nF (K)
C2014 100nF (K)
C2015 100nF (K)
C2004 100nF (K)
C2005 100nF (K)
C2016 100nF (K)
C2017 470nF (K)
C2006 470nF (K)
C2018 100nF (K)
C2007 100nF (K)
C2019 100nF (K)
D207 UDZSTE-176.2B [DZ]
D206 UDZSTE-176.2B [DZ]
D205 UDZSTE-176.2B [DZ]
D203 UDZSTE-176.2B [DZ]
D202 UDZSTE-176.2B [DZ]
D201 UDZSTE-176.2B [DZ]
D216 UDZSTE-176.2B [ZD]
D215 [ZD]
D214 [ZD]
D213 [ZD]
UDZSTE-176.2B
D211 UDZSTE-176.2B [ZD]
D210 [ZD]
D209 UDZSTE-176.2B [ZD]
UDZSTE-176.2B
[V-U30436]
1/9
IC700
AB1 A1P
AF1 A2P
AF4 A3P
AF7 A4P
AD1 AN
AC2 B1P
AE2 B2P
AE5 B3P
AE8 B4P
AF3 BN
AC1 C1P
AF2 C2P
AF5 C3P
AF8 C4P
AF6 CN
AB2 SV1P
AE1 SV2P
AE4 SV3P
AE7 SV4P
AF9 SVN
AA1 NC
AC7 VOUT2
AF12 AIP_RAW_HS_CS
AE12 AIP_RAW_VS
AD13 EXT_OSD_VS
AC13 EXT_OSD_HS
AF13 EXT_OSD_CLK
MOUNT
W203
ON
W219
4.7K (J)
AV_HS
201:2C
201:2C
AV_VS
R2010 33 (J)
R2011 33 (J)
D224 UDZSTE-176.2B [ZD]
D225 UDZSTE-176.2B [ZD]
R2013 33 (J)
HDMI_PWRB
650:4E:651:2C:653:2A:660:1E
HDMI_PWRA
650:4B:651:2A:652:2A:660:1D
R2031 1K (J)
R2033 1K (J)
NOT MOUNT R2032
NOT MOUNT R2030
W234
W235
W236
W237
W238 NOT MOUNT
W239 NOT MOUNT
W240 NOT MOUNT
W241 NOT MOUNT
old name:
F_GND
1 2 3 4 5 6 7 8
A B C D E F

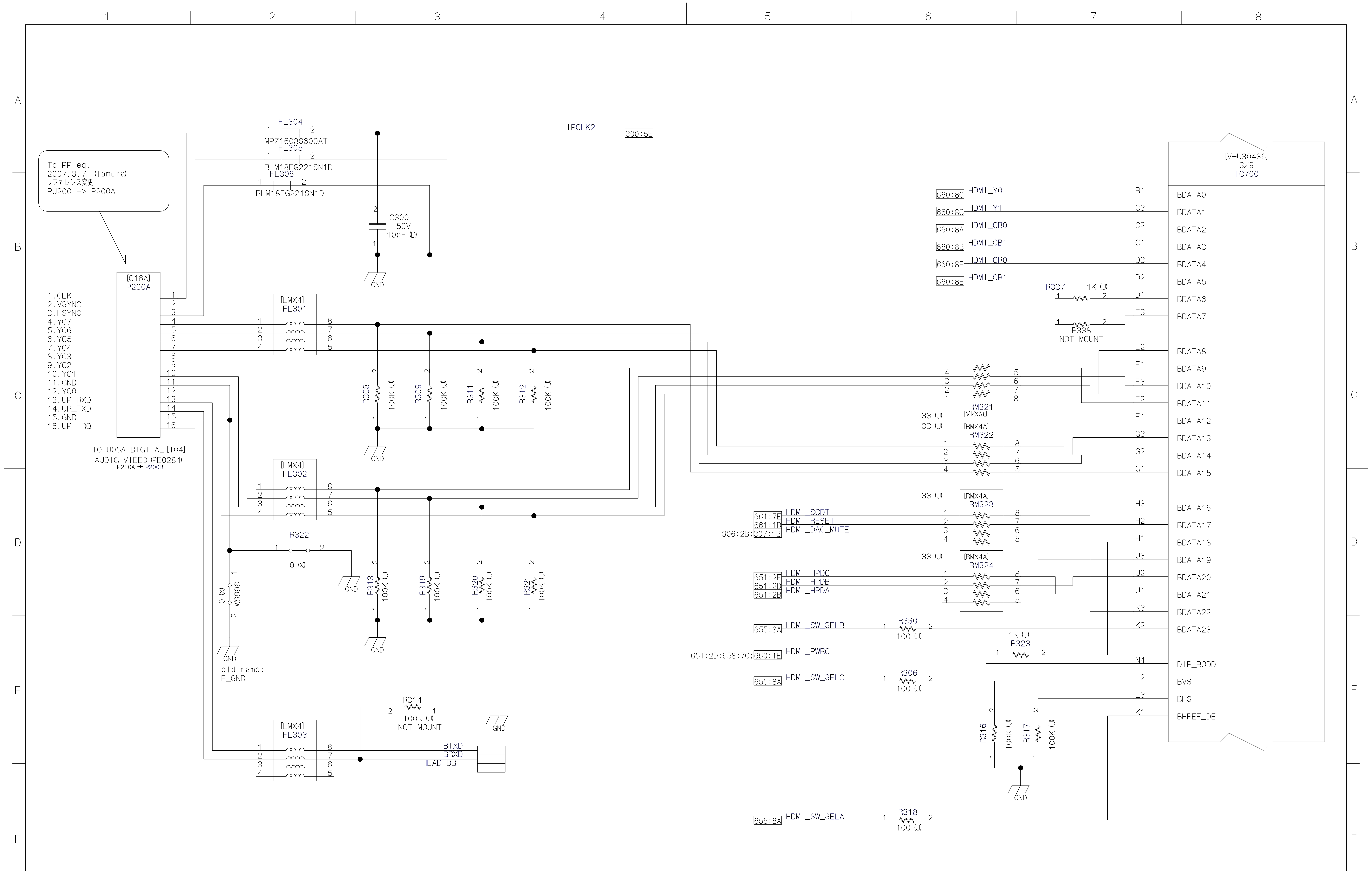
U04AS SIGNAL [200] ANALOG_IN1 (PE0399)


TO U03A AV_TERMINAL [1] CONNECTOR (PE0398)
PJ22 → PJ22A
[C50A2]
PJ22
137701150W3
FL436
MMZ1608S121CT
120ohm
EU_DB_POWER 400:5E
AFT_IN1 400:2C
MZA3216R121AT
FL435
[FL08B]
HDMI_CEC 650:7C;658:7C
SUB_TEST 410:7D
SUB_RXD 410:7D
SUB_TXD 410:7D
MZA3216R121AT
FL434
[FL08B]
SDA2 400:1F
SCL2 400:1F
RMT_IN_S 400:2C
MZA3216R121AT
FL433
[FL08B]
I2S_DA_IN1 400:6C
I2S_DA_OUT 400:6C
I2S_DA_WS 400:6C
I2S_DA_CL 400:6D
MZA3216R121AT
FL431
[FL08B]
I2S_HDMI_OUT 661:8C
I2S_HDMI_WS 661:8B
I2S_HDMI_CL 661:8B
R_HDMI 307:8B
L_HDMI 307:8B
AV_HS 200:3E
AV_VS 200:3E
C4P 200:4C
B4P 200:4C
A4P 200:4B
SV4P 200:4D
C3P 200:4C
B3P 200:4B
A3P 200:4B
SV3P 200:4D
1T
2T
GND
old name:
F_GND
W204
W205
W206
W207
W208
NOT MOUNT
W209
NOT MOUNT
W210
NOT MOUNT
W211
NOT MOUNT
old name:
F_GND
GND
GND

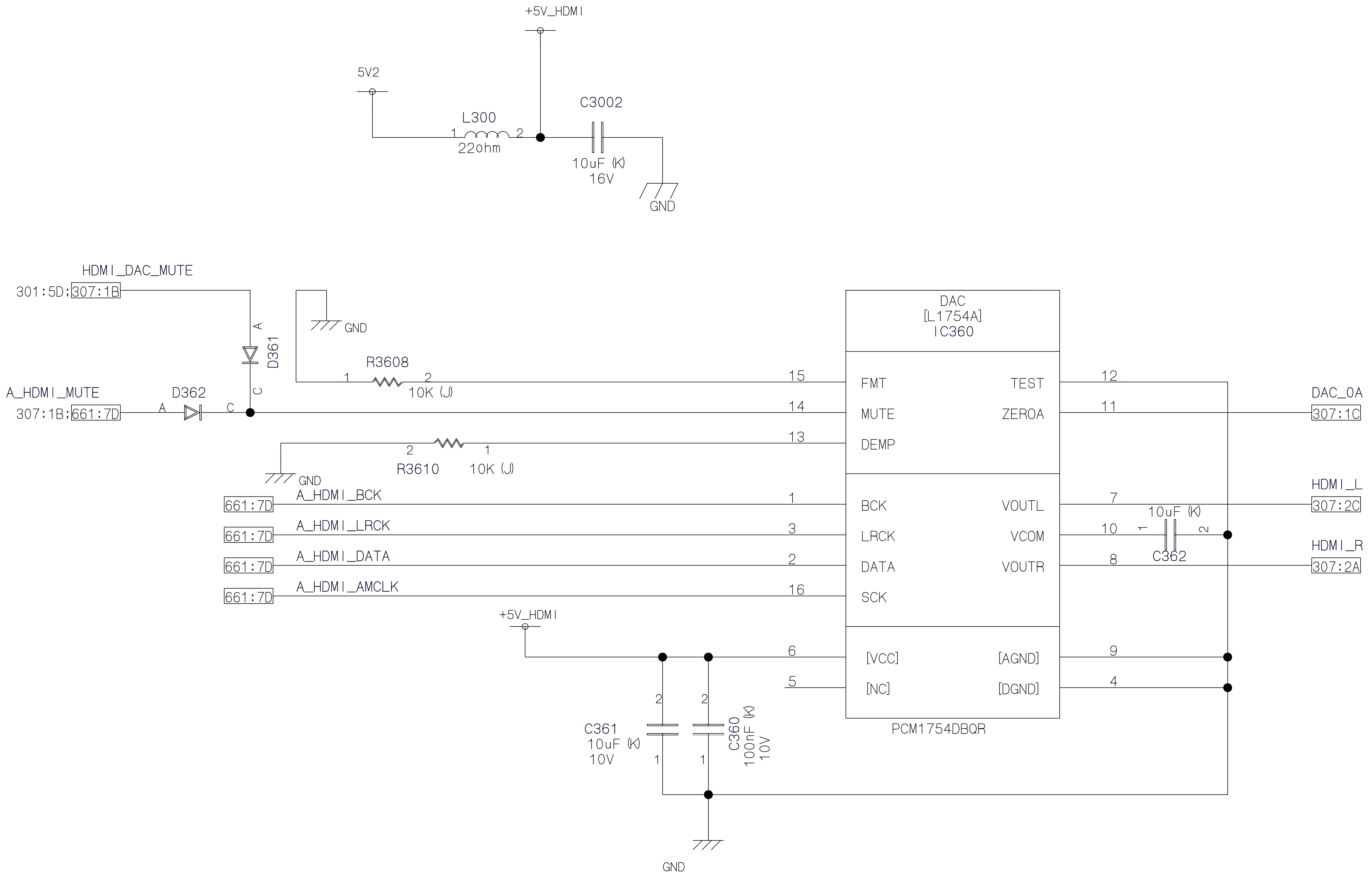
U04AS SIGNAL [201] ANALOG_IN2 (PE0399)


[V-U30436]
2/9
IC700
660:8C HDMI_Y2 P3 ADATA0
660:8C HDMI_Y3 P2 ADATA1
660:8C HDMI_Y4 P1 ADATA2
660:8D HDMI_Y5 R4 ADATA3
660:8D HDMI_Y6 R3 ADATA4
660:8D HDMI_Y7 R2 ADATA5
660:8D HDMI_Y8 R1 ADATA6
660:8D HDMI_Y9 T4 ADATA7
660:8B HDMI_CB2 T3 ADATA8
660:8B HDMI_CB3 T2 ADATA9
660:8B HDMI_CB4 T1 ADATA10
660:8B HDMI_CB5 U4 ADATA11
660:8B HDMI_CB6 U3 ADATA12
660:8B HDMI_CB7 U2 ADATA13
660:8B HDMI_CB8 U1 ADATA14
660:8B HDMI_CB9 V4 ADATA15
660:8E HDMI_CR2 V3 ADATA16
660:8E HDMI_CR3 V2 ADATA17
660:8E HDMI_CR4 V1 ADATA18
660:8E HDMI_CR5 W4 ADATA19
660:8E HDMI_CR6 W3 ADATA20
660:8F HDMI_CR7 W2 ADATA21
660:8F HDMI_CR8 W1 ADATA22
660:8F HDMI_CR9 Y4 ADATA23
658:7C HDMI_BUF_CE
R304
1 2
100 (J)
N3 DIP_AODD
661:7A HDMI_VSYNC Y1 AVS
661:7A HDMI_HSYNC Y2 AHS
661:7A HDMI_DE
R300
1 2
0 (X)
Y3 AHREF_DE
661:7A HDMI_CLK N1 IPCLK0
W3019
1 2
NOT MOUNT
P4 IPCLK1
301:4A IPCLK2
R310
2 1
33 (J)
M4 IPCLK2
517:4D FRC_FILMDETECT_1 L1 IPCLK3
N2 DIP_RAW_HS_CS
517:4D FRC_FILMDETECT_2 M2 DIP_EXT_CLAMP
400:6D HEAD
R302
1 2
330 (J)
M3 DIP_EXT_COAST
200:2D YS_E1
R303
1 2
1K (J)
M1 DIP_CLEAN_HS_OUT

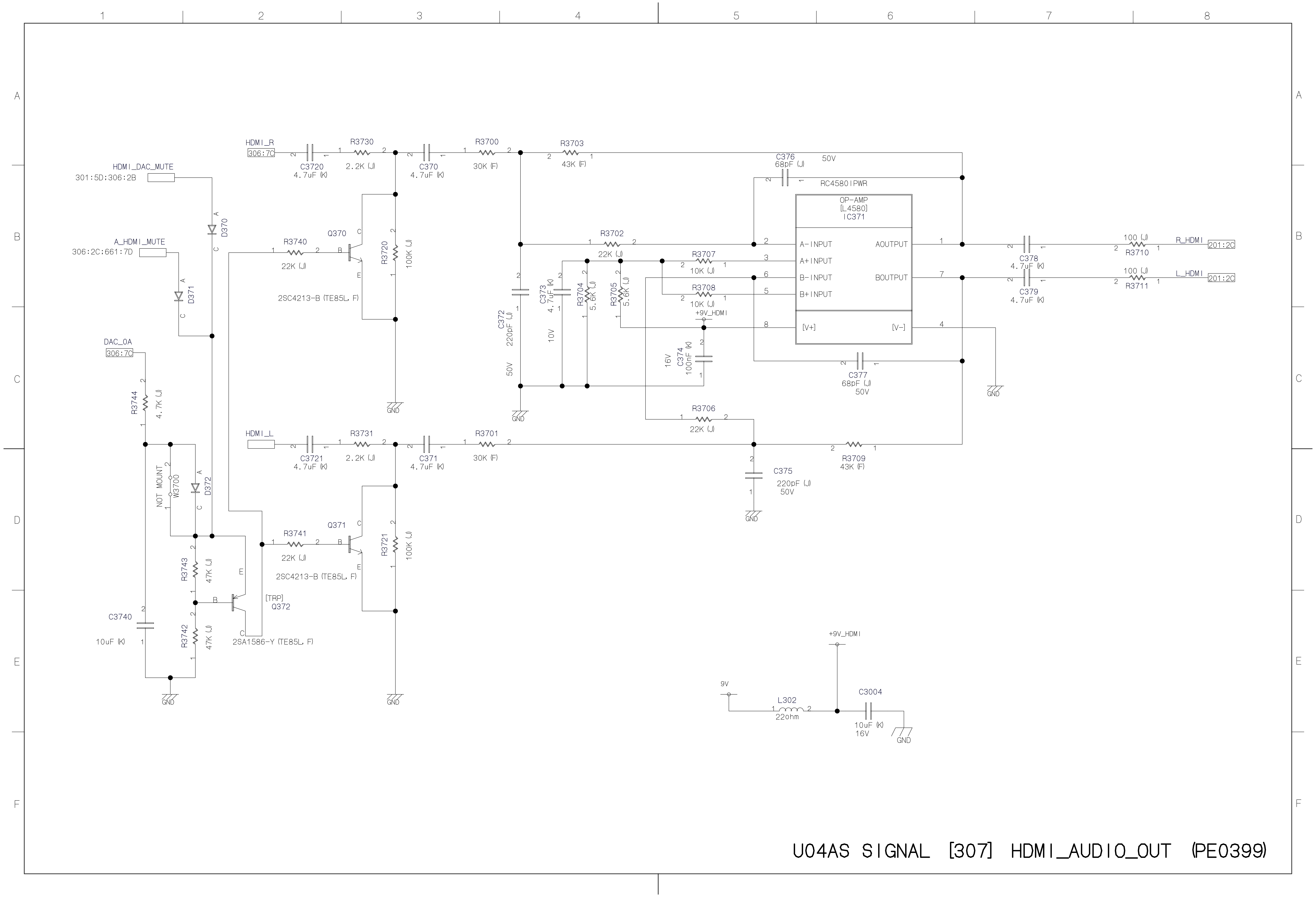
U04AS SIGNAL [300] DIGITAL IN_A (PE0399)


1
2
3
4
5
6
7
8
A
B
C
D
E
F
To PP eq.
2007.3.7 (Tamura)
リファレンス変更
PJ200 -> P200A
[C16A]
P200A
1.CLK
2.VSYNC
3.HSYNC
4.YC7
5.YC6
6.YC5
7.YC4
8.YC3
9.YC2
10.YC1
11.GND
12.YC0
13.UP_RXD
14.UP_TXD
15.GND
16.UP_IRQ
TO U05A DIGITAL [104]
AUDIO, VIDEO (PE0284)
P200A → P200B
FL304
MPZ1608S600AT
FL305
BLM18EG221SN1D
FL306
BLM18EG221SN1D
IPCLK2
300:5E
C300
50V
10pF (D)
GND
[LMX4]
FL301
[LMX4]
FL302
[LMX4]
FL303
R308
R309
R311
R312
R313
R319
R320
R321
100K (J)
R322
0 (X)
W9996
0 (X)
old name:
F_GND
R314
100K (J)
NOT MOUNT
BTXD
BRXD
HEAD_DB
[V-U30436]
3/9
IC700
660:8C HDMI_Y0
660:8C HDMI_Y1
660:8A HDMI_CB0
660:8B HDMI_CB1
660:8E HDMI_CR0
660:8E HDMI_CR1
R337
1K (J)
R338
NOT MOUNT
B1 BDATA0
C3 BDATA1
C2 BDATA2
C1 BDATA3
D3 BDATA4
D2 BDATA5
D1 BDATA6
E3 BDATA7
E2 BDATA8
E1 BDATA9
F3 BDATA10
F2 BDATA11
F1 BDATA12
G3 BDATA13
G2 BDATA14
G1 BDATA15
H3 BDATA16
H2 BDATA17
H1 BDATA18
J3 BDATA19
J2 BDATA20
J1 BDATA21
K3 BDATA22
K2 BDATA23
N4 DIP_BODD
L2 BVS
L3 BHS
K1 BHREF_DE
RM321
[RMX4A]
33 (J)
33 (J)
[RMX4A]
RM322
33 (J)
[RMX4A]
RM323
661:7E HDMI_SCDT
661:1D HDMI_RESET
306:2B;307:1B HDMI_DAC_MUTE
33 (J)
[RMX4A]
RM324
651:2E HDMI_HPDC
651:2D HDMI_HPDB
651:2B HDMI_HPDA
655:8A HDMI_SW_SELB
R330
100 (J)
651:2D;658:7C;660:1E HDMI_PWRC
1K (J)
R323
655:8A HDMI_SW_SELC
R306
100 (J)
R316
100K (J)
R317
100K (J)
655:8A HDMI_SW_SELA
R318
100 (J)

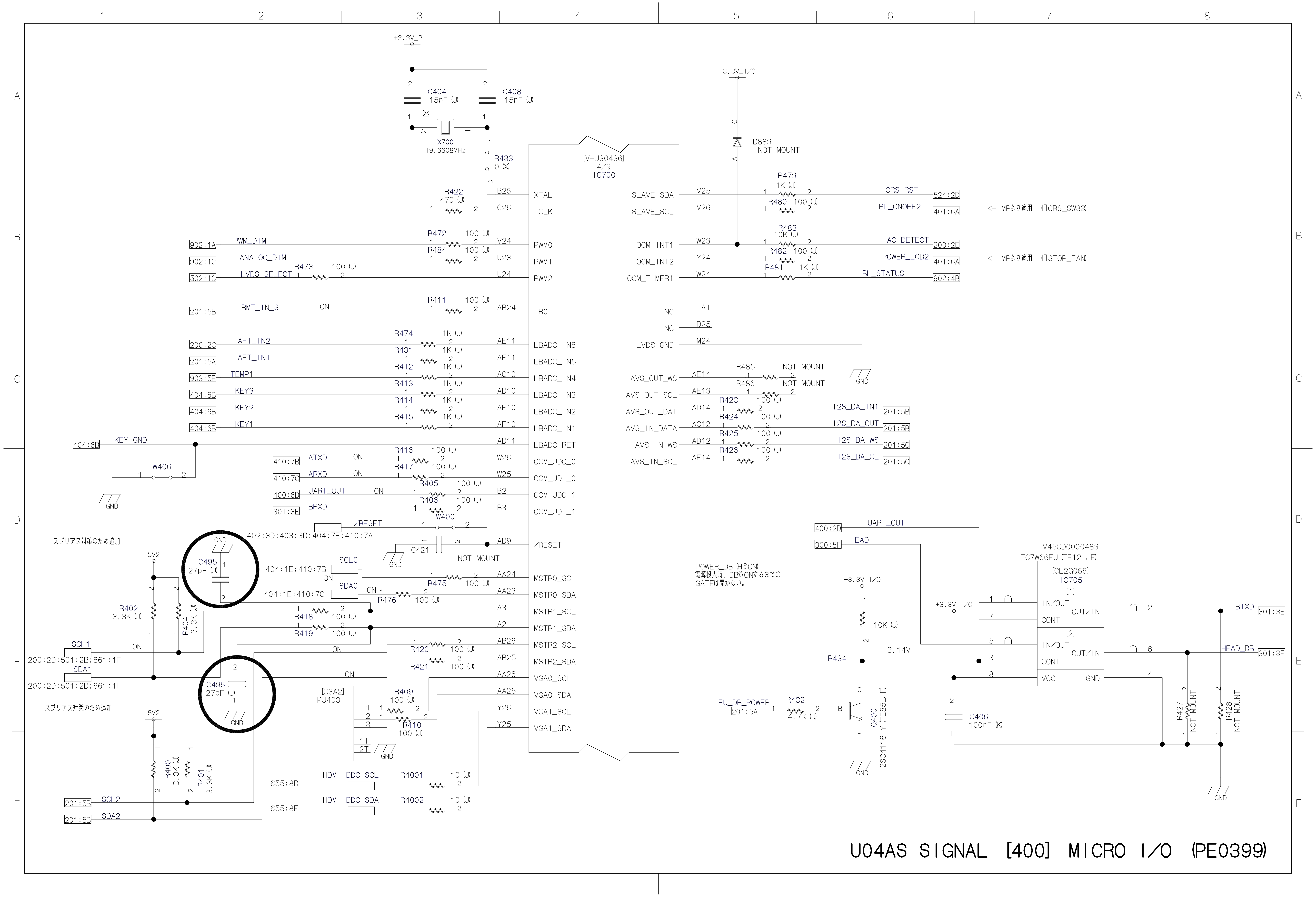
U04AS SIGNAL [301] DIGITAL IN_B (PE0399)


+5V_HDMI
5V2
L300
1
2
22ohm
C3002
10uF (K)
16V
GND
HDMI_DAC_MUTE
301:5D:307:1B
A
D361
C
GND
R3608
1
2
10K (J)
A_HDMI_MUTE
307:1B:661:7D
D362
A
C
R3610
2
1
10K (J)
GND
A_HDMI_BCK
661:7D
A_HDMI_LRCK
661:7D
A_HDMI_DATA
661:7D
A_HDMI_AMCLK
661:7D
+5V_HDMI
DAC
[L1754A]
IC360
15
FMT
14
MUTE
13
DEMP
1
BCK
3
LRCK
2
DATA
16
SCK
6
[VCC]
5
[NC]
12
TEST
11
ZEROA
7
VOUTL
10
VCOM
8
VOUTR
9
[AGND]
4
[DGND]
PCM1754DBQR
10uF (K)
1
2
C362
DAC_0A
307:1C
HDMI_L
307:2C
HDMI_R
307:2A
C361
10uF (K)
10V
2
1
C360
100nF (K)
10V
2
1
GND

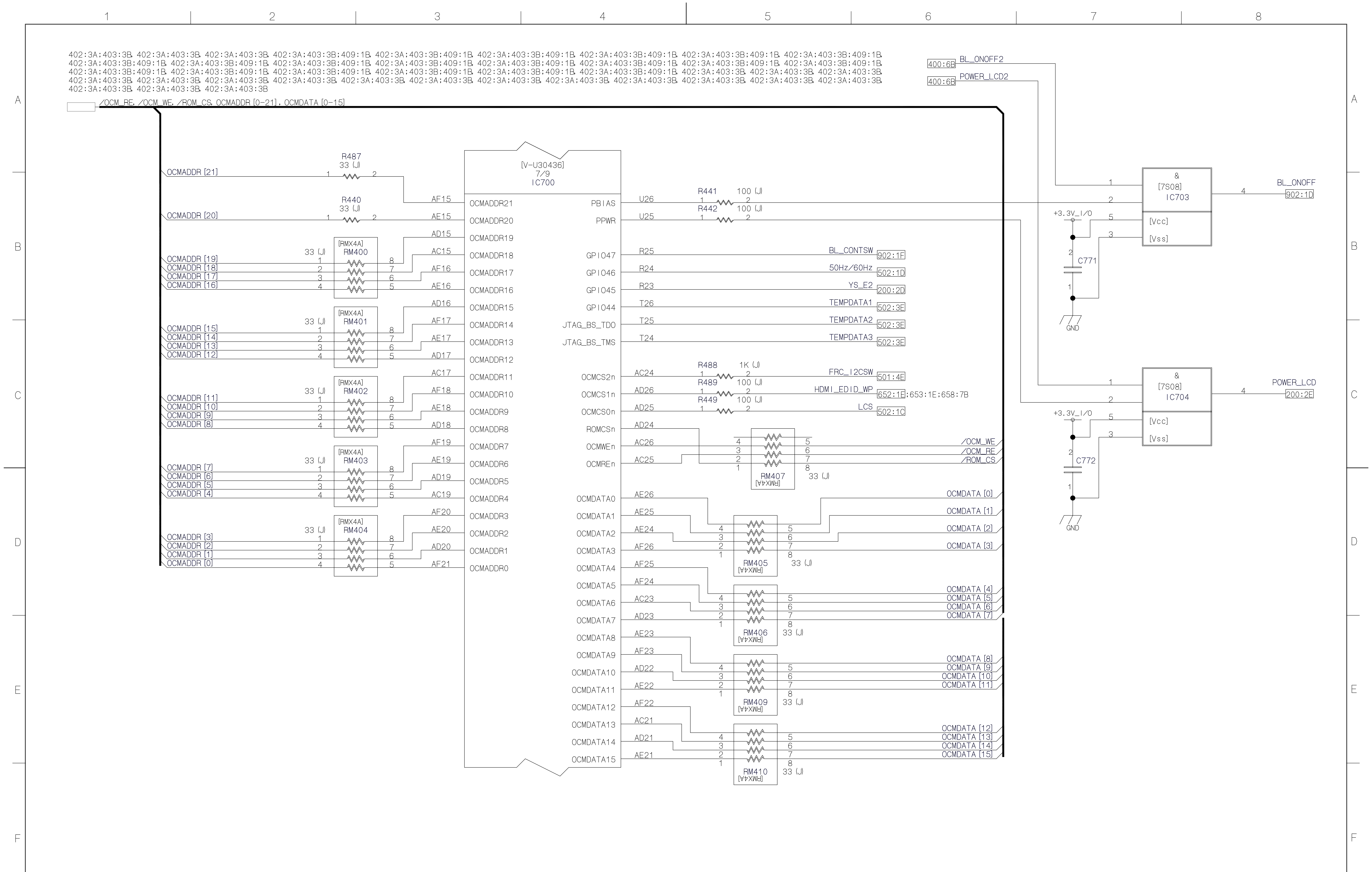
U04AS SIGNAL [306] HDMI_AUDIO_DAC (PE0399)


1
2
3
4
5
6
7
8
A
B
C
D
E
F
HDMI_R
306:7C
C3720
4.7uF (K)
R3730
2.2K (J)
C370
4.7uF (K)
R3700
30K (F)
R3703
43K (F)
C376
68pF (J)
50V
RC4580IPWR
OP-AMP
[L4580]
IC371
A-INPUT
A+INPUT
B-INPUT
B+INPUT
[V+]
[V-]
AOUTPUT
BOUTPUT
R3702
22K (J)
R3707
10K (J)
R3708
10K (J)
+9V_HDMI
C372
220pF (J)
50V
C373
4.7uF (K)
10V
R3704
5.6K (J)
R3705
5.6K (J)
C374
100nF (K)
16V
C377
68pF (J)
50V
C378
4.7uF (K)
C379
4.7uF (K)
100 (J)
R3710
100 (J)
R3711
R_HDMI
201:2C
L_HDMI
201:2C
HDMI_DAC_MUTE
301:5D:306:2B
A_HDMI_MUTE
306:2C:661:7D
D370
D371
D372
A
C
R3740
22K (J)
Q370
2SC4213-B (TE85L, F)
R3720
100K (J)
B
C
E
GND
DAC_0A
306:7C
R3744
4.7K (J)
NOT MOUNT
W3700
R3743
47K (J)
R3742
47K (J)
[TRP]
Q372
2SA1586-Y (TE85L, F)
C3740
10uF (K)
HDMI_L
C3721
4.7uF (K)
R3731
2.2K (J)
C371
4.7uF (K)
R3701
30K (F)
R3741
22K (J)
Q371
2SC4213-B (TE85L, F)
R3721
100K (J)
R3706
22K (J)
R3709
43K (F)
C375
220pF (J)
50V
9V
L302
22ohm
C3004
10uF (K)
16V

U04AS SIGNAL [307] HDMI_AUDIO_OUT (PE0399)

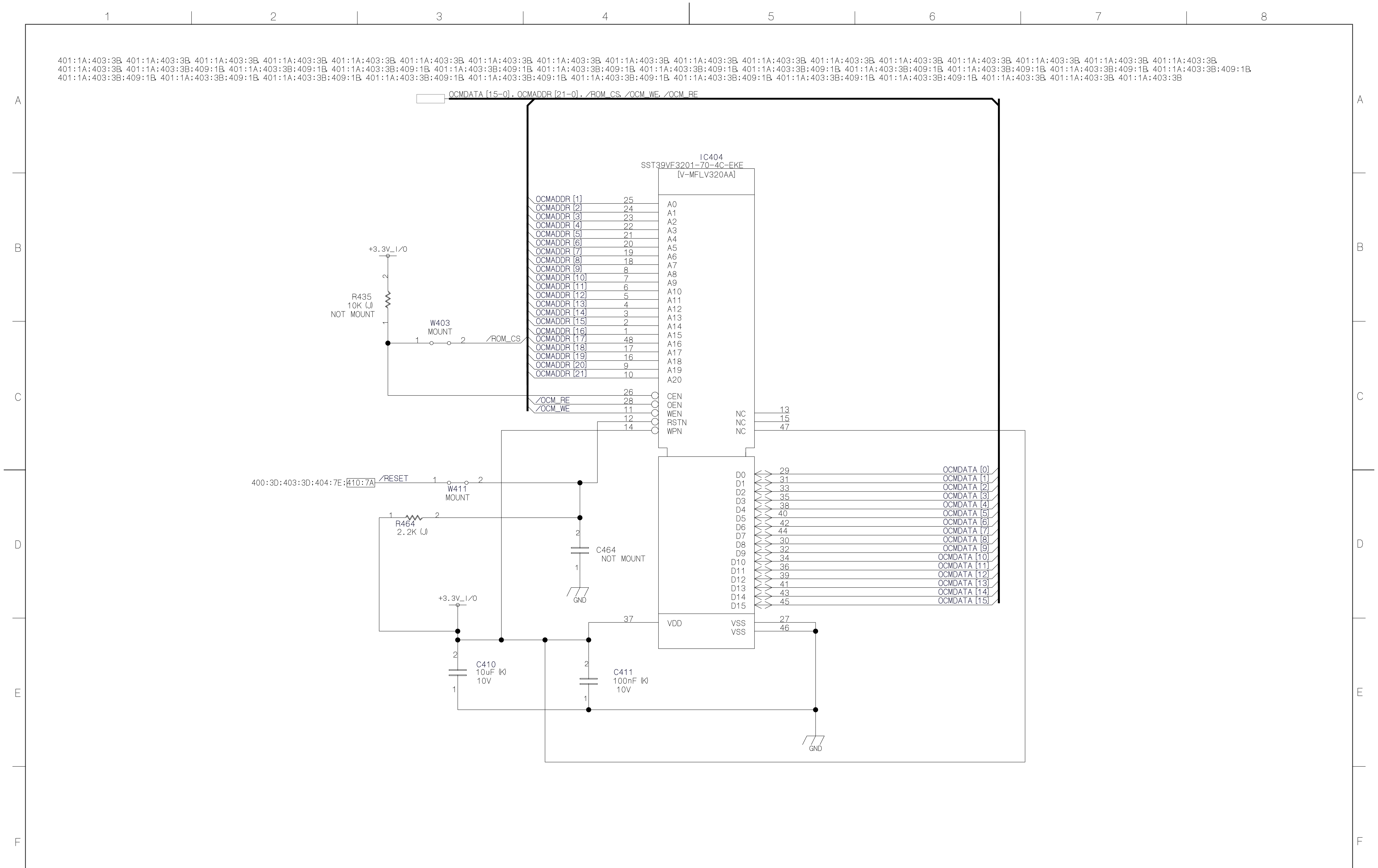

+3.3V_PLL
C404
15pF (J)
C408
15pF (J)
X700
19.6608MHz
R433
0 (X)
R422
470 (J)
[V-U30436]
4/9
IC700
XTAL
TCLK
PWM0
PWM1
PWM2
IR0
LBADC_IN6
LBADC_IN5
LBADC_IN4
LBADC_IN3
LBADC_IN2
LBADC_IN1
LBADC_RET
OCM_UDO_0
OCM_UDI_0
OCM_UDO_1
OCM_UDI_1
/RESET
MSTR0_SCL
MSTR0_SDA
MSTR1_SCL
MSTR1_SDA
MSTR2_SCL
MSTR2_SDA
VGA0_SCL
VGA0_SDA
VGA1_SCL
VGA1_SDA
SLAVE_SDA
SLAVE_SCL
OCM_INT1
OCM_INT2
OCM_TIMER1
NC
LVDS_GND
AVS_OUT_WS
AVS_OUT_SCL
AVS_OUT_DAT
AVS_IN_DATA
AVS_IN_WS
AVS_IN_SCL
B26
C26
V24
U23
U24
AB24
AE11
AF11
AC10
AD10
AE10
AF10
AD11
W26
W25
B2
B3
AD9
AA24
AA23
A3
A2
AB26
AB25
AA26
AA25
Y26
Y25
V25
V26
W23
Y24
W24
A1
D25
M24
AE14
AE13
AD14
AC12
AD12
AF14
902:1A PWM_DIM
902:1C ANALOG_DIM
502:1C LVDS_SELECT
201:5B RMT_IN_S
ON
200:2C AFT_IN2
201:5A AFT_IN1
903:5F TEMP1
404:6B KEY3
404:6B KEY2
404:6B KEY1
404:6B KEY_GND
W406
GND
410:7B ATXD
410:7C ARXD
400:6D UART_OUT
301:3E BRXD
R472 100 (J)
R484 100 (J)
R473 100 (J)
R411 100 (J)
R474 1K (J)
R431 1K (J)
R412 1K (J)
R413 1K (J)
R414 1K (J)
R415 1K (J)
R416 100 (J)
R417 100 (J)
R405 100 (J)
R406 100 (J)
W400
/RESET
402:3D:403:3D:404:7E:410:7A
C421
NOT MOUNT
スプリアス対策のため追加
C495
27pF (J)
5V2
R402
3.3K (J)
R404
3.3K (J)
404:1E:410:7B SCL0
404:1E:410:7C SDA0
R475 100 (J)
R476 100 (J)
R418 100 (J)
R419 100 (J)
SCL1
200:2D:501:2B:661:1F
SDA1
200:2D:501:2D:661:1F
R420 100 (J)
R421 100 (J)
C496
27pF (J)
[C3A2]
PJ403
R409 100 (J)
R410 100 (J)
1T
2T
R400
3.3K (J)
R401
3.3K (J)
201:5B SCL2
201:5B SDA2
655:8D
655:8E
HDMI_DDC_SCL
HDMI_DDC_SDA
R4001 10 (J)
R4002 10 (J)
+3.3V_I/O
D889
NOT MOUNT
R479 1K (J)
R480 100 (J)
R483 10K (J)
R482 100 (J)
R481 1K (J)
CRS_RST 524:2D
BL_ONOFF2 401:6A
AC_DETECT 200:2E
POWER_LCD2 401:6A
BL_STATUS 902:4B
<- MPより適用　(旧CRS_SW33)
<- MPより適用　(旧STOP_FAN)
R485 NOT MOUNT
R486 NOT MOUNT
R423 100 (J)
R424 100 (J)
R425 100 (J)
R426 100 (J)
I2S_DA_IN1 201:5B
I2S_DA_OUT 201:5B
I2S_DA_WS 201:5C
I2S_DA_CL 201:5C
400:2D UART_OUT
300:5F HEAD
POWER_DB (HでON)
電源投入時、DBがONするまでは
GATEは開かない。
+3.3V_I/O
10K (J)
R434
3.14V
EU_DB_POWER 201:5A
R432
4.7K (J)
Q400
2SC4116-Y (TE85L, F)
C406
100nF (K)
V45GD0000483
TC7W66FU (TE12L, F)
[CL2G066]
IC705
[1]
IN/OUT
CONT
OUT/IN
[2]
IN/OUT
CONT
OUT/IN
VCC
GND
BTXD 301:3E
HEAD_DB 301:3F
R427 NOT MOUNT
R428 NOT MOUNT
1 2 3 4 5 6 7 8
A B C D E F

U04AS SIGNAL [400] MICRO I/O (PE0399)

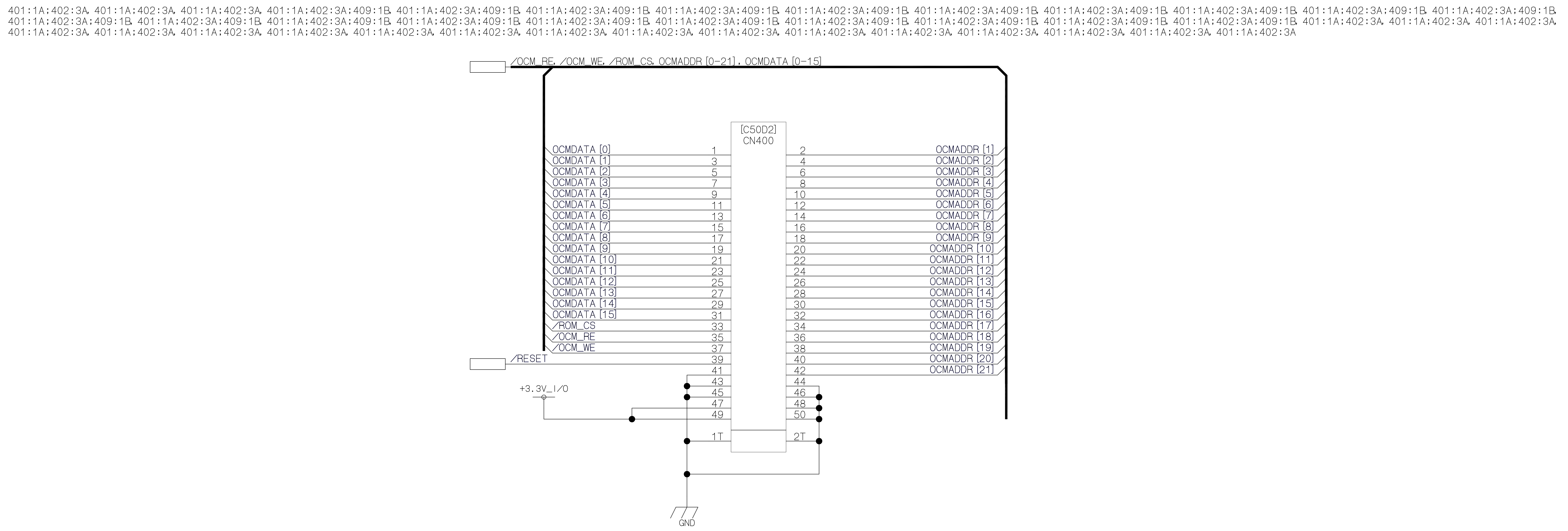


402:3A:403:3B 402:3A:403:3B 402:3A:403:3B 402:3A:403:3B:409:1B 402:3A:403:3B:409:1B 402:3A:403:3B:409:1B 402:3A:403:3B:409:1B 402:3A:403:3B:409:1B 402:3A:403:3B:409:1B
402:3A:403:3B:409:1B 402:3A:403:3B:409:1B 402:3A:403:3B:409:1B 402:3A:403:3B:409:1B 402:3A:403:3B:409:1B 402:3A:403:3B:409:1B 402:3A:403:3B:409:1B 402:3A:403:3B:409:1B
402:3A:403:3B:409:1B 402:3A:403:3B:409:1B 402:3A:403:3B:409:1B 402:3A:403:3B:409:1B 402:3A:403:3B:409:1B 402:3A:403:3B:409:1B 402:3A:403:3B 402:3A:403:3B 402:3A:403:3B
402:3A:403:3B 402:3A:403:3B 402:3A:403:3B 402:3A:403:3B 402:3A:403:3B 402:3A:403:3B 402:3A:403:3B 402:3A:403:3B 402:3A:403:3B 402:3A:403:3B 402:3A:403:3B 402:3A:403:3B 402:3A:403:3B
402:3A:403:3B 402:3A:403:3B 402:3A:403:3B
/OCM_RE, /OCM_WE, /ROM_CS, OCMADDR [0-21], OCMDATA [0-15]
[V-U30436]
7/9
IC700
R487 33 (J)
R440 33 (J)
OCMADDR [21]
OCMADDR [20]
RM400 [RMX4A] 33 (J)
OCMADDR [19]
OCMADDR [18]
OCMADDR [17]
OCMADDR [16]
RM401 [RMX4A] 33 (J)
OCMADDR [15]
OCMADDR [14]
OCMADDR [13]
OCMADDR [12]
RM402 [RMX4A] 33 (J)
OCMADDR [11]
OCMADDR [10]
OCMADDR [9]
OCMADDR [8]
RM403 [RMX4A] 33 (J)
OCMADDR [7]
OCMADDR [6]
OCMADDR [5]
OCMADDR [4]
RM404 [RMX4A] 33 (J)
OCMADDR [3]
OCMADDR [2]
OCMADDR [1]
OCMADDR [0]
AF15 OCMADDR21
AE15 OCMADDR20
AD15 OCMADDR19
AC15 OCMADDR18
AF16 OCMADDR17
AE16 OCMADDR16
AD16 OCMADDR15
AF17 OCMADDR14
AE17 OCMADDR13
AD17 OCMADDR12
AC17 OCMADDR11
AF18 OCMADDR10
AE18 OCMADDR9
AD18 OCMADDR8
AF19 OCMADDR7
AE19 OCMADDR6
AD19 OCMADDR5
AC19 OCMADDR4
AF20 OCMADDR3
AE20 OCMADDR2
AD20 OCMADDR1
AF21 OCMADDR0
PBIAS U26
PPWR U25
GPIO47 R25
GPIO46 R24
GPIO45 R23
GPIO44 T26
JTAG_BS_TDO T25
JTAG_BS_TMS T24
OCMCS2n AC24
OCMCS1n AD26
OCMCS0n AD25
ROMCSn AD24
OCMWEn AC26
OCMREn AC25
OCMDATA0 AE26
OCMDATA1 AE25
OCMDATA2 AE24
OCMDATA3 AF26
OCMDATA4 AF25
OCMDATA5 AF24
OCMDATA6 AC23
OCMDATA7 AD23
OCMDATA8 AE23
OCMDATA9 AF23
OCMDATA10 AD22
OCMDATA11 AE22
OCMDATA12 AF22
OCMDATA13 AC21
OCMDATA14 AD21
OCMDATA15 AE21
R441 100 (J)
R442 100 (J)
BL_CONTSW 902:1F
50Hz/60Hz 502:1D
YS_E2 200:2D
TEMPDATA1 502:3E
TEMPDATA2 502:3E
TEMPDATA3 502:3E
R488 1K (J)
R489 100 (J)
R449 100 (J)
FRC_I2CSW 501:4E
HDMI_EDID_WP 652:1E:653:1E:658:7B
LCS 502:1C
RM407 [RMX4A] 33 (J)
/OCM_WE
/OCM_RE
/ROM_CS
RM405 [RMX4A] 33 (J)
OCMDATA [0]
OCMDATA [1]
OCMDATA [2]
OCMDATA [3]
RM406 [RMX4A] 33 (J)
OCMDATA [4]
OCMDATA [5]
OCMDATA [6]
OCMDATA [7]
RM409 [RMX4A] 33 (J)
OCMDATA [8]
OCMDATA [9]
OCMDATA [10]
OCMDATA [11]
RM410 [RMX4A] 33 (J)
OCMDATA [12]
OCMDATA [13]
OCMDATA [14]
OCMDATA [15]
400:6B BL_ONOFF2
400:6B POWER_LCD2
& [7S08] IC703
[Vcc]
[Vss]
+3.3V_I/O
C771
GND
BL_ONOFF 902:1D
& [7S08] IC704
C772
POWER_LCD 200:2E
1 2 3 4 5 6 7 8
A B C D E F

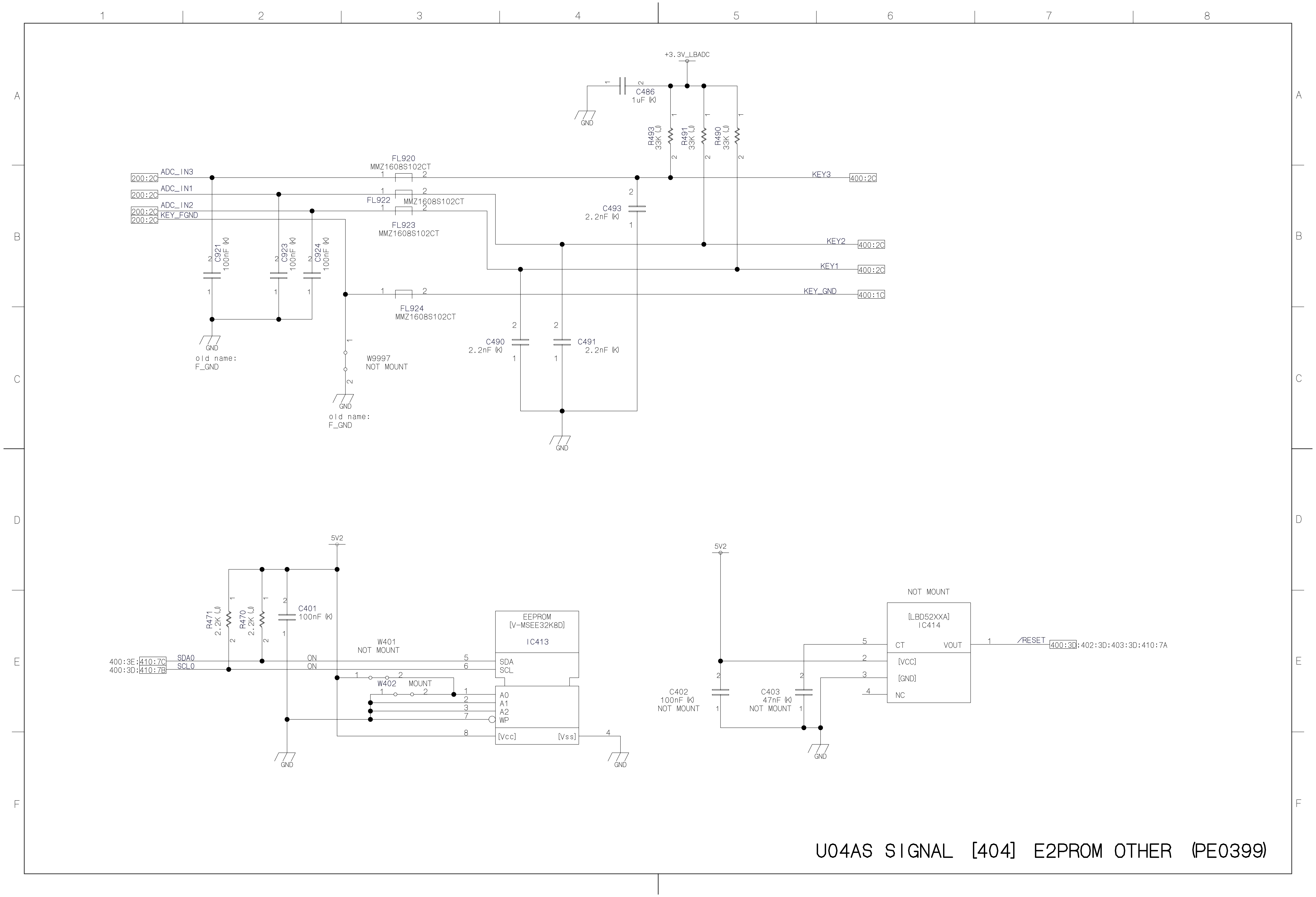
U04AS SIGNAL [401] OCM MEMORY I/F (PE0399)


1
2
3
4
5
6
7
8
A
B
C
D
E
F
401:1A:403:3B, 401:1A:403:3B, 401:1A:403:3B, 401:1A:403:3B, 401:1A:403:3B, 401:1A:403:3B, 401:1A:403:3B, 401:1A:403:3B, 401:1A:403:3B, 401:1A:403:3B, 401:1A:403:3B, 401:1A:403:3B, 401:1A:403:3B, 401:1A:403:3B, 401:1A:403:3B, 401:1A:403:3B, 401:1A:403:3B,
401:1A:403:3B, 401:1A:403:3B:409:1B, 401:1A:403:3B:409:1B, 401:1A:403:3B:409:1B, 401:1A:403:3B:409:1B, 401:1A:403:3B:409:1B, 401:1A:403:3B:409:1B, 401:1A:403:3B:409:1B, 401:1A:403:3B:409:1B, 401:1A:403:3B:409:1B, 401:1A:403:3B:409:1B, 401:1A:403:3B:409:1B,
401:1A:403:3B:409:1B, 401:1A:403:3B:409:1B, 401:1A:403:3B:409:1B, 401:1A:403:3B:409:1B, 401:1A:403:3B:409:1B, 401:1A:403:3B:409:1B, 401:1A:403:3B:409:1B, 401:1A:403:3B:409:1B, 401:1A:403:3B:409:1B, 401:1A:403:3B, 401:1A:403:3B, 401:1A:403:3B
OCMDATA [15-0], OCMADDR [21-0], /ROM_CS, /OCM_WE, /OCM_RE
IC404
SST39VF3201-70-4C-EKE
[V-MFLV320AA]
OCMADDR [1] 25 A0
OCMADDR [2] 24 A1
OCMADDR [3] 23 A2
OCMADDR [4] 22 A3
OCMADDR [5] 21 A4
OCMADDR [6] 20 A5
OCMADDR [7] 19 A6
OCMADDR [8] 18 A7
OCMADDR [9] 8 A8
OCMADDR [10] 7 A9
OCMADDR [11] 6 A10
OCMADDR [12] 5 A11
OCMADDR [13] 4 A12
OCMADDR [14] 3 A13
OCMADDR [15] 2 A14
OCMADDR [16] 1 A15
OCMADDR [17] 48 A16
OCMADDR [18] 17 A17
OCMADDR [19] 16 A18
OCMADDR [20] 9 A19
OCMADDR [21] 10 A20
26 CEN
/OCM_RE 28 OEN
/OCM_WE 11 WEN
12 RSTN
14 WPN
NC 13
NC 15
NC 47
+3.3V_I/O
R435
10K (J)
NOT MOUNT
W403
MOUNT
/ROM_CS
400:3D:403:3D:404:7E:410:7A
/RESET
W411
MOUNT
R464
2.2K (J)
C464
NOT MOUNT
GND
+3.3V_I/O
C410
10uF (K)
10V
C411
100nF (K)
10V
37 VDD
VSS 27
VSS 46
GND
D0 29 OCMDATA [0]
D1 31 OCMDATA [1]
D2 33 OCMDATA [2]
D3 35 OCMDATA [3]
D4 38 OCMDATA [4]
D5 40 OCMDATA [5]
D6 42 OCMDATA [6]
D7 44 OCMDATA [7]
D8 30 OCMDATA [8]
D9 32 OCMDATA [9]
D10 34 OCMDATA [10]
D11 36 OCMDATA [11]
D12 39 OCMDATA [12]
D13 41 OCMDATA [13]
D14 43 OCMDATA [14]
D15 45 OCMDATA [15]

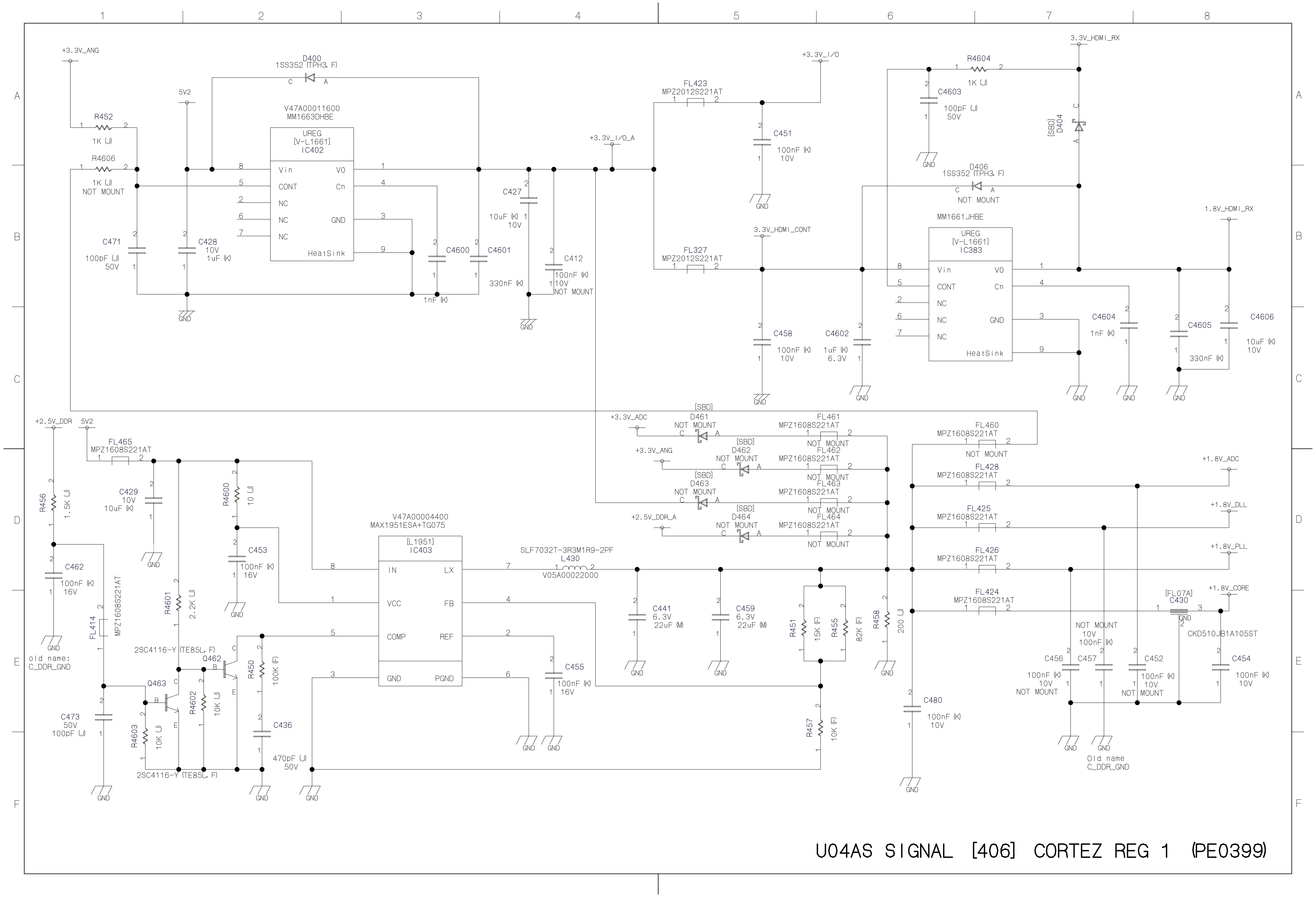
U04AS SIGNAL [402] FLASH MEMORY (PE0399)


401:1A;402:3A, 401:1A;402:3A, 401:1A;402:3A, 401:1A;402:3A;409:1B, 401:1A;402:3A;409:1B, 401:1A;402:3A;409:1B, 401:1A;402:3A;409:1B, 401:1A;402:3A;409:1B, 401:1A;402:3A;409:1B, 401:1A;402:3A;409:1B, 401:1A;402:3A;409:1B, 401:1A;402:3A;409:1B, 401:1A;402:3A;409:1B,
401:1A;402:3A;409:1B, 401:1A;402:3A;409:1B, 401:1A;402:3A;409:1B, 401:1A;402:3A;409:1B, 401:1A;402:3A;409:1B, 401:1A;402:3A;409:1B, 401:1A;402:3A;409:1B, 401:1A;402:3A;409:1B, 401:1A;402:3A;409:1B, 401:1A;402:3A;409:1B, 401:1A;402:3A, 401:1A;402:3A, 401:1A;402:3A,
401:1A;402:3A, 401:1A;402:3A, 401:1A;402:3A, 401:1A;402:3A, 401:1A;402:3A, 401:1A;402:3A, 401:1A;402:3A, 401:1A;402:3A, 401:1A;402:3A, 401:1A;402:3A, 401:1A;402:3A, 401:1A;402:3A, 401:1A;402:3A, 401:1A;402:3A, 401:1A;402:3A
/OCM_RE, /OCM_WE, /ROM_CS, OCMADDR [0-21], OCMDATA [0-15]
[C50D2]
CN400
OCMDATA [0] 1
OCMDATA [1] 3
OCMDATA [2] 5
OCMDATA [3] 7
OCMDATA [4] 9
OCMDATA [5] 11
OCMDATA [6] 13
OCMDATA [7] 15
OCMDATA [8] 17
OCMDATA [9] 19
OCMDATA [10] 21
OCMDATA [11] 23
OCMDATA [12] 25
OCMDATA [13] 27
OCMDATA [14] 29
OCMDATA [15] 31
/ROM_CS 33
/OCM_RE 35
/OCM_WE 37
/RESET 39
41
43
45
47
49
1T
2 OCMADDR [1]
4 OCMADDR [2]
6 OCMADDR [3]
8 OCMADDR [4]
10 OCMADDR [5]
12 OCMADDR [6]
14 OCMADDR [7]
16 OCMADDR [8]
18 OCMADDR [9]
20 OCMADDR [10]
22 OCMADDR [11]
24 OCMADDR [12]
26 OCMADDR [13]
28 OCMADDR [14]
30 OCMADDR [15]
32 OCMADDR [16]
34 OCMADDR [17]
36 OCMADDR [18]
38 OCMADDR [19]
40 OCMADDR [20]
42 OCMADDR [21]
44
46
48
50
2T
+3.3V_I/O
GND

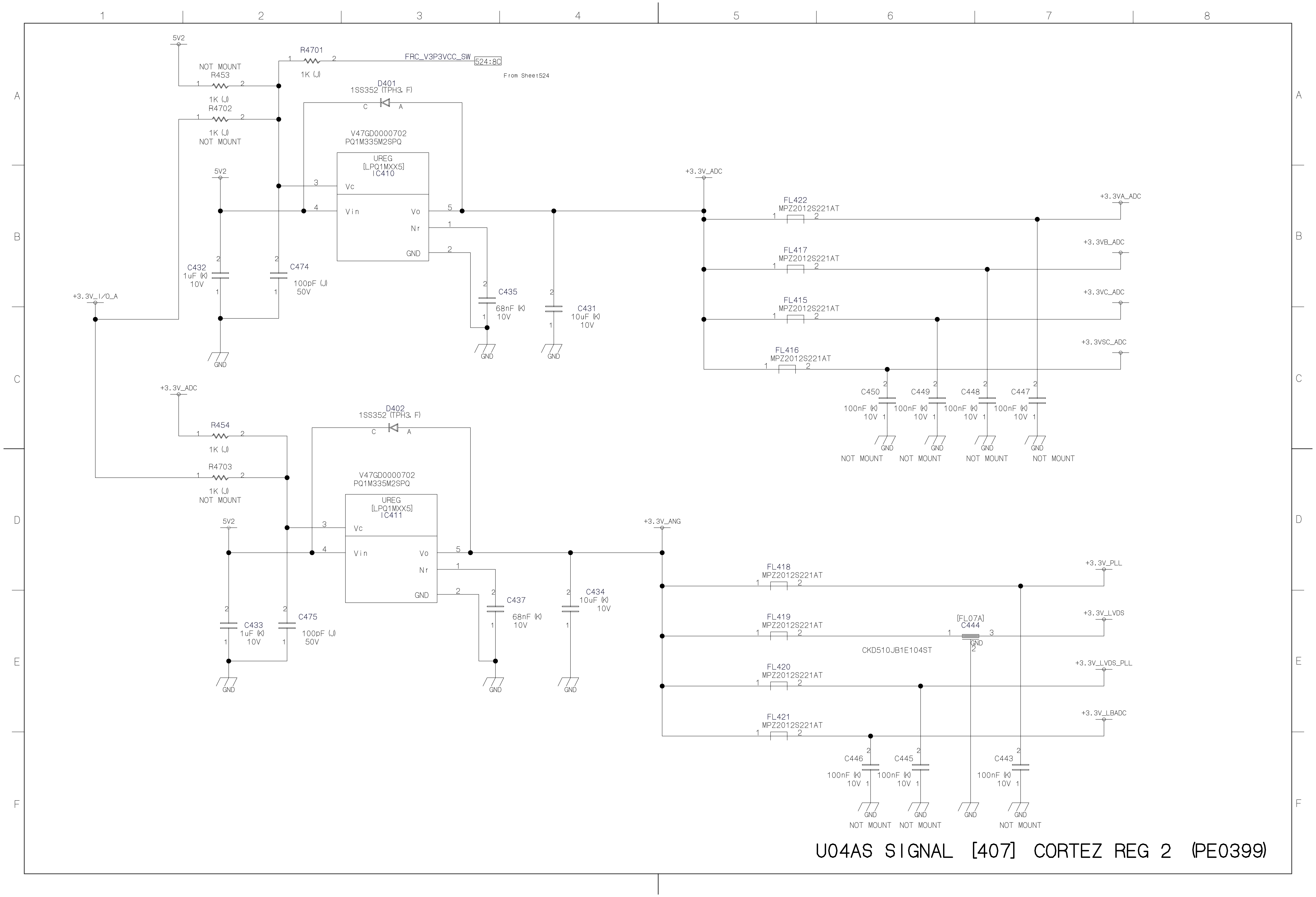
U04AS SIGNAL [403] SRAM MODULE (PE0399)


+3.3V_LBADC
C486
1uF (K)
GND
R493
33K (J)
R491
33K (J)
R490
33K (J)
ADC_IN3
ADC_IN1
ADC_IN2
KEY_FGND
200:2C
FL920
MMZ1608S102CT
FL922
MMZ1608S102CT
FL923
MMZ1608S102CT
FL924
MMZ1608S102CT
C493
2.2nF (K)
KEY3
400:2C
KEY2
400:2C
KEY1
400:2C
KEY_GND
400:1C
C921
100nF (K)
C923
100nF (K)
C924
100nF (K)
old name:
F_GND
W9997
NOT MOUNT
C490
2.2nF (K)
C491
2.2nF (K)
5V2
R471
2.2K (J)
R470
2.2K (J)
C401
100nF (K)
EEPROM
[V-MSEE32K8D]
IC413
W401
NOT MOUNT
400:3E:410:7C
400:3D:410:7B
SDA0
SCL0
ON
SDA
SCL
W402
MOUNT
A0
A1
A2
WP
[Vcc]
[Vss]
NOT MOUNT
[LBD52XXA]
IC414
CT
VOUT
[VCC]
[GND]
NC
/RESET
400:3D:402:3D:403:3D:410:7A
C402
100nF (K)
NOT MOUNT
C403
47nF (K)
NOT MOUNT
1
2
3
4
5
6
7
8
A
B
C
D
E
F

U04AS SIGNAL [404] E2PROM OTHER (PE0399)

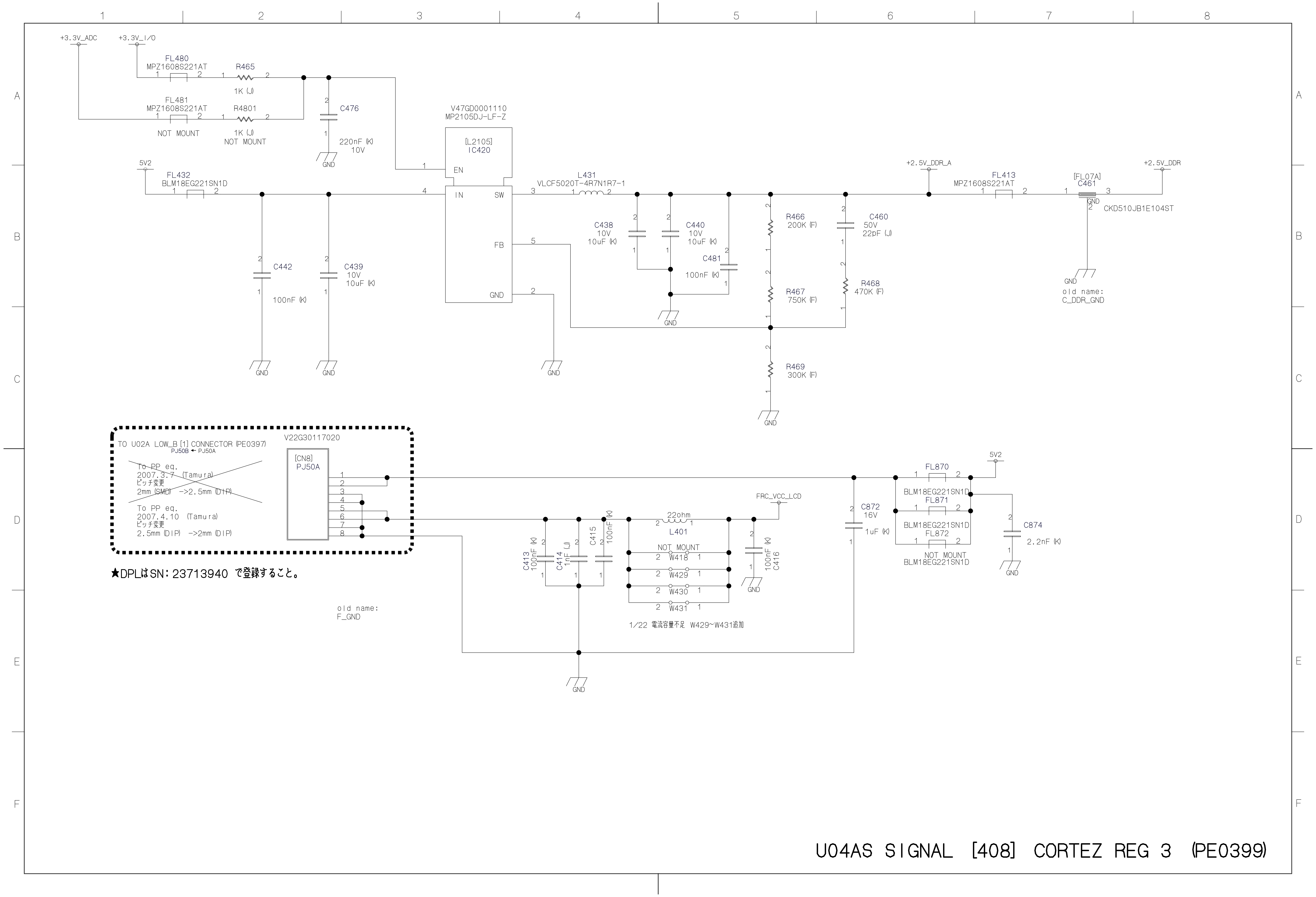

+3.3V_ANG
R452
1K (J)
R4606
1K (J)
NOT MOUNT
C471
100pF (J)
50V
5V2
C428
10V
1uF (K)
GND
D400
1SS352 (TPH3, F)
V47A00011600
MM1663DHBE
UREG
[V-L1661]
IC402
Vin
CONT
NC
V0
Cn
GND
HeatSink
C4600
C4601
1nF (K)
C427
10uF (K)
10V
330nF (K)
C412
100nF (K)
10V
NOT MOUNT
+3.3V_I/O_A
+3.3V_I/O
FL423
MPZ2012S221AT
C451
100nF (K)
10V
3.3V_HDMI_CONT
FL327
MPZ2012S221AT
C458
100nF (K)
10V
C4602
1uF (K)
6.3V
3.3V_HDMI_RX
R4604
1K (J)
C4603
100pF (J)
50V
[SBD]
D404
D406
1SS352 (TPH3, F)
NOT MOUNT
MM1661JHBE
UREG
[V-L1661]
IC383
C4604
1nF (K)
C4605
330nF (K)
C4606
10uF (K)
10V
1.8V_HDMI_RX
+2.5V_DDR
5V2
FL465
MPZ1608S221AT
C429
10V
10uF (K)
R456
1.5K (J)
C462
100nF (K)
16V
FL414
MPZ1608S221AT
old name:
C_DDR_GND
C473
50V
100pF (J)
R4600
10 (J)
C453
100nF (K)
16V
R4601
2.2K (J)
2SC4116-Y (TE85L, F)
Q462
Q463
R4602
10K (J)
R4603
10K (J)
R450
100K (F)
C436
470pF (J)
50V
V47A00004400
MAX1951ESA+TG075
[L1951]
IC403
IN
VCC
COMP
GND
LX
FB
REF
PGND
C455
100nF (K)
16V
SLF7032T-3R3M1R9-2PF
L430
V05A00022000
C441
6.3V
22uF (M)
C459
6.3V
22uF (M)
R451
15K (F)
R455
82K (F)
R457
10K (F)
R458
200 (J)
+3.3V_ADC
[SBD]
D461
NOT MOUNT
FL461
MPZ1608S221AT
+3.3V_ANG
D462
FL462
D463
FL463
+2.5V_DDR_A
D464
FL464
FL460
MPZ1608S221AT
NOT MOUNT
FL428
+1.8V_ADC
FL425
+1.8V_DLL
FL426
+1.8V_PLL
FL424
[FL07A]
C430
+1.8V_CORE
CKD510JB1A105ST
C456
100nF (K)
10V
NOT MOUNT
C457
NOT MOUNT
10V
100nF (K)
C452
C454
C480
100nF (K)
10V
Old name
C_DDR_GND

U04AS SIGNAL [406] CORTEZ REG 1 (PE0399)

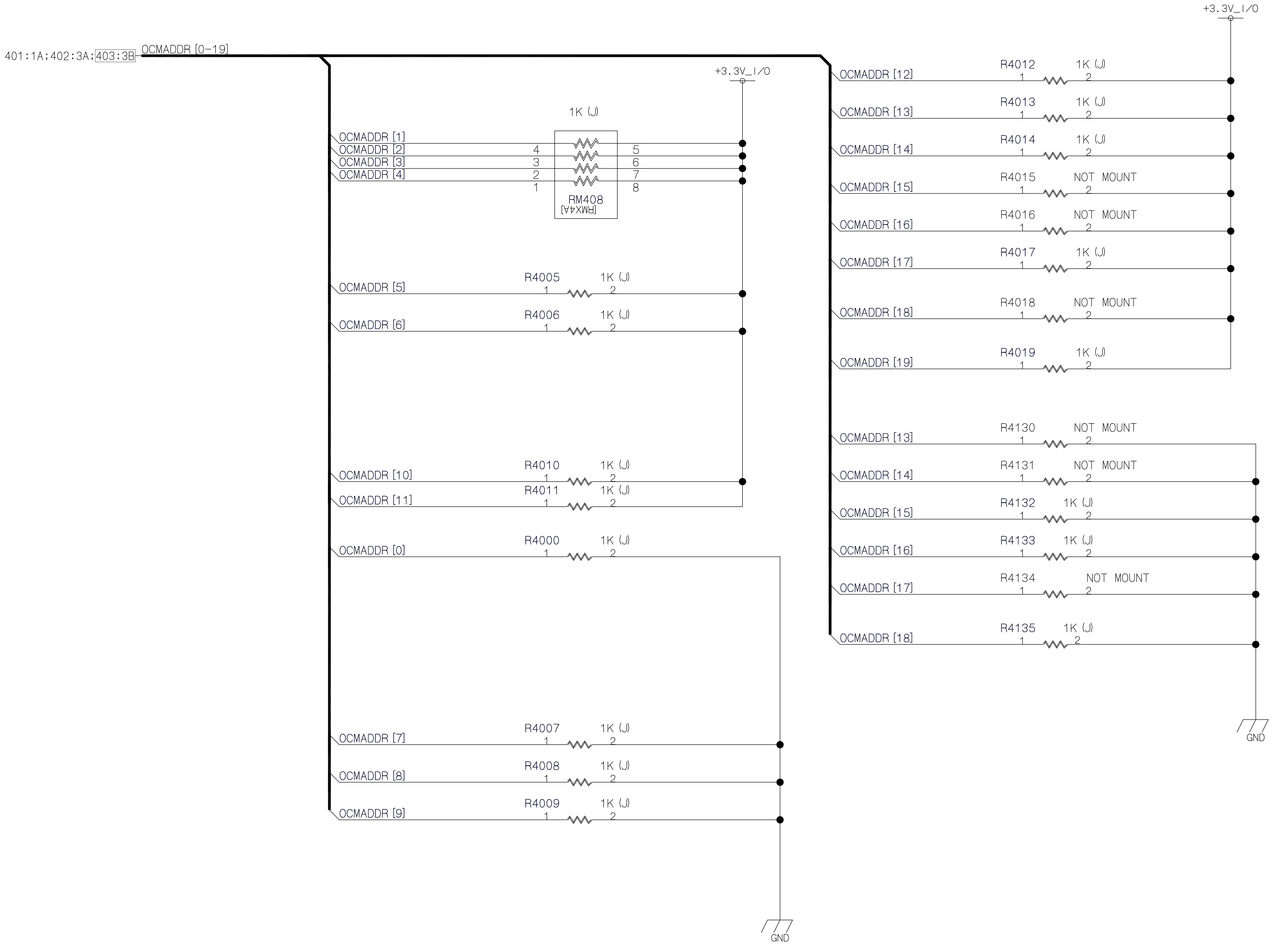


1
2
3
4
5
6
7
8
A
B
C
D
E
F
5V2
NOT MOUNT
R453
1K (J)
R4701
1K (J)
FRC_V3P3VCC_SW
524:8C
From Sheet524
D401
1SS352 (TPH3, F)
C
A
R4702
1K (J)
NOT MOUNT
V47GD0000702
PQ1M335M2SPQ
UREG
[LPQ1MXX5]
IC410
Vc
Vin
Vo
Nr
GND
C432
1uF (K)
10V
C474
100pF (J)
50V
C435
68nF (K)
10V
C431
10uF (K)
10V
GND
+3.3V_I/O_A
+3.3V_ADC
FL422
MPZ2012S221AT
+3.3VA_ADC
FL417
MPZ2012S221AT
+3.3VB_ADC
FL415
MPZ2012S221AT
+3.3VC_ADC
FL416
MPZ2012S221AT
+3.3VSC_ADC
C450
100nF (K)
10V
C449
100nF (K)
10V
C448
100nF (K)
10V
C447
100nF (K)
10V
NOT MOUNT
NOT MOUNT
NOT MOUNT
NOT MOUNT
R454
1K (J)
D402
1SS352 (TPH3, F)
R4703
1K (J)
NOT MOUNT
V47GD0000702
PQ1M335M2SPQ
UREG
[LPQ1MXX5]
IC411
C433
1uF (K)
10V
C475
100pF (J)
50V
C437
68nF (K)
10V
C434
10uF (K)
10V
+3.3V_ANG
FL418
MPZ2012S221AT
+3.3V_PLL
FL419
MPZ2012S221AT
[FL07A]
C444
CKD510JB1E104ST
+3.3V_LVDS
FL420
MPZ2012S221AT
+3.3V_LVDS_PLL
FL421
MPZ2012S221AT
+3.3V_LBADC
C446
100nF (K)
10V
C445
100nF (K)
10V
C443
100nF (K)
10V
NOT MOUNT
NOT MOUNT
NOT MOUNT

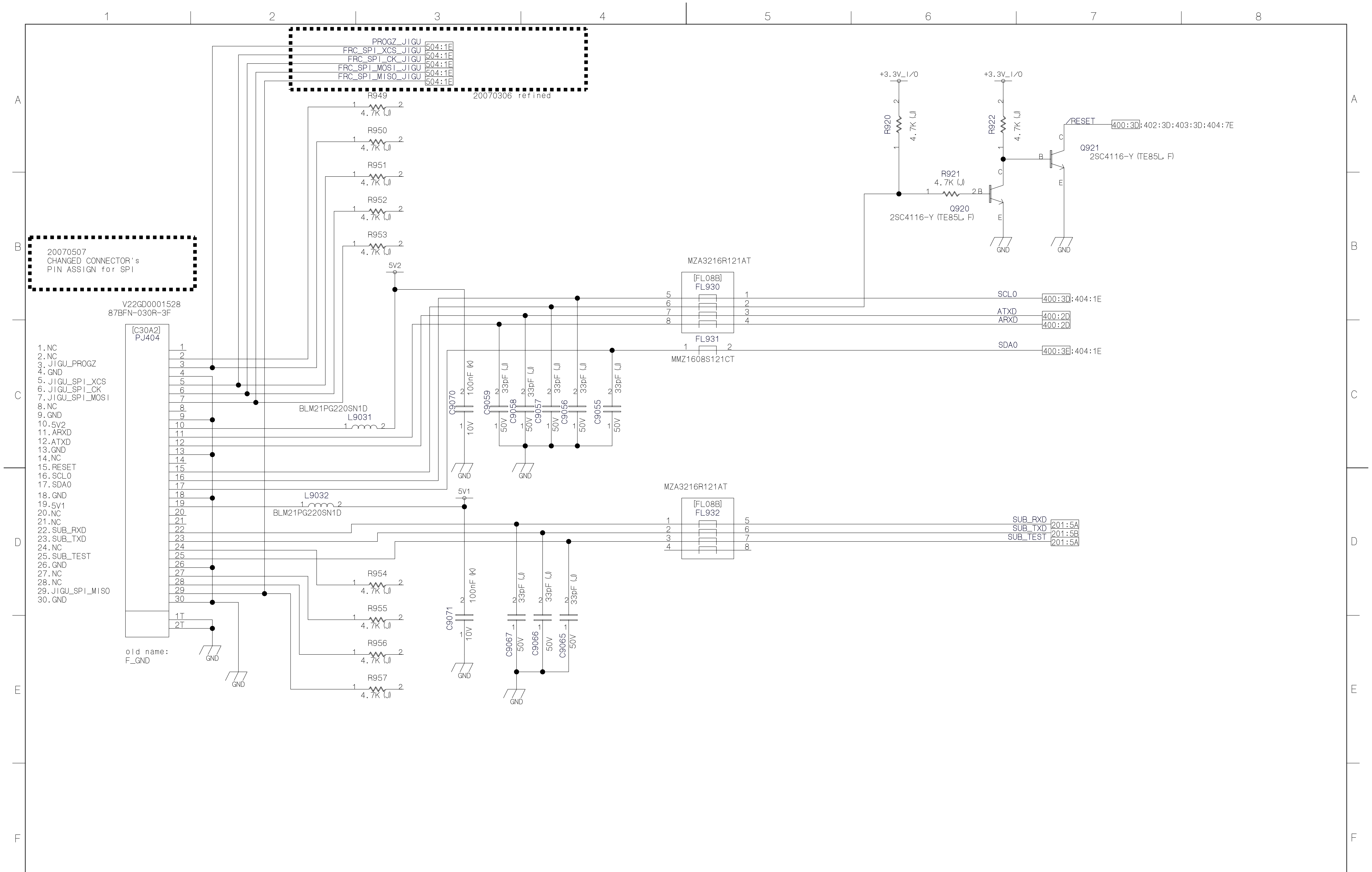
U04AS SIGNAL [407] CORTEZ REG 2 (PE0399)


1
2
3
4
5
6
7
8
A
B
C
D
E
F
+3.3V_ADC
+3.3V_I/O
FL480
MPZ1608S221AT
R465
1K (J)
FL481
MPZ1608S221AT
R4801
NOT MOUNT
1K (J)
NOT MOUNT
C476
220nF (K)
10V
GND
V47GD0001110
MP2105DJ-LF-Z
[L2105]
IC420
EN
IN
SW
FB
GND
5V2
FL432
BLM18EG221SN1D
L431
VLCF5020T-4R7N1R7-1
C438
10V
10uF (K)
C440
10V
10uF (K)
C481
100nF (K)
R466
200K (F)
R467
750K (F)
R469
300K (F)
C460
50V
22pF (J)
R468
470K (F)
+2.5V_DDR_A
FL413
MPZ1608S221AT
[FL07A]
C461
CKD510JB1E104ST
+2.5V_DDR
old name:
C_DDR_GND
C442
100nF (K)
C439
10V
10uF (K)
TO U02A LOW_B [1] CONNECTOR (PE0397)
PJ50B ← PJ50A
~~To PP eq.~~
~~2007.3.7 (Tamura)~~
~~ピッチ変更~~
~~2mm (SMD) ->2.5mm (DIP)~~
To PP eq.
2007.4.10 (Tamura)
ピッチ変更
2.5mm (DIP) ->2mm (DIP)
V22G30117020
[CN8]
PJ50A
★DPLはSN：23713940 で登録すること。
old name:
F_GND
C413
100nF (K)
C414
1nF (J)
C415
100nF (K)
22ohm
L401
NOT MOUNT
W418
W429
W430
W431
1/22 電流容量不足 W429~W431追加
FRC_VCC_LCD
C416
100nF (K)
C872
16V
1uF (K)
FL870
BLM18EG221SN1D
FL871
BLM18EG221SN1D
FL872
NOT MOUNT
BLM18EG221SN1D
C874
2.2nF (K)

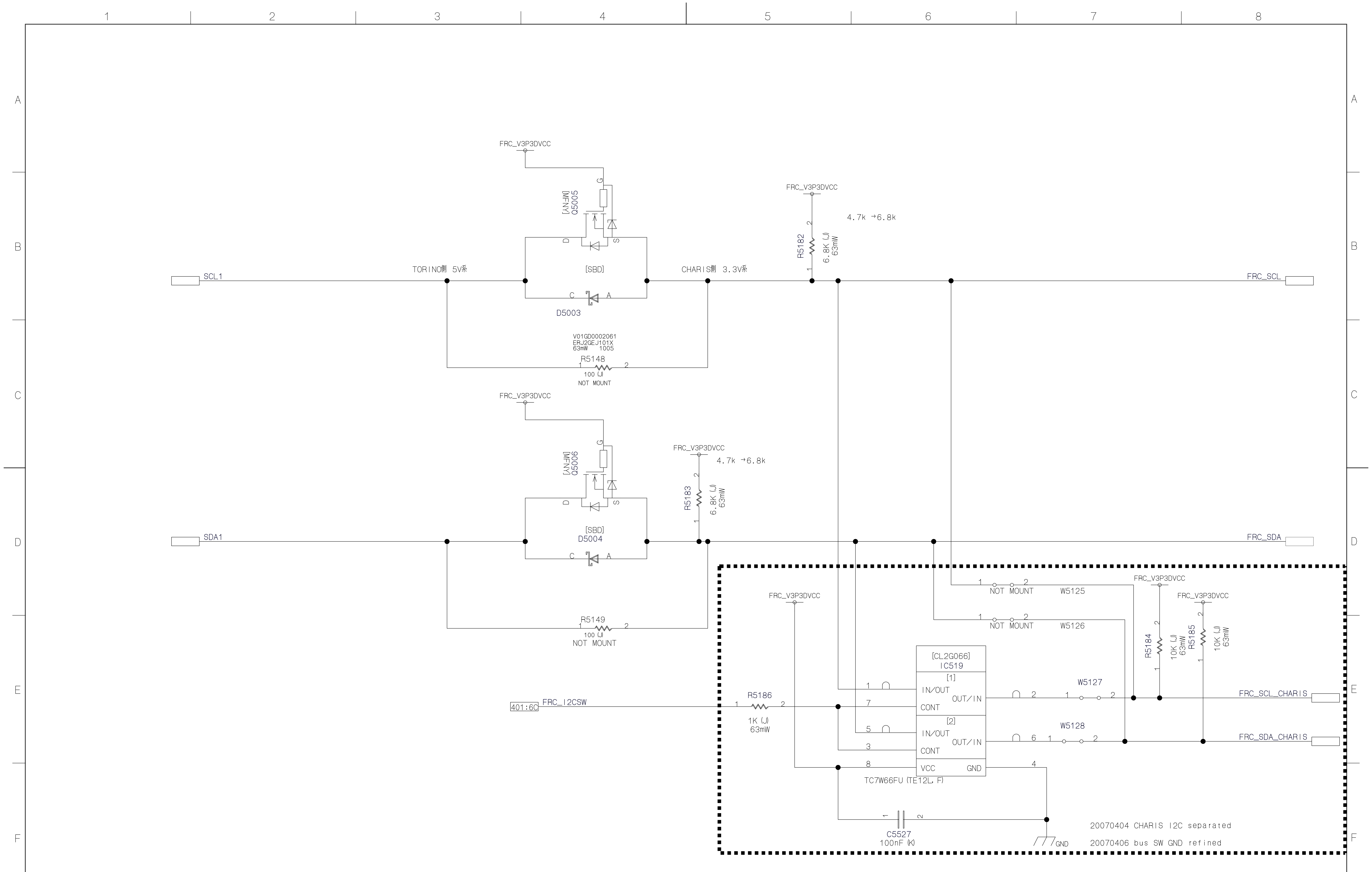
U04AS SIGNAL [408] CORTEZ REG 3 (PE0399)


401:1A;402:3A;403:3B
OCMADDR [0-19]
+3.3V_I/O
1K (J)
OCMADDR [1]
OCMADDR [2]
OCMADDR [3]
OCMADDR [4]
4
3
2
1
5
6
7
8
RM408
[RMX4A]
OCMADDR [5]
R4005 1K (J)
OCMADDR [6]
R4006 1K (J)
OCMADDR [10]
R4010 1K (J)
OCMADDR [11]
R4011 1K (J)
OCMADDR [0]
R4000 1K (J)
OCMADDR [7]
R4007 1K (J)
OCMADDR [8]
R4008 1K (J)
OCMADDR [9]
R4009 1K (J)
GND
+3.3V_I/O
OCMADDR [12]
R4012 1K (J)
OCMADDR [13]
R4013 1K (J)
OCMADDR [14]
R4014 1K (J)
OCMADDR [15]
R4015 NOT MOUNT
OCMADDR [16]
R4016 NOT MOUNT
OCMADDR [17]
R4017 1K (J)
OCMADDR [18]
R4018 NOT MOUNT
OCMADDR [19]
R4019 1K (J)
OCMADDR [13]
R4130 NOT MOUNT
OCMADDR [14]
R4131 NOT MOUNT
OCMADDR [15]
R4132 1K (J)
OCMADDR [16]
R4133 1K (J)
OCMADDR [17]
R4134 NOT MOUNT
OCMADDR [18]
R4135 1K (J)
GND

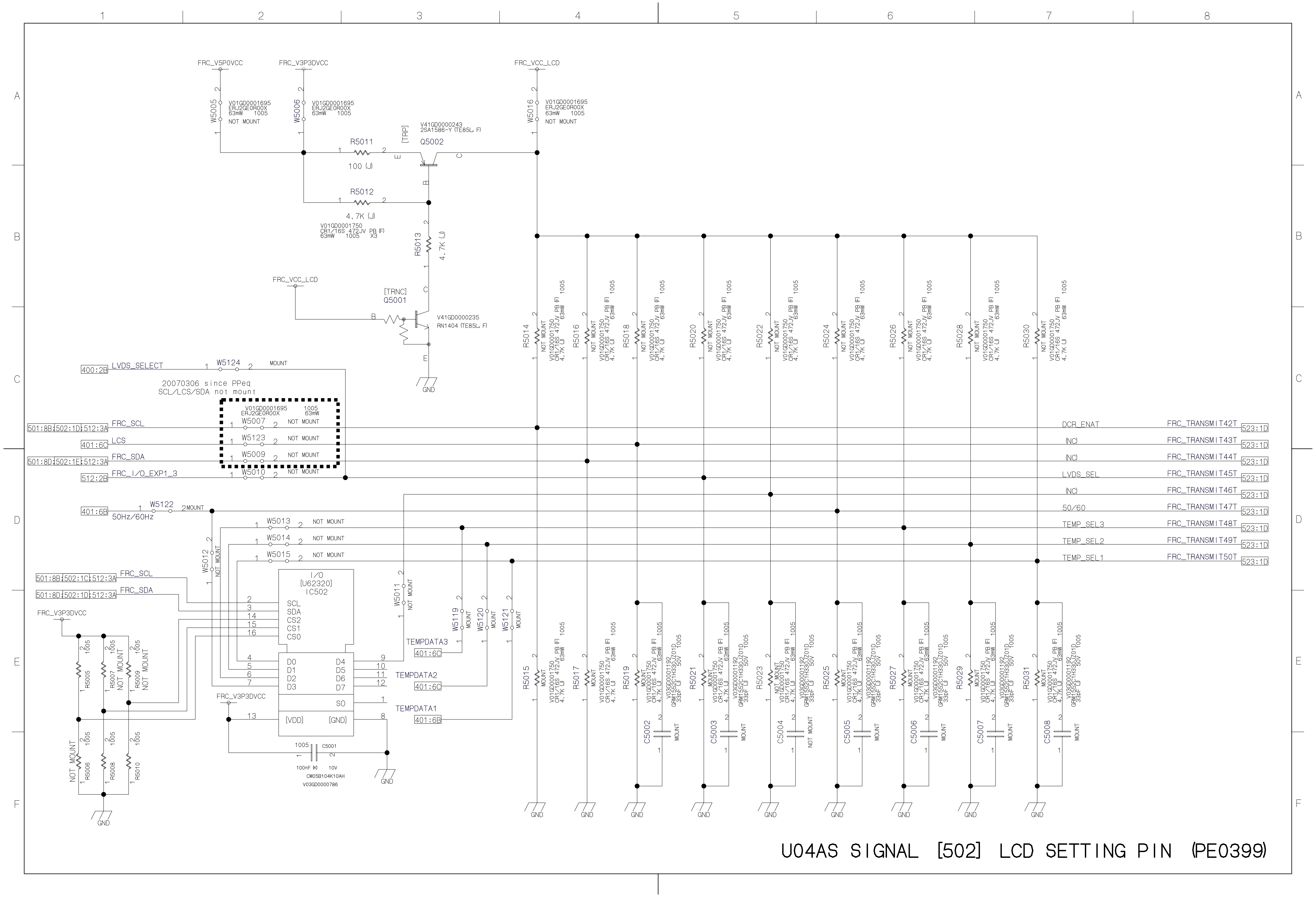
U04AS SIGNAL [409] BOOT CONFIG (PE0399)


PROGZ_JIGU 504:1E
FRC_SPI_XCS_JIGU 504:1E
FRC_SPI_CK_JIGU 504:1E
FRC_SPI_MOSI_JIGU 504:1E
FRC_SPI_MISO_JIGU 504:1E
20070306 refined
R949 4.7K (J)
R950 4.7K (J)
R951 4.7K (J)
R952 4.7K (J)
R953 4.7K (J)
20070507
CHANGED CONNECTOR's
PIN ASSIGN for SPI
V22GD0001528
87BFN-030R-3F
[C30A2]
PJ404
1.NC
2.NC
3.JIGU_PROGZ
4.GND
5.JIGU_SPI_XCS
6.JIGU_SPI_CK
7.JIGU_SPI_MOSI
8.NC
9.GND
10.5V2
11.ARXD
12.ATXD
13.GND
14.NC
15.RESET
16.SCL0
17.SDA0
18.GND
19.5V1
20.NC
21.NC
22.SUB_RXD
23.SUB_TXD
24.NC
25.SUB_TEST
26.GND
27.NC
28.NC
29.JIGU_SPI_MISO
30.GND
1T
2T
old name:
F_GND
GND
5V2
BLM21PG220SN1D
L9031
C9070 100nF (K) 10V
C9059 33pF (J) 50V
C9058 33pF (J) 50V
C9057 33pF (J) 50V
C9056 33pF (J) 50V
C9055 33pF (J) 50V
L9032
BLM21PG220SN1D
5V1
C9071 100nF (K) 10V
C9067 33pF (J) 50V
C9066 33pF (J) 50V
C9065 33pF (J) 50V
R954 4.7K (J)
R955 4.7K (J)
R956 4.7K (J)
R957 4.7K (J)
MZA3216R121AT
[FL08B]
FL930
FL931
MMZ1608S121CT
SCL0 400:3D;404:1E
ATXD 400:2D
ARXD 400:2D
SDA0 400:3E;404:1E
MZA3216R121AT
[FL08B]
FL932
SUB_RXD 201:5A
SUB_TXD 201:5B
SUB_TEST 201:5A
+3.3V_I/O
R920 4.7K (J)
+3.3V_I/O
R922 4.7K (J)
R921 4.7K (J)
Q920
2SC4116-Y (TE85L, F)
Q921
2SC4116-Y (TE85L, F)
/RESET 400:3D;402:3D;403:3D;404:7E
B C E
1 2 3 4 5 6 7 8
A B C D E F

U04AS SIGNAL [410] SERVICE CONNECTOR (PE0399)

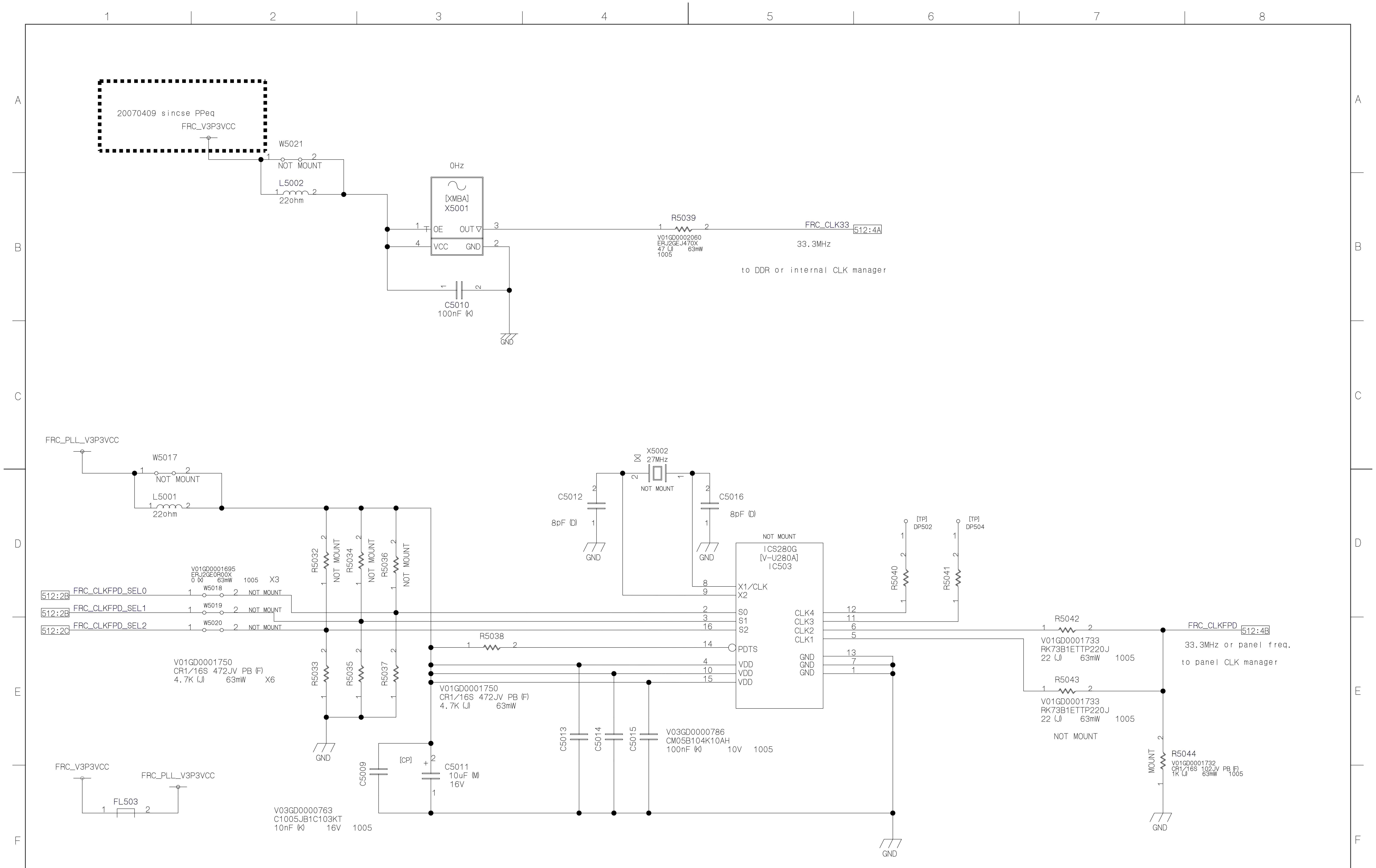

1
2
3
4
5
6
7
8
A
B
C
D
E
F
FRC_V3P3DVCC
G
[MFNY]
Q5005
D
S
[SBD]
SCL1
TORINO側 5V系
CHARIS側 3.3V系
C
A
D5003
V01GD0002061
ERJ2GEJ101X
63mW 1005
R5148
1
2
100 (J)
NOT MOUNT
FRC_V3P3DVCC
4.7k →6.8k
2
R5182
6.8K (J)
63mW
1
FRC_SCL
FRC_V3P3DVCC
G
[MFNY]
Q5006
D
S
[SBD]
D5004
C
A
SDA1
FRC_V3P3DVCC
4.7k →6.8k
2
R5183
6.8K (J)
63mW
1
FRC_SDA
R5149
1
2
100 (J)
NOT MOUNT
1
2
NOT MOUNT
W5125
1
2
NOT MOUNT
W5126
FRC_V3P3DVCC
FRC_V3P3DVCC
FRC_V3P3DVCC
2
R5184
10K (J)
63mW
1
2
R5185
10K (J)
63mW
1
[CL2G066]
IC519
[1]
1
IN/OUT
OUT/IN
7
CONT
[2]
5
IN/OUT
OUT/IN
3
CONT
8
VCC
GND
4
TC7W66FU (TE12L, F)
401:6C
FRC_I2CSW
R5186
1
2
1K (J)
63mW
W5127
2
1
2
FRC_SCL_CHARIS
W5128
6
1
2
FRC_SDA_CHARIS
1
2
C5527
100nF (K)
GND
20070404 CHARIS I2C separated
20070406 bus SW GND refined

U04AS SIGNAL [501] I2C Level shifter (PE0399)

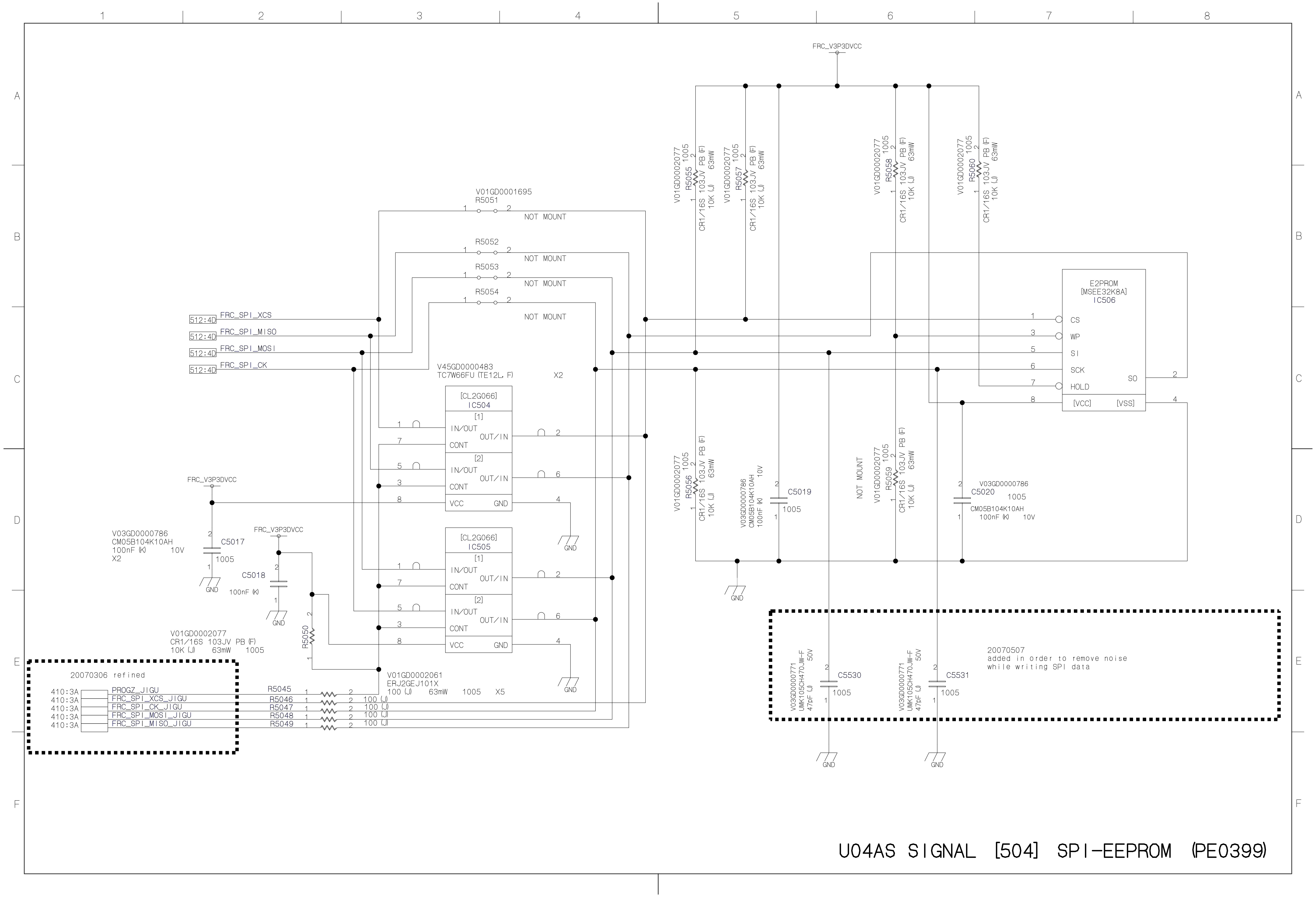


1
2
3
4
5
6
7
8
A
B
C
D
E
F
FRC_V5P0VCC
FRC_V3P3DVCC
FRC_VCC_LCD
W5005
W5006
W5016
V01GD0001695
ERJ2GE0R00X
63mW 1005
NOT MOUNT
R5011
100 (J)
R5012
4.7K (J)
[TRP]
V41GD0000243
2SA1586-Y (TE85L, F)
Q5002
E
C
B
V01GD0001750
CR1/16S 472JV PB (F)
63mW 1005 X3
R5013
4.7K (J)
FRC_VCC_LCD
[TRNC]
Q5001
V41GD0000235
RN1404 (TE85L, F)
GND
400:2B
LVDS_SELECT
W5124
MOUNT
20070306 since PPeq
SCL/LCS/SDA not mount
501:8B
502:1D
512:3A
FRC_SCL
W5007
NOT MOUNT
401:6C
LCS
W5123
501:8D
502:1E
FRC_SDA
W5009
512:2B
FRC_I/O_EXP1_3
W5010
401:6B
W5122
50Hz/60Hz
W5013
W5014
W5015
W5012
501:8B
502:1C
512:3A
FRC_SCL
501:8D
502:1D
FRC_SDA
I/O
[U62320]
IC502
SCL
SDA
CS2
CS1
CS0
D0
D1
D2
D3
D4
D5
D6
D7
SO
[VDD]
[GND]
W5011
W5119
W5120
W5121
TEMPDATA3
401:6C
TEMPDATA2
401:6C
TEMPDATA1
401:6B
FRC_V3P3DVCC
R5005
R5007
R5009
R5006
R5008
R5010
NOT MOUNT
C5001
1005
100nF (K)
10V
CM05B104K10AH
V03GD0000786
R5014
R5016
R5018
R5020
R5022
R5024
R5026
R5028
R5030
R5015
R5017
R5019
R5021
R5023
R5025
R5027
R5029
R5031
C5002
C5003
C5004
C5005
C5006
C5007
C5008
V01GD0001750
CR1/16S 472JV PB (F)
63mW 1005
4.7K (J)
V03GD0001192
GRM1552C1H330JZ01D
33pF (J) 50V 1005
MOUNT
NOT MOUNT
DCR_ENAT
(NC)
(NC)
LVDS_SEL
(NC)
50/60
TEMP_SEL3
TEMP_SEL2
TEMP_SEL1
FRC_TRANSMIT42T
FRC_TRANSMIT43T
FRC_TRANSMIT44T
FRC_TRANSMIT45T
FRC_TRANSMIT46T
FRC_TRANSMIT47T
FRC_TRANSMIT48T
FRC_TRANSMIT49T
FRC_TRANSMIT50T
523:1D
GND

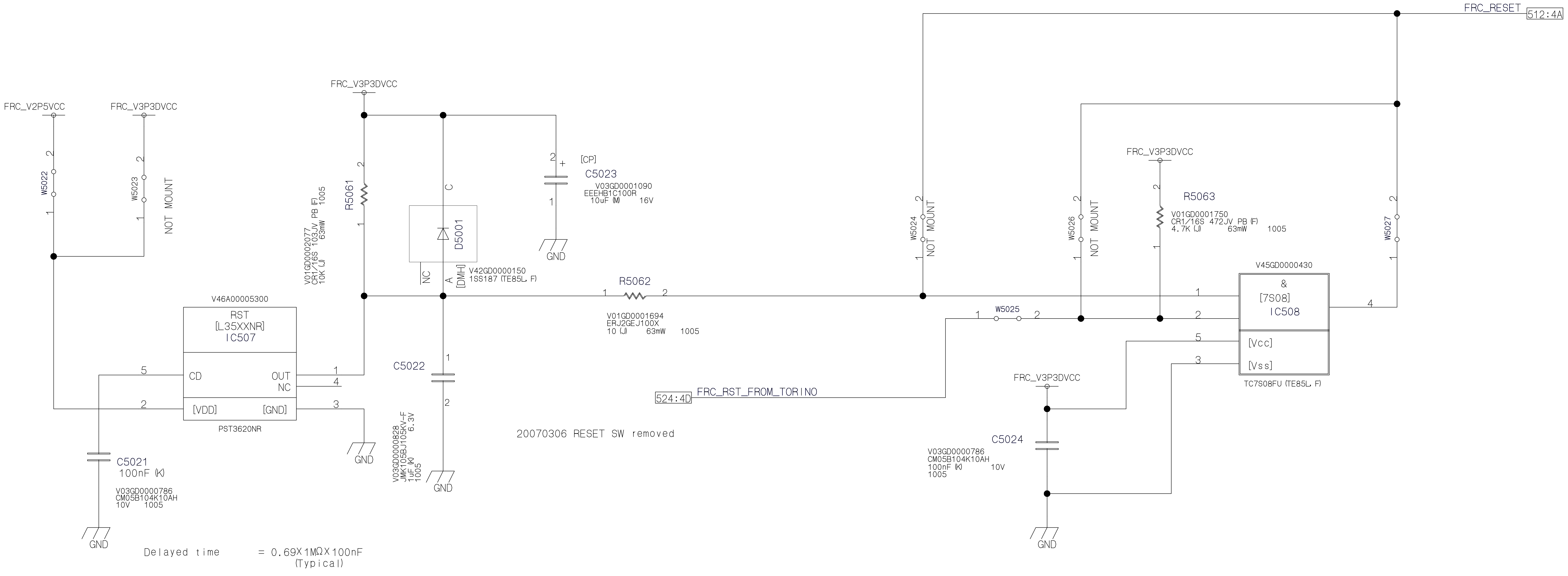
U04AS SIGNAL [502] LCD SETTING PIN (PE0399)


20070409 sincse PPeq
FRC_V3P3VCC
W5021
NOT MOUNT
L5002
22ohm
0Hz
[XMBA]
X5001
OE
OUT
VCC
GND
R5039
V01GD0002060
ERJ2GEJ470X
47 (J) 63mW
1005
FRC_CLK33
512:4A
33.3MHz
to DDR or internal CLK manager
C5010
100nF (K)
GND
FRC_PLL_V3P3VCC
W5017
NOT MOUNT
L5001
22ohm
R5032
R5034
R5036
NOT MOUNT
NOT MOUNT
NOT MOUNT
V01GD0001695
ERJ2GE0R00X
0 (X) 63mW 1005 X3
512:2B
FRC_CLKFPD_SEL0
W5018
NOT MOUNT
512:2B
FRC_CLKFPD_SEL1
W5019
NOT MOUNT
512:2C
FRC_CLKFPD_SEL2
W5020
NOT MOUNT
V01GD0001750
CR1/16S 472JV PB (F)
4.7K (J) 63mW X6
R5033
R5035
R5037
GND
R5038
V01GD0001750
CR1/16S 472JV PB (F)
4.7K (J) 63mW
C5009
[CP]
C5011
10uF (M)
16V
V03GD0000763
C1005JB1C103KT
10nF (K) 16V 1005
FRC_V3P3VCC
FRC_PLL_V3P3VCC
FL503
X5002
27MHz
NOT MOUNT
C5012
8pF (D)
GND
C5016
8pF (D)
GND
NOT MOUNT
ICS280G
[V-U280A]
IC503
X1/CLK
X2
S0
S1
S2
PDTS
VDD
VDD
VDD
CLK4
CLK3
CLK2
CLK1
GND
GND
GND
C5013
C5014
C5015
V03GD0000786
CM05B104K10AH
100nF (K) 10V 1005
GND
[TP]
DP502
[TP]
DP504
R5040
R5041
R5042
V01GD0001733
RK73B1ETTP220J
22 (J) 63mW 1005
R5043
V01GD0001733
RK73B1ETTP220J
22 (J) 63mW 1005
NOT MOUNT
FRC_CLKFPD
512:4B
33.3MHz or panel freq.
to panel CLK manager
MOUNT
R5044
V01GD0001732
CR1/16S 102JV PB (F)
1K (J) 63mW 1005
GND
1
2
3
4
5
6
7
8
A
B
C
D
E
F

U04AS SIGNAL [503] Clock Generator (PE0399)


1
2
3
4
5
6
7
8
A
B
C
D
E
F
FRC_V3P3DVCC
V01GD0002077
R5055 1005
CR1/16S 103JV PB (F)
10K (J) 63mW
V01GD0002077
R5057 1005
CR1/16S 103JV PB (F)
10K (J) 63mW
V01GD0002077
R5058 1005
CR1/16S 103JV PB (F)
10K (J) 63mW
V01GD0002077
R5060 1005
CR1/16S 103JV PB (F)
10K (J) 63mW
V01GD0001695
R5051
NOT MOUNT
R5052
NOT MOUNT
R5053
NOT MOUNT
R5054
NOT MOUNT
512:4D
FRC_SPI_XCS
512:4D
FRC_SPI_MISO
512:4D
FRC_SPI_MOSI
512:4D
FRC_SPI_CK
V45GD0000483
TC7W66FU (TE12L, F)
X2
[CL2G066]
IC504
[1]
IN/OUT
OUT/IN
CONT
[2]
IN/OUT
OUT/IN
CONT
VCC
GND
E2PROM
[MSEE32K8A]
IC506
CS
WP
SI
SCK
HOLD
SO
[VCC]
[VSS]
FRC_V3P3DVCC
V03GD0000786
CM05B104K10AH
100nF (K) 10V
X2
C5017
1005
GND
FRC_V3P3DVCC
C5018
100nF (K)
GND
[CL2G066]
IC505
[1]
IN/OUT
OUT/IN
CONT
[2]
IN/OUT
OUT/IN
CONT
VCC
GND
GND
GND
V01GD0002077
CR1/16S 103JV PB (F)
10K (J) 63mW 1005
R5050
V01GD0002061
ERJ2GEJ101X
100 (J) 63mW 1005 X5
20070306 refined
410:3A
410:3A
410:3A
410:3A
410:3A
PROGZ_JIGU
FRC_SPI_XCS_JIGU
FRC_SPI_CK_JIGU
FRC_SPI_MOSI_JIGU
FRC_SPI_MISO_JIGU
R5045
R5046 100 (J)
R5047 100 (J)
R5048 100 (J)
R5049 100 (J)
V01GD0002077
R5056 1005
CR1/16S 103JV PB (F)
10K (J) 63mW
V03GD0000786
CM05B104K10AH
100nF (K) 10V
C5019
1005
NOT MOUNT
V01GD0002077
R5059 1005
CR1/16S 103JV PB (F)
10K (J) 63mW
V03GD0000786
C5020
1005
CM05B104K10AH
100nF (K) 10V
GND
V03GD0000771
UMK105CH470JW-F
47pF (J) 50V
C5530
1005
V03GD0000771
UMK105CH470JW-F
47pF (J) 50V
C5531
1005
20070507
added in order to remove noise
while writing SPI data
GND
GND

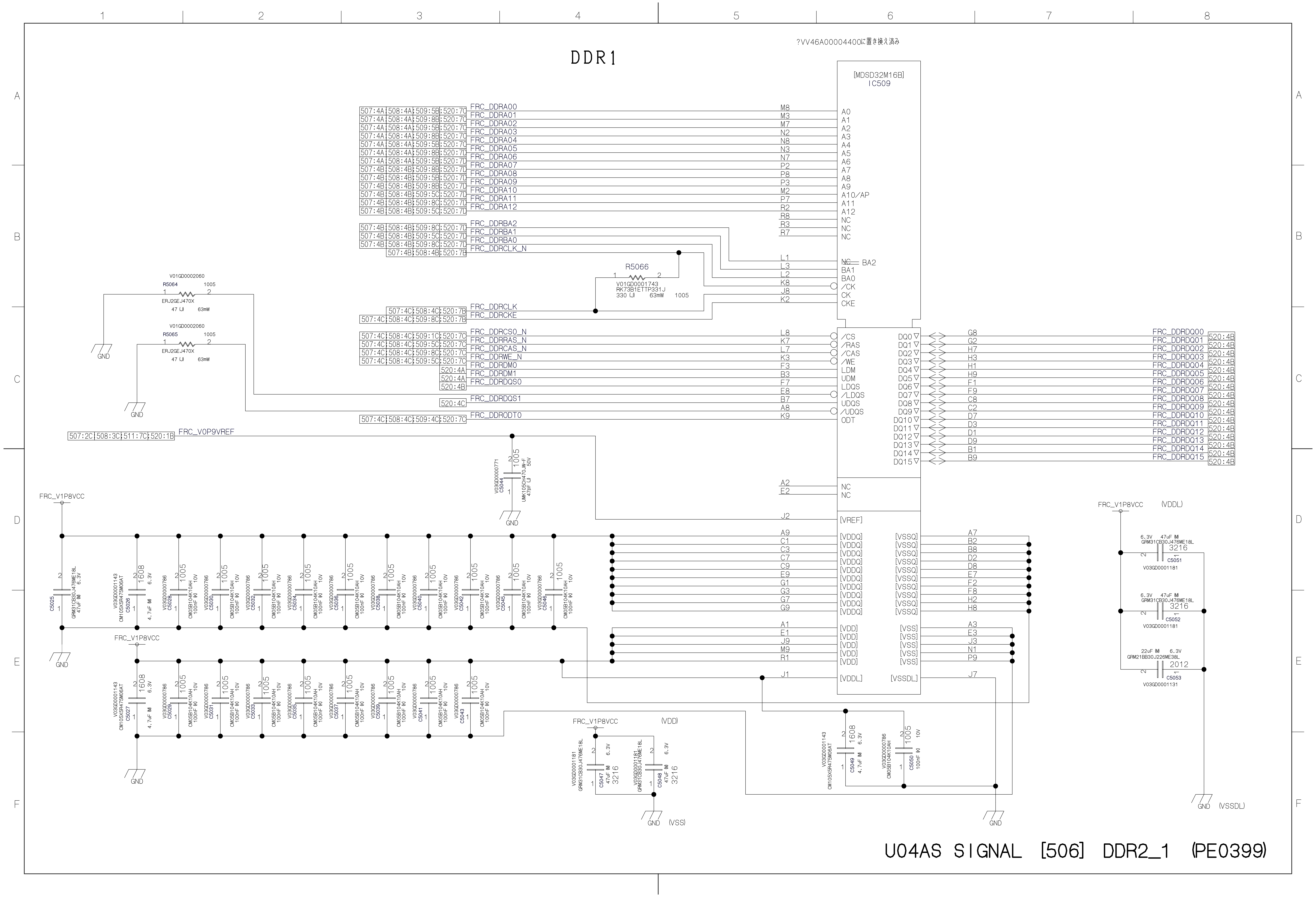
U04AS SIGNAL [504] SPI-EEPROM (PE0399)


FRC_V2P5VCC
FRC_V3P3DVCC
W5022
W5023
NOT MOUNT
FRC_V3P3DVCC
R5061
V01GD0002077
CR1/16S 103JV PB (F)
10K (J) 63mW 1005
D5001
C
A
NC
[DMH]
V42GD0000150
1SS187 (TE85L, F)
[CP]
C5023
V03GD0001090
EEEHB1C100R
10uF (M) 16V
GND
V46A00005300
RST
[L35XXNR]
IC507
CD
OUT
NC
[VDD]
[GND]
PST3620NR
GND
C5021
100nF (K)
V03GD0000786
CM05B104K10AH
10V 1005
GND
C5022
V03GD0000828
JMK105BJ105KV-F
1uF (K) 6.3V
1005
GND
R5062
V01GD0001694
ERJ2GEJ100X
10 (J) 63mW 1005
524:4D
FRC_RST_FROM_TORINO
FRC_RESET
512:4A
W5024
NOT MOUNT
W5026
NOT MOUNT
FRC_V3P3DVCC
R5063
V01GD0001750
CR1/16S 472JV PB (F)
4.7K (J) 63mW 1005
W5027
W5025
V45GD0000430
&
[7S08]
IC508
[Vcc]
[Vss]
TC7S08FU (TE85L, F)
FRC_V3P3DVCC
C5024
V03GD0000786
CM05B104K10AH
100nF (K) 10V
1005
GND

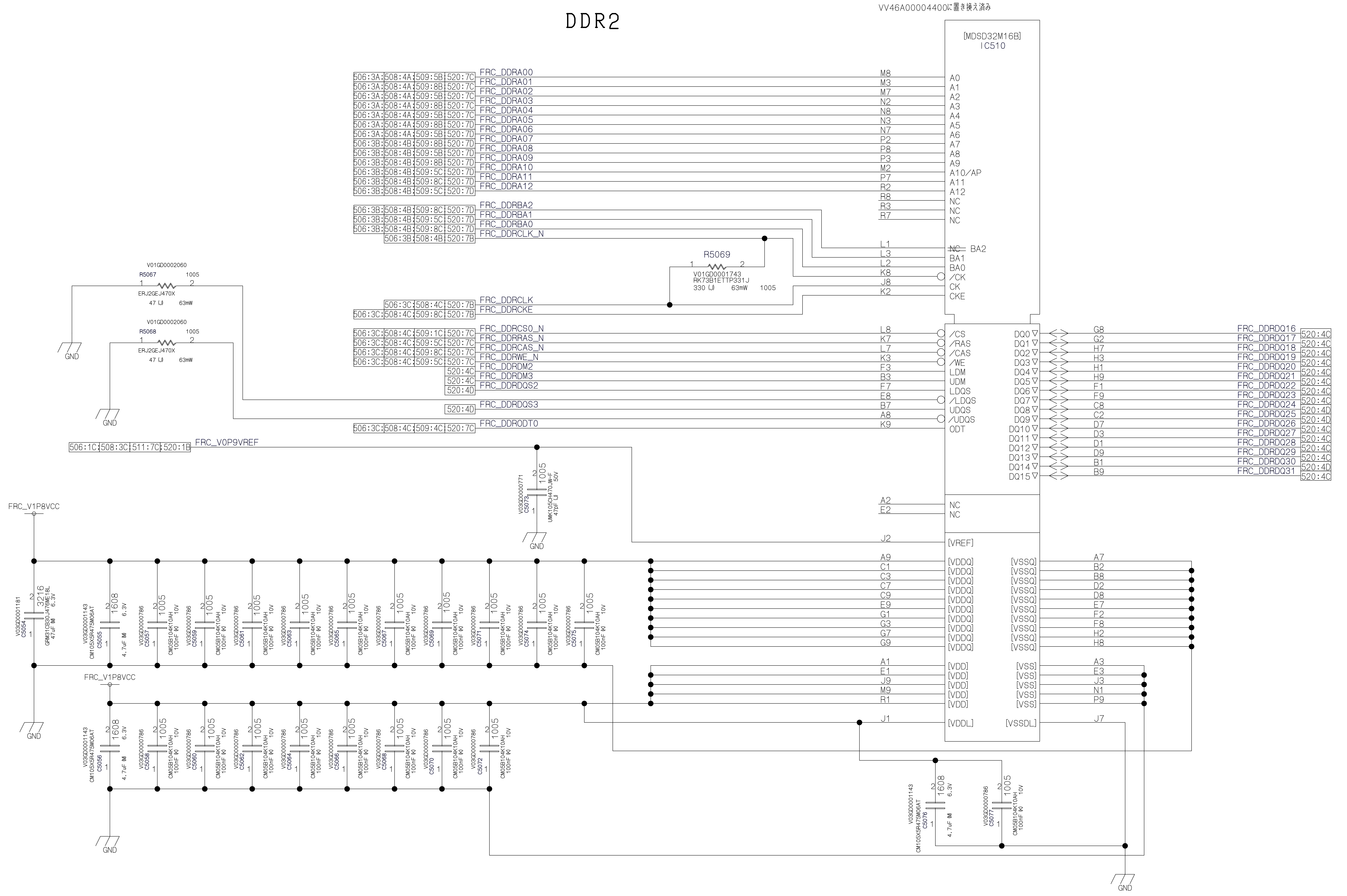
20070306 RESET SW removed

Delayed time = 0.69X1MΩX100nF
(Typical)

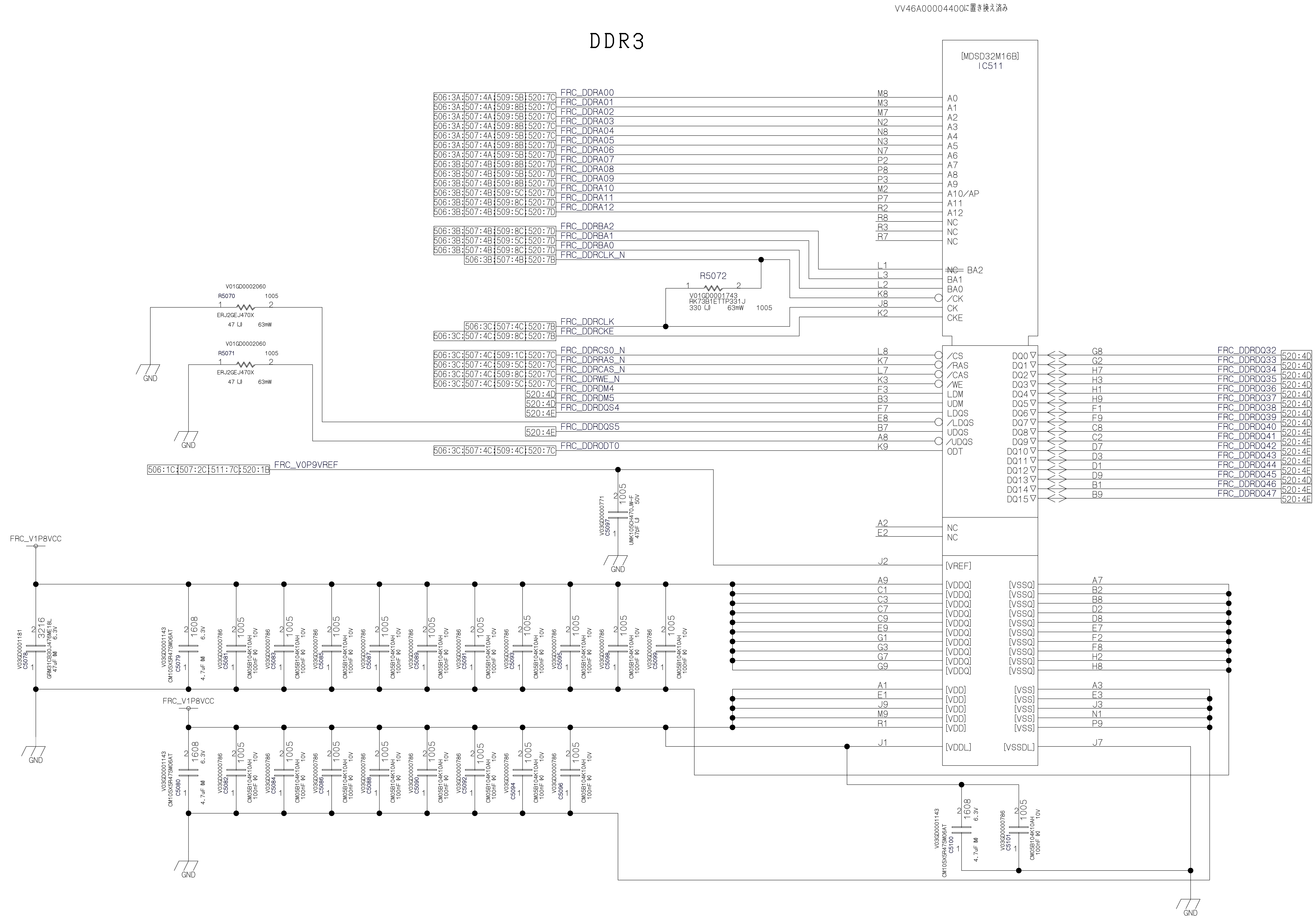
U04AS SIGNAL [505] RESET (PE0399)


DDR1
?VV46A00004400に置き換え済み
[MDSD32M16B]
IC509
FRC_DDRA00
FRC_DDRA01
FRC_DDRA02
FRC_DDRA03
FRC_DDRA04
FRC_DDRA05
FRC_DDRA06
FRC_DDRA07
FRC_DDRA08
FRC_DDRA09
FRC_DDRA10
FRC_DDRA11
FRC_DDRA12
FRC_DDRBA2
FRC_DDRBA1
FRC_DDRBA0
FRC_DDRCLK_N
FRC_DDRCLK
FRC_DDRCKE
FRC_DDRCS0_N
FRC_DDRRAS_N
FRC_DDRCAS_N
FRC_DDRWE_N
FRC_DDRDM0
FRC_DDRDM1
FRC_DDRDQS0
FRC_DDRDQS1
FRC_DDRODT0
FRC_V0P9VREF
A0
A1
A2
A3
A4
A5
A6
A7
A8
A9
A10/AP
A11
A12
NC
BA2
BA1
BA0
/CK
CK
CKE
/CS
/RAS
/CAS
/WE
LDM
UDM
LDQS
/LDQS
UDQS
/UDQS
ODT
[VREF]
[VDDQ]
[VSSQ]
[VDD]
[VSS]
[VDDL]
[VSSDL]
DQ0 – DQ15
FRC_DDRDQ00 – FRC_DDRDQ15
520:4B
R5066
V01GD0001743
RK73B1ETTP331J
330 (J) 63mW 1005
R5064
R5065
V01GD0002060
ERJ2GEJ470X
47 (J) 63mW 1005
C5044
V03GD0000771
UMK105CH470JW-F
47pF (J) 50V
C5025
GRM31CB30J476ME18L
47uF M 6.3V
C5026
C5027
C5049
V03GD0001143
CM105X5R475M06AT
4.7uF M 6.3V 1608
C5028 – C5046
V03GD0000786
CM05B104K10AH
100nF (K) 10V 1005
C5047
C5048
C5051
C5052
V03GD0001181
GRM31CB30J476ME18L
47uF M 6.3V 3216
C5053
V03GD0001131
GRM21BB30J226ME38L
22uF M 6.3V 2012
C5050
FRC_V1P8VCC
(VDDL)
(VDD)
(VSS)
(VSSDL)
GND
1 2 3 4 5 6 7 8
A B C D E F

U04AS SIGNAL [506] DDR2_1 (PE0399)

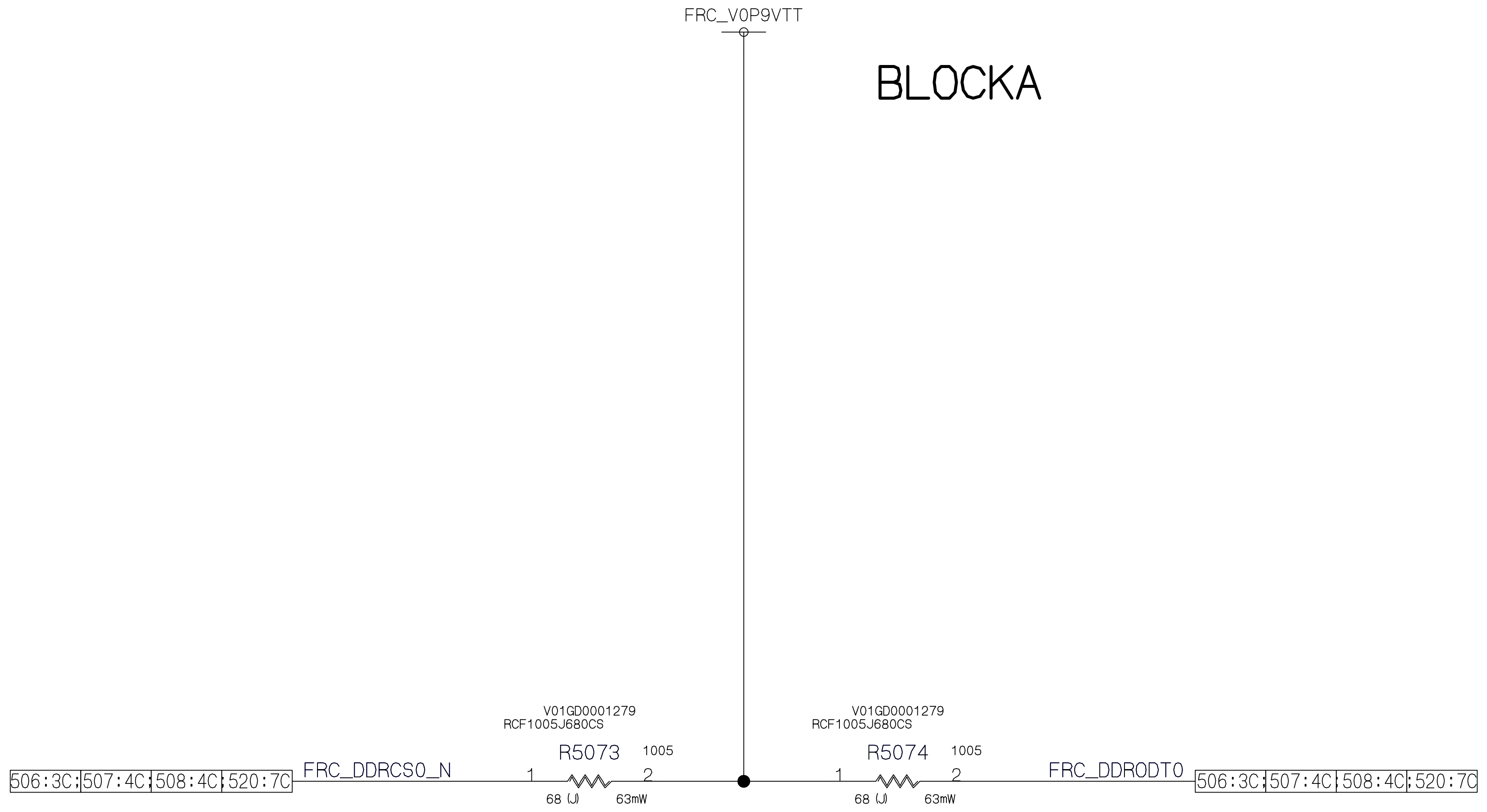

DDR2
VV46A00004400に置き換え済み
[MDSD32M16B]
IC510
506:3A 508:4A 509:5B 520:7C FRC_DDRA00 M8 A0
506:3A 508:4A 509:8B 520:7C FRC_DDRA01 M3 A1
506:3A 508:4A 509:5B 520:7C FRC_DDRA02 M7 A2
506:3A 508:4A 509:8B 520:7C FRC_DDRA03 N2 A3
506:3A 508:4A 509:5B 520:7C FRC_DDRA04 N8 A4
506:3A 508:4A 509:8B 520:7D FRC_DDRA05 N3 A5
506:3A 508:4A 509:5B 520:7D FRC_DDRA06 N7 A6
506:3B 508:4B 509:8B 520:7D FRC_DDRA07 P2 A7
506:3B 508:4B 509:5B 520:7D FRC_DDRA08 P8 A8
506:3B 508:4B 509:8B 520:7D FRC_DDRA09 P3 A9
506:3B 508:4B 509:5C 520:7D FRC_DDRA10 M2 A10/AP
506:3B 508:4B 509:8C 520:7D FRC_DDRA11 P7 A11
506:3B 508:4B 509:5C 520:7D FRC_DDRA12 R2 A12
R8 NC
R3 NC
R7 NC
506:3B 508:4B 509:8C 520:7D FRC_DDRBA2 L1 ~~NC~~ BA2
506:3B 508:4B 509:5C 520:7D FRC_DDRBA1 L3 BA1
506:3B 508:4B 509:8C 520:7D FRC_DDRBA0 L2 BA0
506:3B 508:4B 520:7B FRC_DDRCLK_N K8 /CK
R5069
1 2
V01GD0001743
RK73B1ETTP331J
330 (J) 63mW 1005
506:3C 508:4C 520:7B FRC_DDRCLK J8 CK
506:3C 508:4C 509:8C 520:7B FRC_DDRCKE K2 CKE
V01GD0002060
R5067 1005
1 2
ERJ2GEJ470X
47 (J) 63mW
GND
V01GD0002060
R5068 1005
1 2
ERJ2GEJ470X
47 (J) 63mW
GND
506:3C 508:4C 509:1C 520:7C FRC_DDRCS0_N L8 /CS
506:3C 508:4C 509:5C 520:7C FRC_DDRRAS_N K7 /RAS
506:3C 508:4C 509:8C 520:7C FRC_DDRCAS_N L7 /CAS
506:3C 508:4C 509:5C 520:7C FRC_DDRWE_N K3 /WE
520:4C FRC_DDRDM2 F3 LDM
520:4C FRC_DDRDM3 B3 UDM
520:4D FRC_DDRDQS2 F7 LDQS
E8 /LDQS
520:4D FRC_DDRDQS3 B7 UDQS
A8 /UDQS
506:3C 508:4C 509:4C 520:7C FRC_DDRODT0 K9 ODT
DQ0 G8 FRC_DDRDQ16 520:4C
DQ1 G2 FRC_DDRDQ17 520:4C
DQ2 H7 FRC_DDRDQ18 520:4C
DQ3 H3 FRC_DDRDQ19 520:4C
DQ4 H1 FRC_DDRDQ20 520:4C
DQ5 H9 FRC_DDRDQ21 520:4C
DQ6 F1 FRC_DDRDQ22 520:4C
DQ7 F9 FRC_DDRDQ23 520:4C
DQ8 C8 FRC_DDRDQ24 520:4C
DQ9 C2 FRC_DDRDQ25 520:4D
DQ10 D7 FRC_DDRDQ26 520:4D
DQ11 D3 FRC_DDRDQ27 520:4C
DQ12 D1 FRC_DDRDQ28 520:4C
DQ13 D9 FRC_DDRDQ29 520:4C
DQ14 B1 FRC_DDRDQ30 520:4C
DQ15 B9 FRC_DDRDQ31 520:4D
520:4C
506:1C 508:3C 511:7C 520:1B FRC_V0P9VREF
V03GD0000771 C5073 2 1 1005 UMK105CH470JW-F 47pF (J) 50V
GND
A2 NC
E2 NC
J2 [VREF]
A9 [VDDQ] [VSSQ] A7
C1 [VDDQ] [VSSQ] B2
C3 [VDDQ] [VSSQ] B8
C7 [VDDQ] [VSSQ] D2
C9 [VDDQ] [VSSQ] D8
E9 [VDDQ] [VSSQ] E7
G1 [VDDQ] [VSSQ] F2
G3 [VDDQ] [VSSQ] F8
G7 [VDDQ] [VSSQ] H2
G9 [VDDQ] [VSSQ] H8
A1 [VDD] [VSS] A3
E1 [VDD] [VSS] E3
J9 [VDD] [VSS] J3
M9 [VDD] [VSS] N1
R1 [VDD] [VSS] P9
J1 [VDDL] [VSSDL] J7
FRC_V1P8VCC
V03GD0001181 C5054 2 1 3216 GRM31CB30J476ME18L 47uF (M) 6.3V
V03GD0001143 CM105X5R475M06AT C5055 2 1 1608 6.3V 4.7uF (M)
V03GD0000786 C5057 2 1 1005 CM05B104K10AH 100nF (K) 10V
V03GD0000786 C5059 2 1 1005 CM05B104K10AH 100nF (K) 10V
V03GD0000786 C5061 2 1 1005 CM05B104K10AH 100nF (K) 10V
V03GD0000786 C5063 2 1 1005 CM05B104K10AH 100nF (K) 10V
V03GD0000786 C5065 2 1 1005 CM05B104K10AH 100nF (K) 10V
V03GD0000786 C5067 2 1 1005 CM05B104K10AH 100nF (K) 10V
V03GD0000786 C5069 2 1 1005 CM05B104K10AH 100nF (K) 10V
V03GD0000786 C5071 2 1 1005 CM05B104K10AH 100nF (K) 10V
V03GD0000786 C5074 2 1 1005 CM05B104K10AH 100nF (K) 10V
V03GD0000786 C5075 2 1 1005 CM05B104K10AH 100nF (K) 10V
GND
FRC_V1P8VCC
V03GD0001143 CM105X5R475M06AT C5056 2 1 1608 6.3V 4.7uF (M)
V03GD0000786 C5058 2 1 1005 CM05B104K10AH 100nF (K) 10V
V03GD0000786 C5060 2 1 1005 CM05B104K10AH 100nF (K) 10V
V03GD0000786 C5062 2 1 1005 CM05B104K10AH 100nF (K) 10V
V03GD0000786 C5064 2 1 1005 CM05B104K10AH 100nF (K) 10V
V03GD0000786 C5066 2 1 1005 CM05B104K10AH 100nF (K) 10V
V03GD0000786 C5068 2 1 1005 CM05B104K10AH 100nF (K) 10V
V03GD0000786 C5070 2 1 1005 CM05B104K10AH 100nF (K) 10V
V03GD0000786 C5072 2 1 1005 CM05B104K10AH 100nF (K) 10V
GND
V03GD0001143 CM105X5R475M06AT C5076 2 1 1608 6.3V 4.7uF (M)
V03GD0000786 C5077 2 1 1005 CM05B104K10AH 100nF (K) 10V
GND

U04AS SIGNAL [507] DDR2_2 (PE0399)

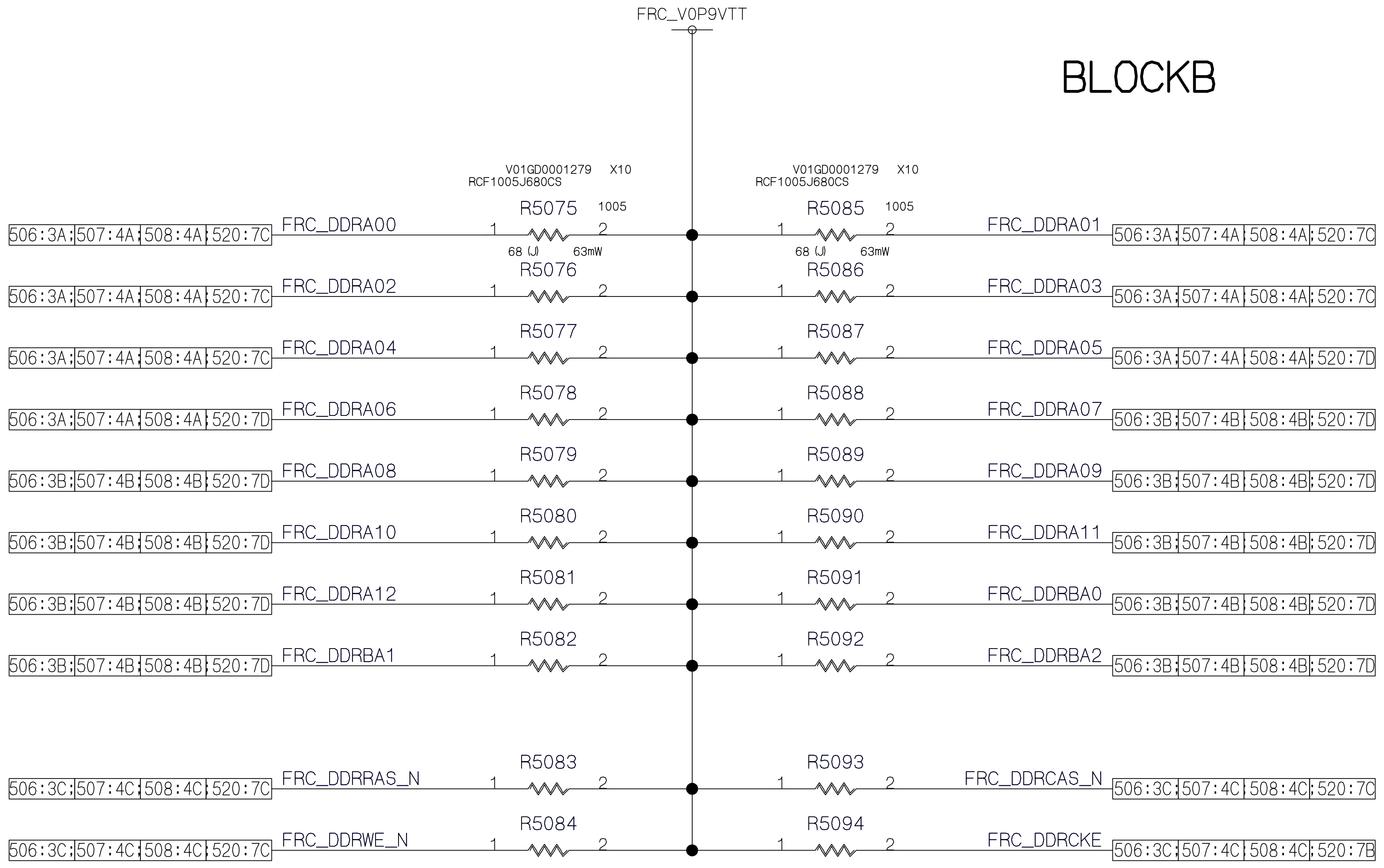

VV46A00004400に置き換え済み
DDR3
[MDSD32M16B]
IC511
506:3A;507:4A;509:5B;520:7C FRC_DDRA00 M8 A0
506:3A;507:4A;509:8B;520:7C FRC_DDRA01 M3 A1
506:3A;507:4A;509:5B;520:7C FRC_DDRA02 M7 A2
506:3A;507:4A;509:8B;520:7C FRC_DDRA03 N2 A3
506:3A;507:4A;509:5B;520:7C FRC_DDRA04 N8 A4
506:3A;507:4A;509:8B;520:7D FRC_DDRA05 N3 A5
506:3A;507:4A;509:5B;520:7D FRC_DDRA06 N7 A6
506:3B;507:4B;509:8B;520:7D FRC_DDRA07 P2 A7
506:3B;507:4B;509:5B;520:7D FRC_DDRA08 P8 A8
506:3B;507:4B;509:8B;520:7D FRC_DDRA09 P3 A9
506:3B;507:4B;509:5C;520:7D FRC_DDRA10 M2 A10/AP
506:3B;507:4B;509:8C;520:7D FRC_DDRA11 P7 A11
506:3B;507:4B;509:5C;520:7D FRC_DDRA12 R2 A12
R8 NC
R3 NC
R7 NC
506:3B;507:4B;509:8C;520:7D FRC_DDRBA2 L1 ~~NC~~ BA2
506:3B;507:4B;509:5C;520:7D FRC_DDRBA1 L3 BA1
506:3B;507:4B;509:8C;520:7D FRC_DDRBA0 L2 BA0
506:3B;507:4B;520:7B FRC_DDRCLK_N K8 /CK
R5072
1 2
V01GD0001743
RK73B1ETTP331J
330 (J) 63mW 1005
506:3C;507:4C;520:7B FRC_DDRCLK J8 CK
506:3C;507:4C;509:8C;520:7B FRC_DDRCKE K2 CKE
V01GD0002060
R5070 1005
1 2
ERJ2GEJ470X
47 (J) 63mW
V01GD0002060
R5071 1005
1 2
ERJ2GEJ470X
47 (J) 63mW
GND
GND
506:3C;507:4C;509:1C;520:7C FRC_DDRCS0_N L8 /CS
506:3C;507:4C;509:5C;520:7C FRC_DDRRAS_N K7 /RAS
506:3C;507:4C;509:8C;520:7C FRC_DDRCAS_N L7 /CAS
506:3C;507:4C;509:5C;520:7C FRC_DDRWE_N K3 /WE
520:4D FRC_DDRDM4 F3 LDM
520:4D FRC_DDRDM5 B3 UDM
520:4E FRC_DDRDQS4 F7 LDQS
E8 /LDQS
520:4E FRC_DDRDQS5 B7 UDQS
A8 /UDQS
506:3C;507:4C;509:4C;520:7C FRC_DDRODT0 K9 ODT
DQ0 G8 FRC_DDRDQ32 520:4D
DQ1 G2 FRC_DDRDQ33 520:4D
DQ2 H7 FRC_DDRDQ34 520:4D
DQ3 H3 FRC_DDRDQ35 520:4D
DQ4 H1 FRC_DDRDQ36 520:4D
DQ5 H9 FRC_DDRDQ37 520:4D
DQ6 F1 FRC_DDRDQ38 520:4D
DQ7 F9 FRC_DDRDQ39 520:4D
DQ8 C8 FRC_DDRDQ40 520:4E
DQ9 C2 FRC_DDRDQ41 520:4E
DQ10 D7 FRC_DDRDQ42 520:4E
DQ11 D3 FRC_DDRDQ43 520:4E
DQ12 D1 FRC_DDRDQ44 520:4E
DQ13 D9 FRC_DDRDQ45 520:4D
DQ14 B1 FRC_DDRDQ46 520:4E
DQ15 B9 FRC_DDRDQ47 520:4E
506:1C;507:2C;511:7C;520:1B FRC_V0P9VREF
V03GD0000771
C5097
UMK105CH470JW-F
47pF (J) 50V
1005
GND
A2 NC
E2 NC
J2 [VREF]
A9 C1 C3 C7 C9 E9 G1 G3 G7 G9 [VDDQ]
[VSSQ] A7 B2 B8 D2 D8 E7 F2 F8 H2 H8
A1 E1 J9 M9 R1 [VDD]
[VSS] A3 E3 J3 N1 P9
J1 [VDDL]
[VSSDL] J7
FRC_V1P8VCC
V03GD0001181
C5078
GRM31CB30J476ME18L
47uF (M) 6.3V
3216
V03GD0001143
C5079
CM105X5R475M06AT
4.7uF (M) 6.3V
1608
V03GD0000786
C5081 C5083 C5085 C5087 C5089 C5091 C5093 C5095 C5098 C5099
CM05B104K10AH
100nF (K) 10V
1005
GND
FRC_V1P8VCC
V03GD0001143
C5080
CM105X5R475M06AT
4.7uF (M) 6.3V
1608
V03GD0000786
C5082 C5084 C5086 C5088 C5090 C5092 C5094 C5096
CM05B104K10AH
100nF (K) 10V
1005
GND
V03GD0001143
C5100
CM105X5R475M06AT
4.7uF (M) 6.3V
1608
V03GD0000786
C5101
CM05B104K10AH
100nF (K) 10V
1005
GND

U04AS SIGNAL [508] DDR2_3 (PE0399)

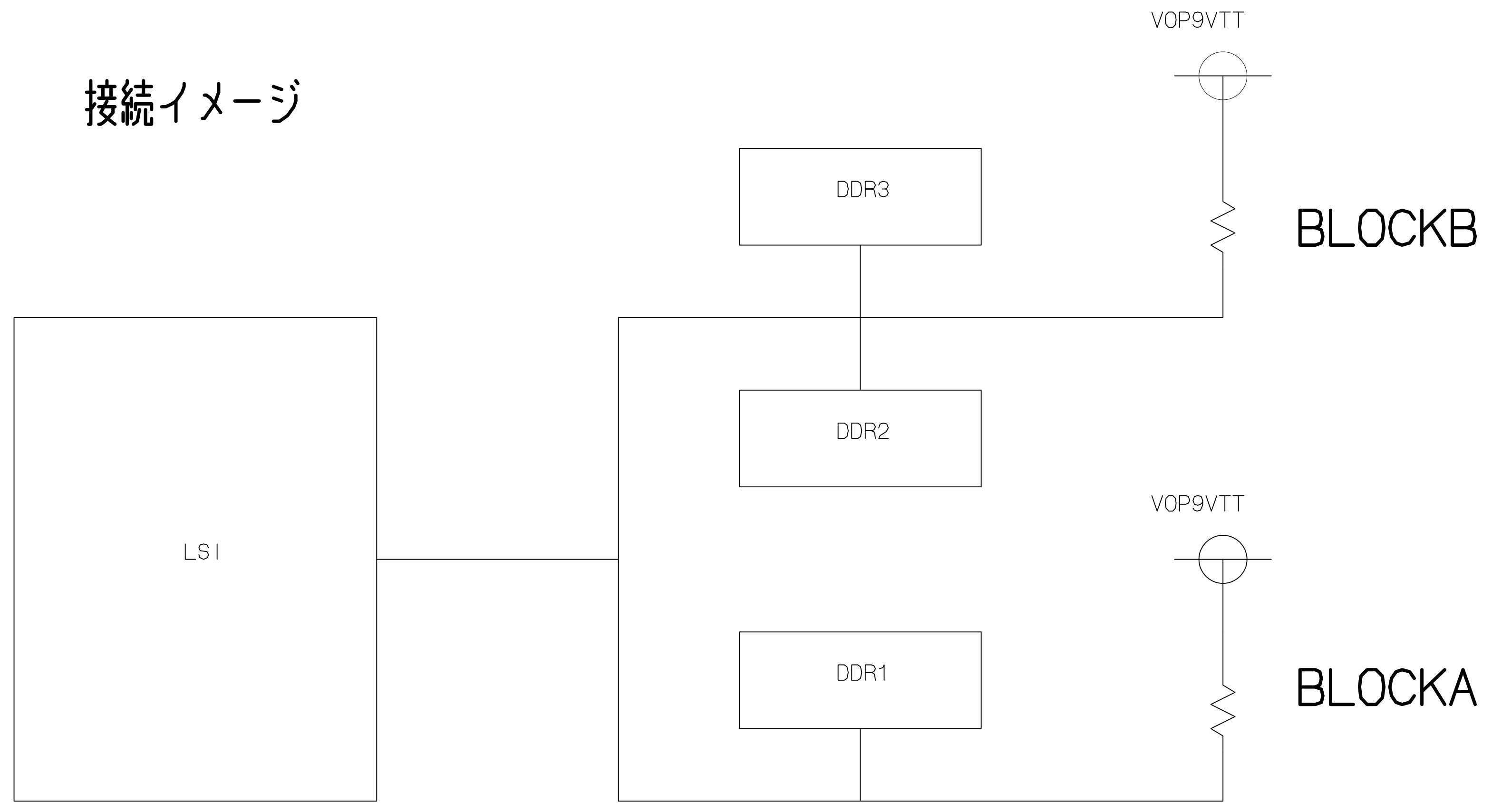
DDR1に接続


FRC_V0P9VTT
BLOCKA
V01GD0001279
RCF1005J680CS
R5073
1005
1
2
68 (J)
63mW
V01GD0001279
RCF1005J680CS
R5074
1005
1
2
68 (J)
63mW
506:3C;507:4C;508:4C;520:7C
FRC_DDRCS0_N
FRC_DDRODT0
506:3C;507:4C;508:4C;520:7C

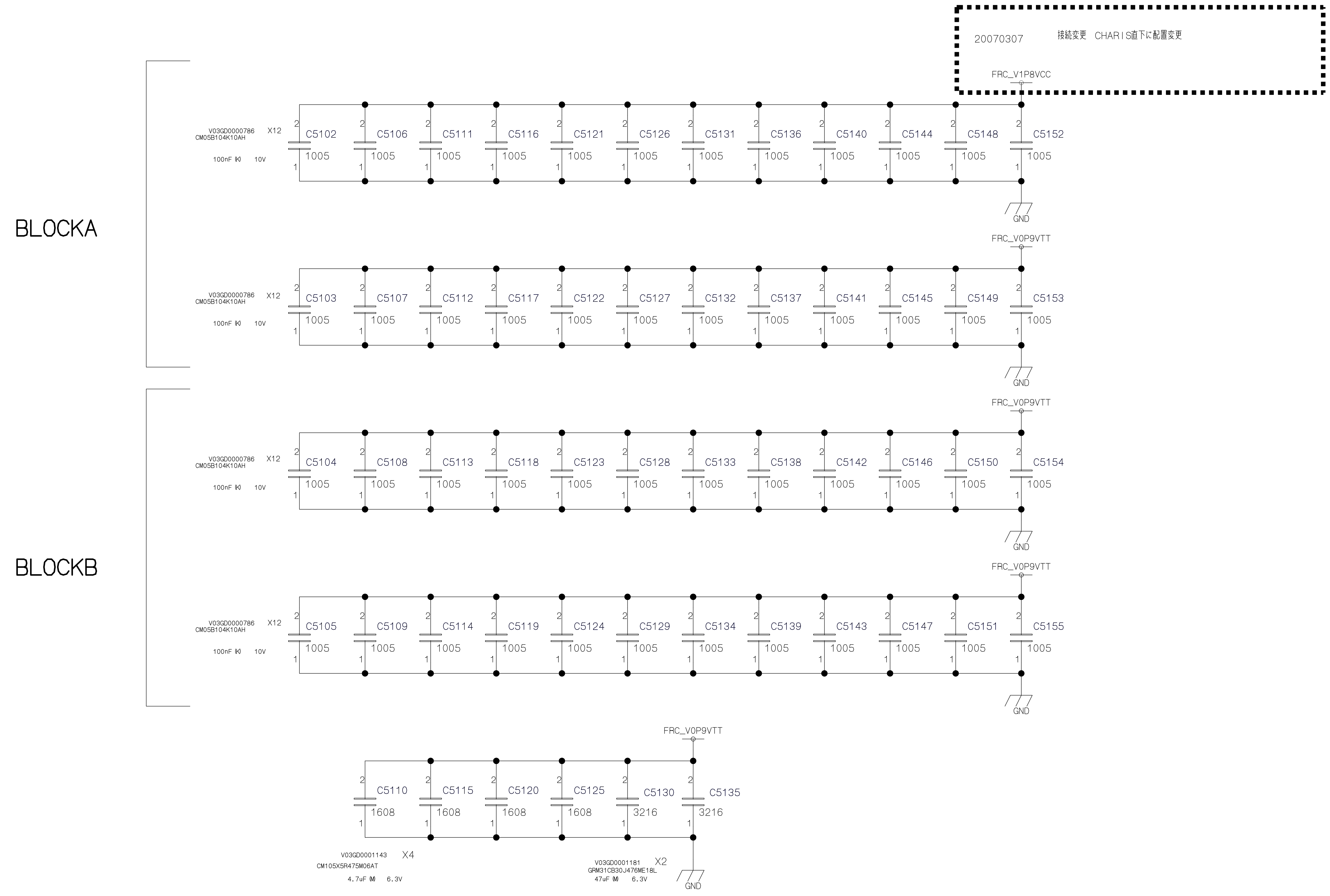
DDR2/3に接続


FRC_V0P9VTT
BLOCKB
V01GD0001279 X10
RCF1005J680CS
V01GD0001279 X10
RCF1005J680CS
R5075 1005
R5085 1005
68 (J) 63mW
68 (J) 63mW
506:3A;507:4A;508:4A;520:7C FRC_DDRA00
FRC_DDRA01 506:3A;507:4A;508:4A;520:7C
R5076
R5086
506:3A;507:4A;508:4A;520:7C FRC_DDRA02
FRC_DDRA03 506:3A;507:4A;508:4A;520:7C
R5077
R5087
506:3A;507:4A;508:4A;520:7C FRC_DDRA04
FRC_DDRA05 506:3A;507:4A;508:4A;520:7D
R5078
R5088
506:3A;507:4A;508:4A;520:7D FRC_DDRA06
FRC_DDRA07 506:3B;507:4B;508:4B;520:7D
R5079
R5089
506:3B;507:4B;508:4B;520:7D FRC_DDRA08
FRC_DDRA09 506:3B;507:4B;508:4B;520:7D
R5080
R5090
506:3B;507:4B;508:4B;520:7D FRC_DDRA10
FRC_DDRA11 506:3B;507:4B;508:4B;520:7D
R5081
R5091
506:3B;507:4B;508:4B;520:7D FRC_DDRA12
FRC_DDRBA0 506:3B;507:4B;508:4B;520:7D
R5082
R5092
506:3B;507:4B;508:4B;520:7D FRC_DDRBA1
FRC_DDRBA2 506:3B;507:4B;508:4B;520:7D
R5083
R5093
506:3C;507:4C;508:4C;520:7C FRC_DDRRAS_N
FRC_DDRCAS_N 506:3C;507:4C;508:4C;520:7C
R5084
R5094
506:3C;507:4C;508:4C;520:7C FRC_DDRWE_N
FRC_DDRCKE 506:3C;507:4C;508:4C;520:7B
1
2

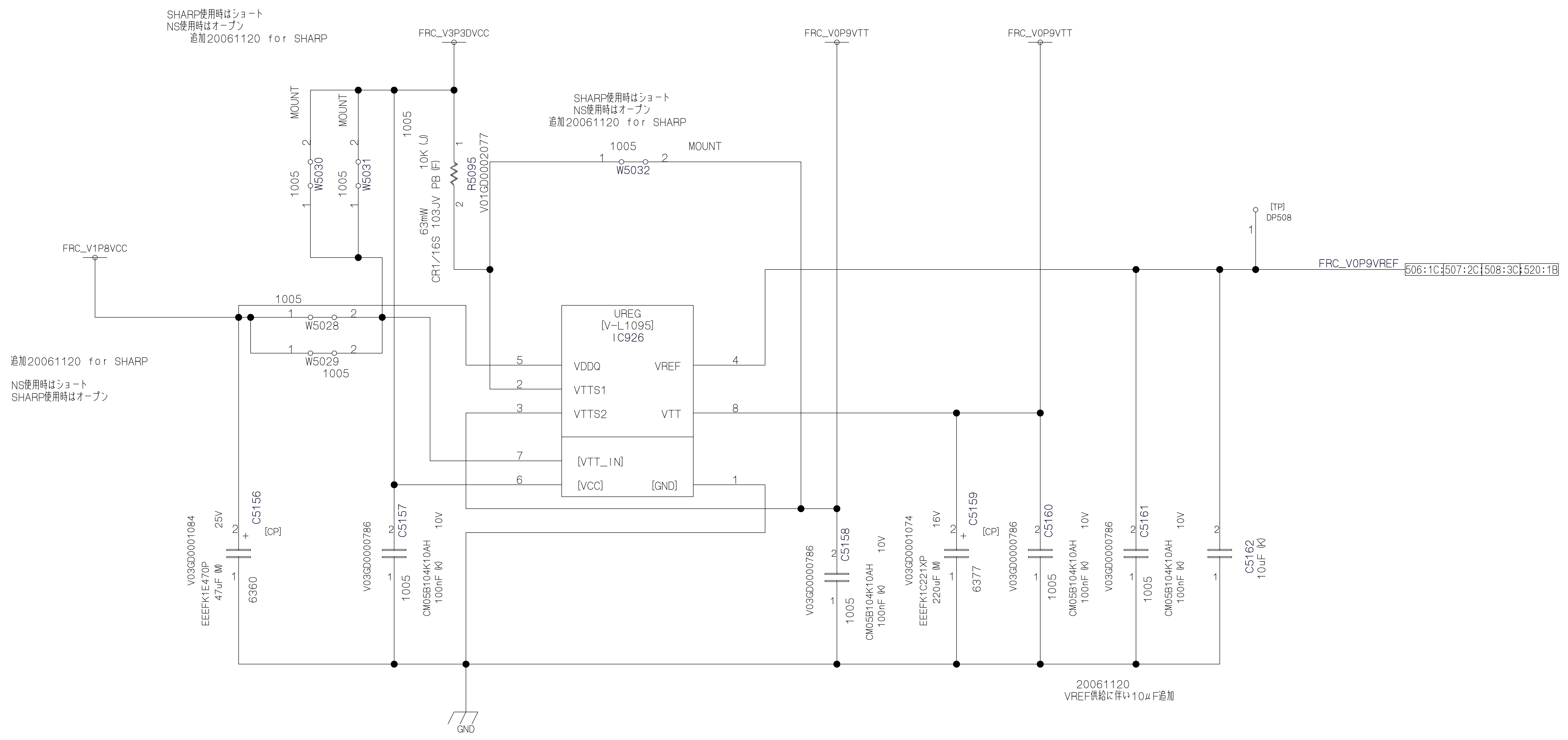
接続イメージ


V0P9VTT
DDR3
BLOCKB
DDR2
LSI
V0P9VTT
DDR1
BLOCKA

U04AS SIGNAL [509] DDR2_4 (PE0399)

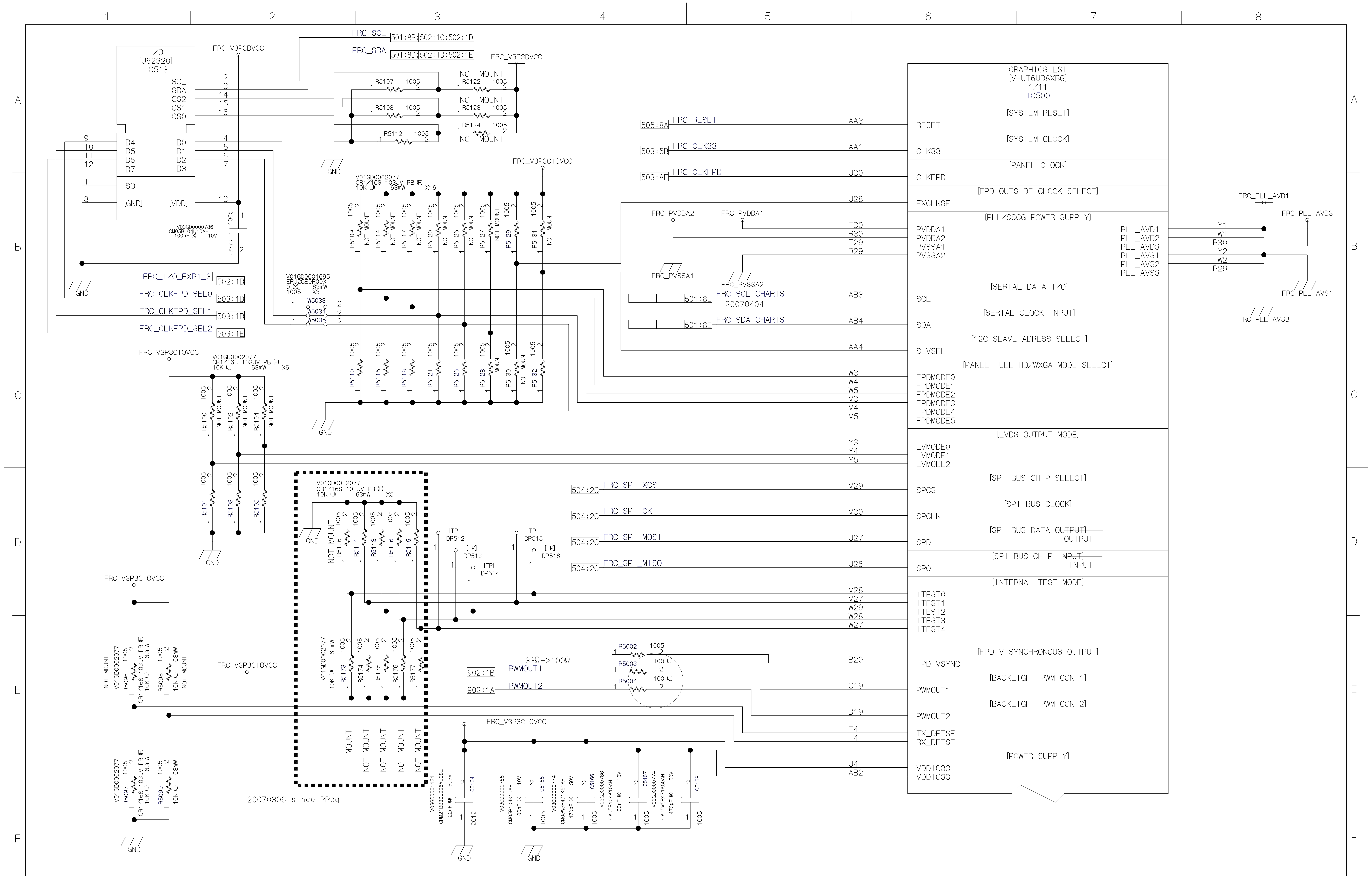

20070307
接続変更　CHARIS直下に配置変更
FRC_V1P8VCC
BLOCKA
V03GD0000786 X12
CM05B104K10AH
100nF (K) 10V
C5102 C5106 C5111 C5116 C5121 C5126 C5131 C5136 C5140 C5144 C5148 C5152
1005
GND
FRC_V0P9VTT
V03GD0000786 X12
CM05B104K10AH
100nF (K) 10V
C5103 C5107 C5112 C5117 C5122 C5127 C5132 C5137 C5141 C5145 C5149 C5153
1005
GND
BLOCKB
FRC_V0P9VTT
V03GD0000786 X12
CM05B104K10AH
100nF (K) 10V
C5104 C5108 C5113 C5118 C5123 C5128 C5133 C5138 C5142 C5146 C5150 C5154
1005
GND
FRC_V0P9VTT
V03GD0000786 X12
CM05B104K10AH
100nF (K) 10V
C5105 C5109 C5114 C5119 C5124 C5129 C5134 C5139 C5143 C5147 C5151 C5155
1005
GND
FRC_V0P9VTT
C5110 C5115 C5120 C5125
1608
C5130 C5135
3216
V03GD0001143 X4
CM105X5R475M06AT
4.7uF (M) 6.3V
V03GD0001181 X2
GRM31CB30J476ME18L
47uF (M) 6.3V
GND
1
2

U04AS SIGNAL [510] DDR2_5 (PE0399)

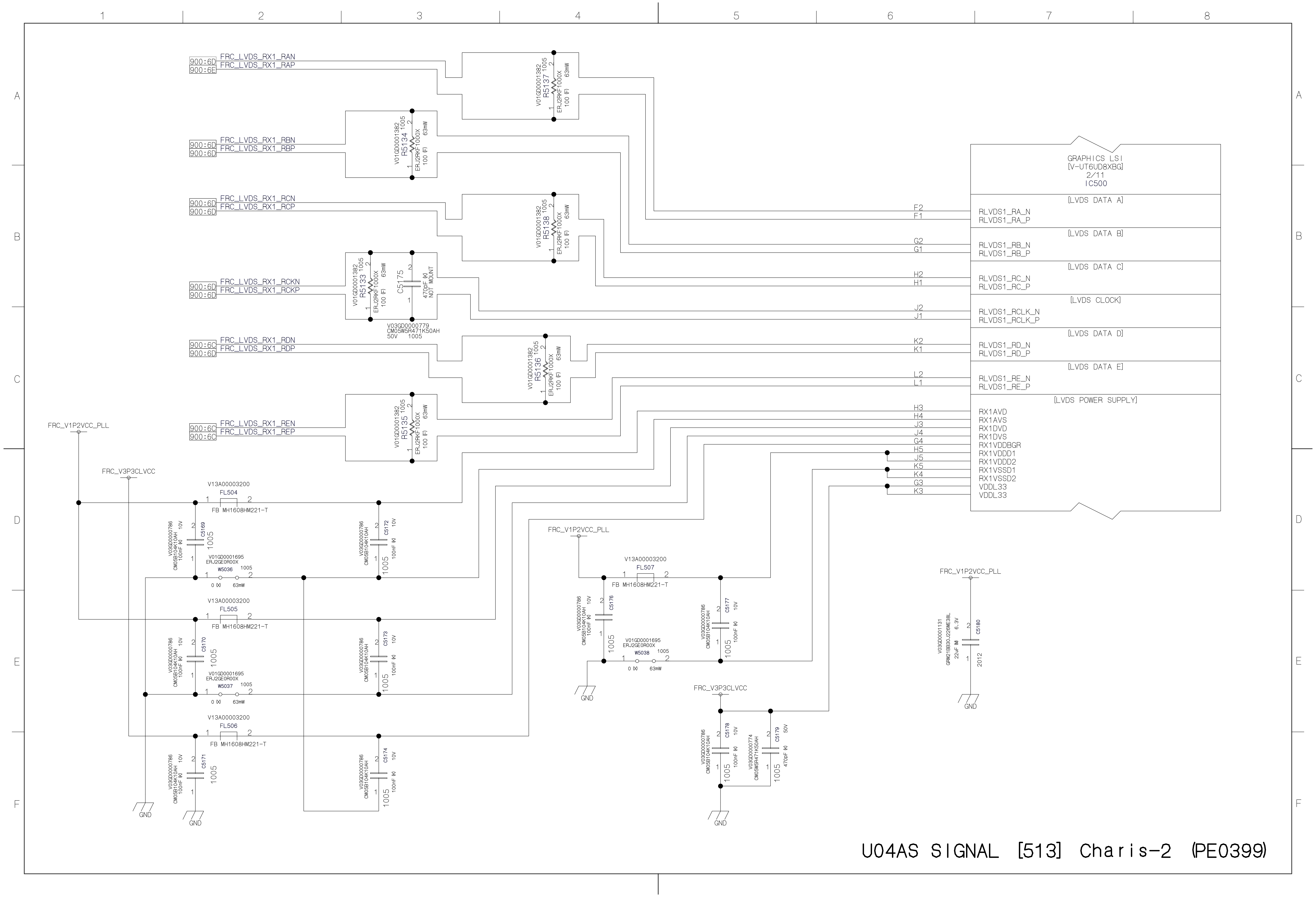

SHARP使用時はショート
NS使用時はオープン
追加20061120 for SHARP
FRC_V3P3DVCC
FRC_V0P9VTT
FRC_V0P9VTT
SHARP使用時はショート
NS使用時はオープン
追加20061120 for SHARP
MOUNT
MOUNT
W5030
W5031
1005
1005
1005
10K (J)
CR1/16S 103JV PB (F)
63mW
R5095
V01GD0002077
1005
MOUNT
W5032
[TP]
DP508
FRC_V1P8VCC
FRC_V0P9VREF
506:1C
507:2C
508:3C
520:1B
1005
W5028
W5029
1005
UREG
[V-L1095]
IC926
VDDQ
VTTS1
VTTS2
[VTT_IN]
[VCC]
VREF
VTT
[GND]
追加20061120 for SHARP
NS使用時はショート
SHARP使用時はオープン
C5156
[CP]
25V
V03GD0001084
EEEFK1E470P
47uF (M)
6360
C5157
V03GD0000786
1005
CM05B104K10AH
100nF (K)
10V
C5158
V03GD0000786
1005
CM05B104K10AH
100nF (K)
10V
C5159
[CP]
16V
V03GD0001074
EEEFK1C221XP
220uF (M)
6377
C5160
V03GD0000786
1005
CM05B104K10AH
100nF (K)
10V
C5161
V03GD0000786
1005
CM05B104K10AH
100nF (K)
10V
C5162
10uF (K)
GND
20061120
VREF供給に伴い10μF追加

+1.8V→0.9V

U04AS SIGNAL [511] TERMINATION-REG (PE0399)

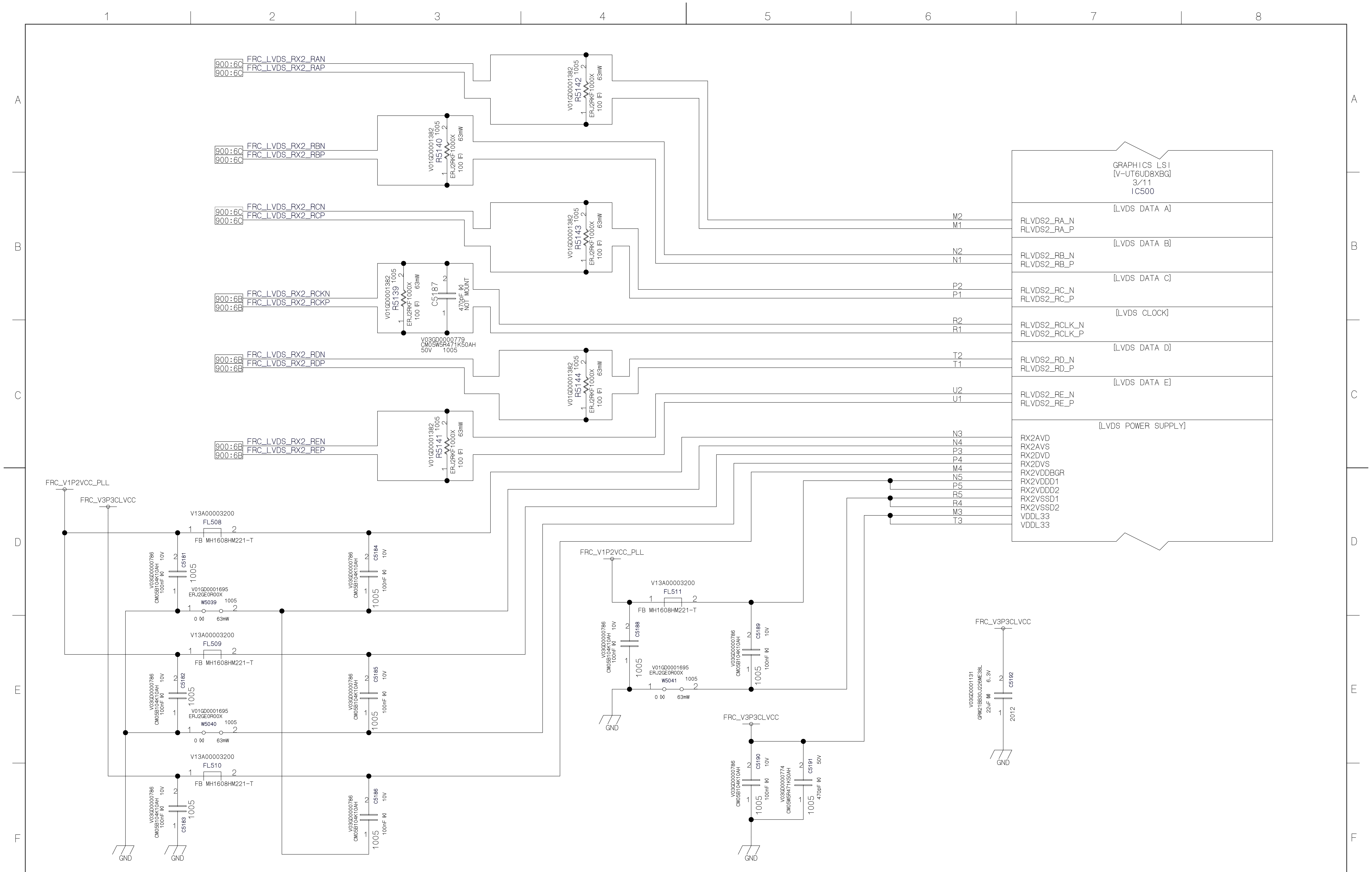

I/O
[U62320]
IC513
SCL
SDA
CS2
CS1
CS0
D4
D5
D6
D7
D0
D1
D2
D3
S0
[GND]
[VDD]
FRC_V3P3DVCC
FRC_SCL
FRC_SDA
501:8B 502:1C 502:1D
501:8D 502:1D 502:1E
NOT MOUNT
R5107
R5108
R5112
R5122
R5123
R5124
GND
V03GD0000786
CM05B104K10AH
100nF (K) 10V
C5163
FRC_I/0_EXP1_3
502:1D
FRC_CLKFPD_SEL0
503:1D
FRC_CLKFPD_SEL1
503:1D
FRC_CLKFPD_SEL2
503:1E
V01GD0001695
ERJ2GE0R00X
0 Ω 63mW
1005 X3
W5033
W5034
W5035
FRC_V3P3CIOVCC
V01GD0002077
CR1/16S 103JV PB (F)
10K (J) 63mW X16
R5109
R5114
R5117
R5120
R5125
R5127
R5129
R5131
R5110
R5115
R5118
R5121
R5126
R5128
R5130
R5132
X6
R5100
R5102
R5104
R5101
R5103
R5105
X5
R5106
R5111
R5113
R5116
R5119
R5173
R5174
R5175
R5176
R5177
MOUNT
NOT MOUNT
R5096
R5097
R5098
R5099
20070306 since PPeq
[TP]
DP512
DP513
DP514
DP515
DP516
33Ω->100Ω
R5002
R5003
R5004
100 (J)
902:1B
902:1A
PWMOUT1
PWMOUT2
C5164
C5165
C5166
C5167
C5168
GRM21BB30J226ME38L
22uF (M) 6.3V
2012
CM05B104K10AH
100nF (K) 10V
V03GD0000774
CM05HG471K50AH
470pF (K) 50V
505:8A
FRC_RESET
503:5B
FRC_CLK33
503:8E
FRC_CLKFPD
FRC_PVDDA2
FRC_PVDDA1
FRC_PVSSA1
FRC_PVSSA2
501:8E
FRC_SCL_CHARIS
20070404
FRC_SDA_CHARIS
504:2C
FRC_SPI_XCS
FRC_SPI_CK
FRC_SPI_MOSI
FRC_SPI_MISO
AA3
AA1
U30
U28
T30
R30
T29
R29
AB3
AB4
AA4
W3
W4
W5
V3
V4
V5
Y3
Y4
Y5
V29
V30
U27
U26
V28
V27
W29
W28
W27
B20
C19
D19
F4
T4
U4
AB2
GRAPHICS LSI
[V-UT6UD8XBG]
1/11
IC500
[SYSTEM RESET]
RESET
[SYSTEM CLOCK]
CLK33
[PANEL CLOCK]
CLKFPD
[FPD OUTSIDE CLOCK SELECT]
EXCLKSEL
[PLL/SSCG POWER SUPPLY]
PVDDA1
PVDDA2
PVSSA1
PVSSA2
PLL_AVD1
PLL_AVD2
PLL_AVD3
PLL_AVS1
PLL_AVS2
PLL_AVS3
[SERIAL DATA I/O]
SCL
[SERIAL CLOCK INPUT]
SDA
[12C SLAVE ADRESS SELECT]
SLVSEL
[PANEL FULL HD/WXGA MODE SELECT]
FPDMODE0
FPDMODE1
FPDMODE2
FPDMODE3
FPDMODE4
FPDMODE5
[LVDS OUTPUT MODE]
LVMODE0
LVMODE1
LVMODE2
[SPI BUS CHIP SELECT]
SPCS
[SPI BUS CLOCK]
SPCLK
[SPI BUS DATA ~~OUTPUT~~] OUTPUT
SPD
[SPI BUS CHIP ~~INPUT~~] INPUT
SPQ
[INTERNAL TEST MODE]
ITEST0
ITEST1
ITEST2
ITEST3
ITEST4
[FPD V SYNCHRONOUS OUTPUT]
FPD_VSYNC
[BACKLIGHT PWM CONT1]
PWMOUT1
[BACKLIGHT PWM CONT2]
PWMOUT2
TX_DETSEL
RX_DETSEL
[POWER SUPPLY]
VDDIO33
VDDIO33
Y1
W1
P30
Y2
W2
P29
FRC_PLL_AVD1
FRC_PLL_AVD3
FRC_PLL_AVS1
FRC_PLL_AVS3
1
2
3
4
5
6
7
8
A
B
C
D
E
F

U04AS SIGNAL [512] Charis-1 (PE0399)



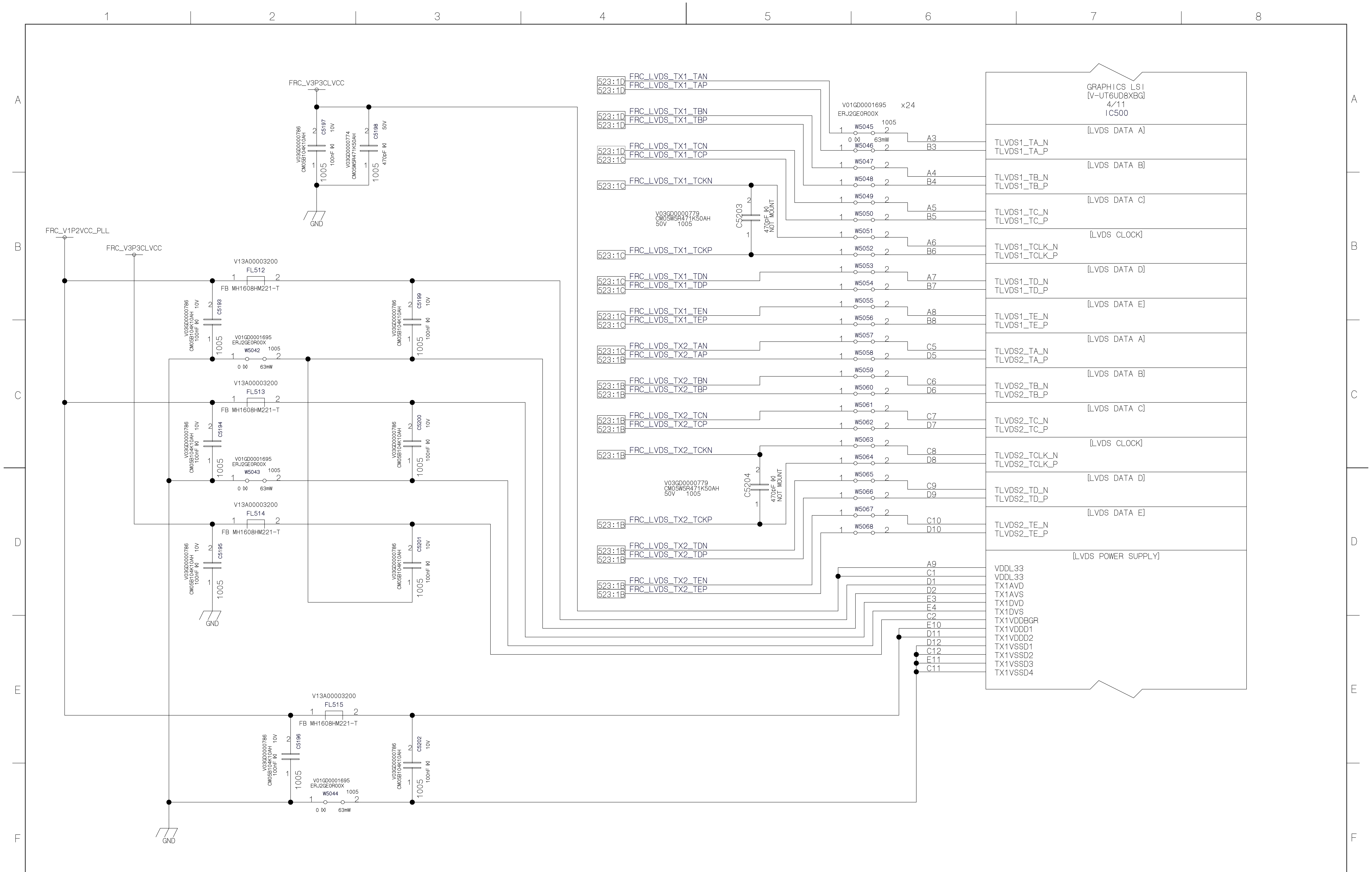
1
2
3
4
5
6
7
8
A
B
C
D
E
F
900:6D FRC_LVDS_RX1_RAN
900:6E FRC_LVDS_RX1_RAP
900:6D FRC_LVDS_RX1_RBN
900:6D FRC_LVDS_RX1_RBP
900:6D FRC_LVDS_RX1_RCN
900:6D FRC_LVDS_RX1_RCP
900:6D FRC_LVDS_RX1_RCKN
900:6D FRC_LVDS_RX1_RCKP
900:6C FRC_LVDS_RX1_RDN
900:6D FRC_LVDS_RX1_RDP
900:6C FRC_LVDS_RX1_REN
900:6C FRC_LVDS_RX1_REP
V01GD0001382 R5137 1005 ERJ2RKF1000X 100 (F) 63mW
V01GD0001382 R5134 1005 ERJ2RKF1000X 100 (F) 63mW
V01GD0001382 R5138 1005 ERJ2RKF1000X 100 (F) 63mW
V01GD0001382 R5133 1005 ERJ2RKF1000X 100 (F) 63mW
C5175 470pF (K) NOT MOUNT
V03GD0000779 CM05W5R471K50AH 50V 1005
V01GD0001382 R5136 1005 ERJ2RKF1000X 100 (F) 63mW
V01GD0001382 R5135 1005 ERJ2RKF1000X 100 (F) 63mW
GRAPHICS LSI
[V-UT6UD8XBG]
2/11
IC500
[LVDS DATA A]
F2 RLVDS1_RA_N
F1 RLVDS1_RA_P
[LVDS DATA B]
G2 RLVDS1_RB_N
G1 RLVDS1_RB_P
[LVDS DATA C]
H2 RLVDS1_RC_N
H1 RLVDS1_RC_P
[LVDS CLOCK]
J2 RLVDS1_RCLK_N
J1 RLVDS1_RCLK_P
[LVDS DATA D]
K2 RLVDS1_RD_N
K1 RLVDS1_RD_P
[LVDS DATA E]
L2 RLVDS1_RE_N
L1 RLVDS1_RE_P
[LVDS POWER SUPPLY]
H3 RX1AVD
H4 RX1AVS
J3 RX1DVD
J4 RX1DVS
G4 RX1VDDBGR
H5 RX1VDDD1
J5 RX1VDDD2
K5 RX1VSSD1
K4 RX1VSSD2
G3 VDDL33
K3 VDDL33
FRC_V1P2VCC_PLL
FRC_V3P3CLVCC
V13A00003200 FL504 FB MH1608HM221-T
V03GD0000786 CM05B104K10AH C5169 100nF (K) 10V 1005
V03GD0000786 CM05B104K10AH C5172 100nF (K) 10V 1005
V01GD0001695 ERJ2GE0R00X W5036 1005 0 (X) 63mW
V13A00003200 FL505 FB MH1608HM221-T
V03GD0000786 CM05B104K10AH C5170 100nF (K) 10V 1005
V03GD0000786 CM05B104K10AH C5173 100nF (K) 10V 1005
V01GD0001695 ERJ2GE0R00X W5037 1005 0 (X) 63mW
V13A00003200 FL506 FB MH1608HM221-T
V03GD0000786 CM05B104K10AH C5171 100nF (K) 10V 1005
V03GD0000786 CM05B104K10AH C5174 100nF (K) 10V 1005
GND
FRC_V1P2VCC_PLL
V13A00003200 FL507 FB MH1608HM221-T
V03GD0000786 CM05B104K10AH C5176 100nF (K) 10V 1005
V03GD0000786 CM05B104K10AH C5177 100nF (K) 10V 1005
V01GD0001695 ERJ2GE0R00X W5038 1005 0 (X) 63mW
GND
FRC_V3P3CLVCC
V03GD0000786 CM05B104K10AH C5178 100nF (K) 10V 1005
V03GD0000774 CM05W5R471K50AH C5179 470pF (K) 50V 1005
GND
FRC_V1P2VCC_PLL
V03GD0001131 GRM21BB30J226ME38L C5180 22uF (M) 6.3V 2012
GND

U04AS SIGNAL [513] Charis-2 (PE0399)

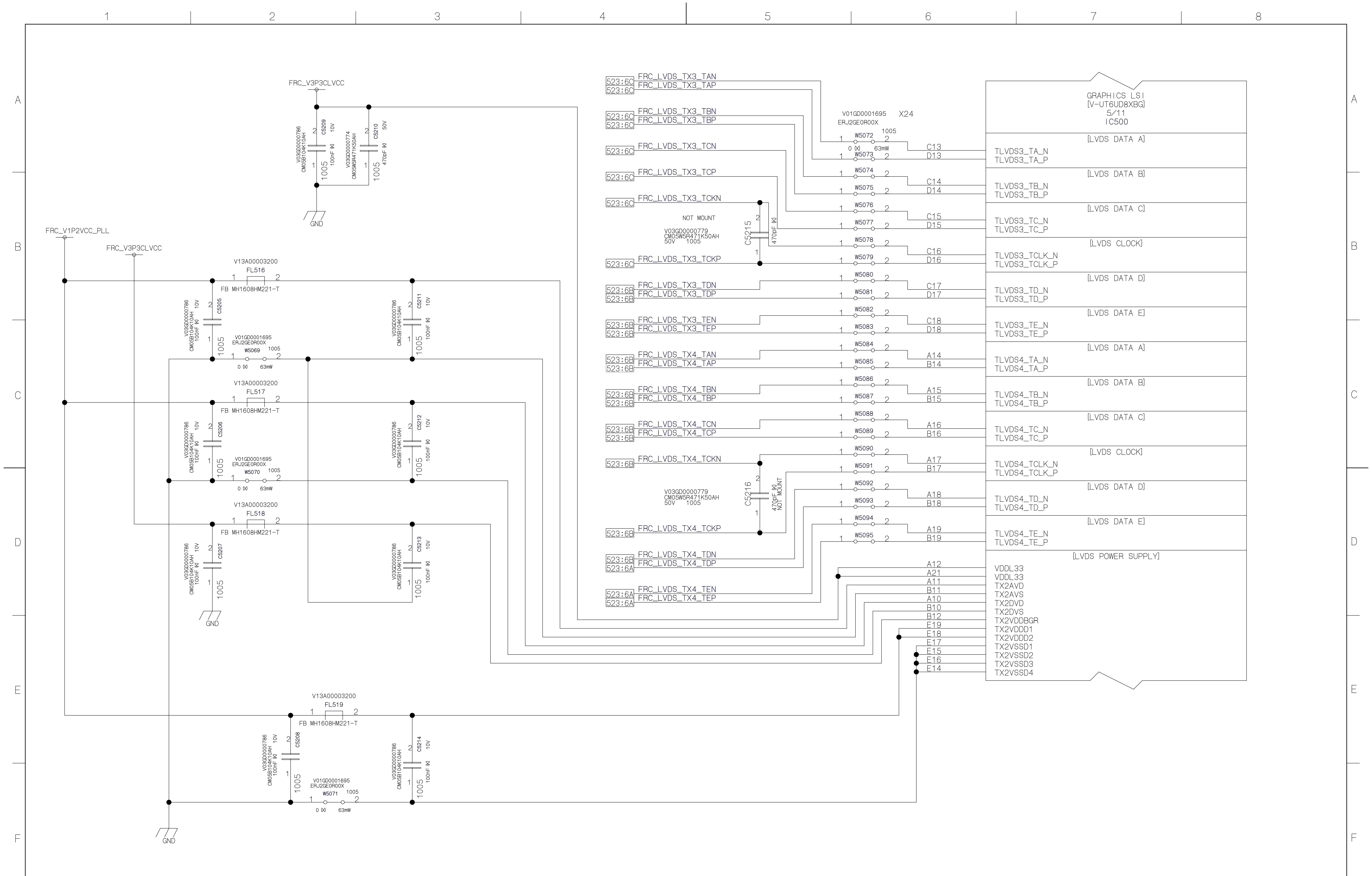


900:6C FRC_LVDS_RX2_RAN
900:6C FRC_LVDS_RX2_RAP
900:6C FRC_LVDS_RX2_RBN
900:6C FRC_LVDS_RX2_RBP
900:6C FRC_LVDS_RX2_RCN
900:6C FRC_LVDS_RX2_RCP
900:6B FRC_LVDS_RX2_RCKN
900:6B FRC_LVDS_RX2_RCKP
900:6B FRC_LVDS_RX2_RDN
900:6B FRC_LVDS_RX2_RDP
900:6B FRC_LVDS_RX2_REN
900:6B FRC_LVDS_RX2_REP
V01GD0001382 R5142 1005 ERJ2RKF1000X 100 (F) 63mW
V01GD0001382 R5140 1005 ERJ2RKF1000X 100 (F) 63mW
V01GD0001382 R5143 1005 ERJ2RKF1000X 100 (F) 63mW
V01GD0001382 R5139 1005 ERJ2RKF1000X 100 (F) 63mW
C5187 470pF (K) NOT MOUNT
V03GD0000779 CM05W5R471K50AH 50V 1005
V01GD0001382 R5144 1005 ERJ2RKF1000X 100 (F) 63mW
V01GD0001382 R5141 1005 ERJ2RKF1000X 100 (F) 63mW
GRAPHICS LSI
[V-UT6UD8XBG]
3/11
IC500
[LVDS DATA A]
M2 RLVDS2_RA_N
M1 RLVDS2_RA_P
[LVDS DATA B]
N2 RLVDS2_RB_N
N1 RLVDS2_RB_P
[LVDS DATA C]
P2 RLVDS2_RC_N
P1 RLVDS2_RC_P
[LVDS CLOCK]
R2 RLVDS2_RCLK_N
R1 RLVDS2_RCLK_P
[LVDS DATA D]
T2 RLVDS2_RD_N
T1 RLVDS2_RD_P
[LVDS DATA E]
U2 RLVDS2_RE_N
U1 RLVDS2_RE_P
[LVDS POWER SUPPLY]
N3 RX2AVD
N4 RX2AVS
P3 RX2DVD
P4 RX2DVS
M4 RX2VDDBGR
N5 RX2VDDD1
P5 RX2VDDD2
R5 RX2VSSD1
R4 RX2VSSD2
M3 VDDL33
T3 VDDL33
FRC_V1P2VCC_PLL
FRC_V3P3CLVCC
V13A00003200 FL508 FB MH1608HM221-T
V13A00003200 FL509 FB MH1608HM221-T
V13A00003200 FL510 FB MH1608HM221-T
V13A00003200 FL511 FB MH1608HM221-T
V03GD0000786 CM05B104K10AH C5181 100nF (K) 10V 1005
V03GD0000786 CM05B104K10AH C5184 100nF (K) 10V 1005
V03GD0000786 CM05B104K10AH C5182 100nF (K) 10V 1005
V03GD0000786 CM05B104K10AH C5185 100nF (K) 10V 1005
V03GD0000786 CM05B104K10AH C5183 100nF (K) 10V 1005
V03GD0000786 CM05B104K10AH C5186 100nF (K) 10V 1005
V03GD0000786 CM05B104K10AH C5188 100nF (K) 10V 1005
V03GD0000786 CM05B104K10AH C5189 100nF (K) 10V 1005
V03GD0000786 CM05B104K10AH C5190 100nF (K) 10V 1005
V03GD0000774 CM05W5R471K50AH C5191 470pF (K) 50V 1005
V01GD0001695 ERJ2GE0R00X W5039 1005 0 (X) 63mW
V01GD0001695 ERJ2GE0R00X W5040 1005 0 (X) 63mW
V01GD0001695 ERJ2GE0R00X W5041 1005 0 (X) 63mW
V03GD0001131 GRM21BB30J226ME38L C5192 22uF (M) 6.3V 2012
GND

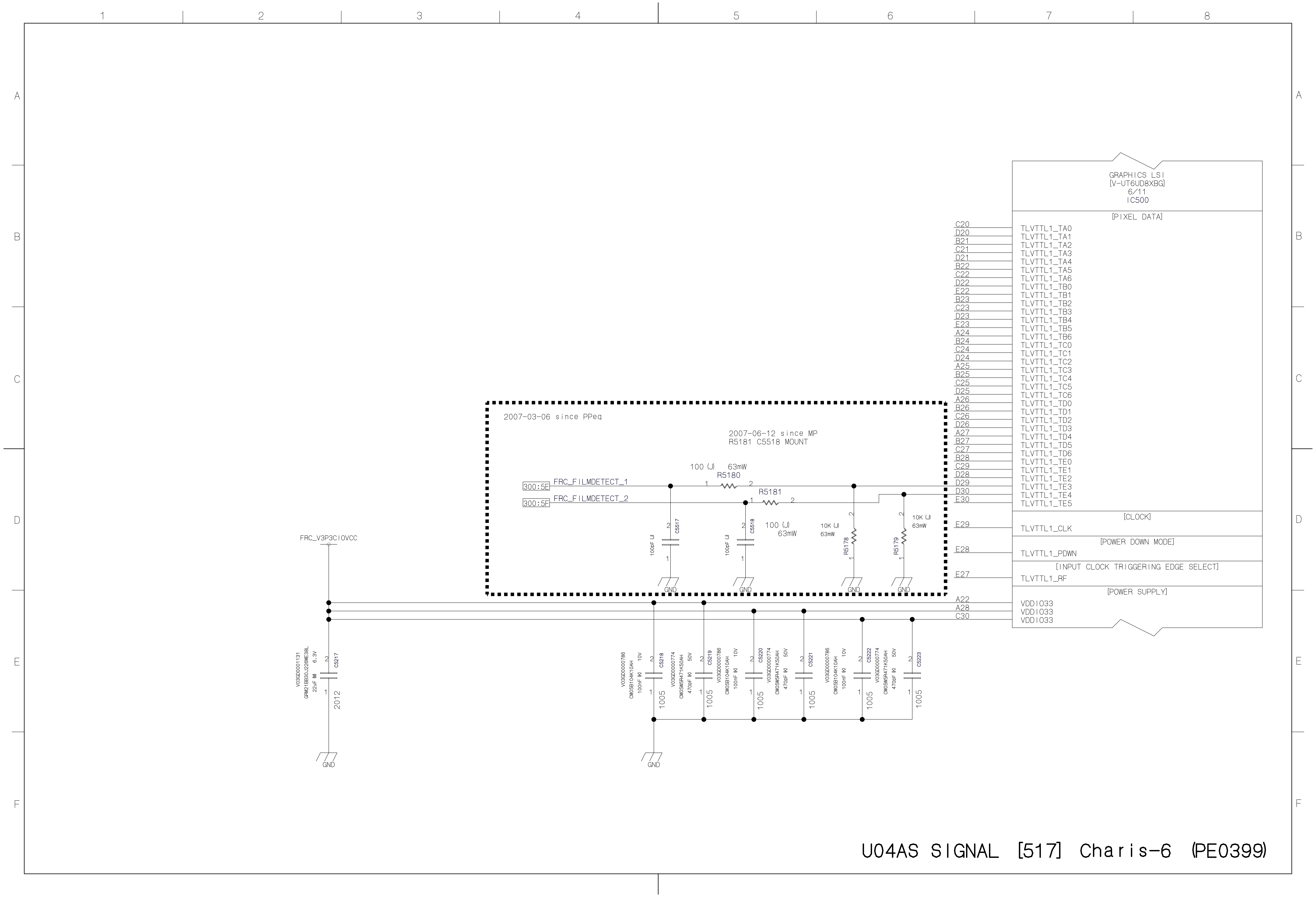
U04AS SIGNAL [514] Charis-3 (PE0399)


1
2
3
4
5
6
7
8
A
B
C
D
E
F
FRC_V3P3CLVCC
C5197
V03GD0000786
CM05B104K10AH
100nF (K)
10V
1005
C5198
V03GD0000774
CM05W5R471K50AH
470pF (K)
50V
1005
GND
FRC_V1P2VCC_PLL
FRC_V3P3CLVCC
V13A00003200
FL512
FB MH1608HM221-T
C5193
C5199
V01GD0001695
ERJ2GE0R00X
W5042
0 (X)
63mW
1005
V13A00003200
FL513
FB MH1608HM221-T
C5194
C5200
W5043
V13A00003200
FL514
FB MH1608HM221-T
C5195
C5201
V13A00003200
FL515
FB MH1608HM221-T
C5196
C5202
W5044
523:1D FRC_LVDS_TX1_TAN
523:1D FRC_LVDS_TX1_TAP
523:1D FRC_LVDS_TX1_TBN
523:1D FRC_LVDS_TX1_TBP
523:1D FRC_LVDS_TX1_TCN
523:1C FRC_LVDS_TX1_TCP
523:1C FRC_LVDS_TX1_TCKN
C5203
V03GD0000779
CM05W5R471K50AH
50V 1005
470pF (K)
NOT MOUNT
523:1C FRC_LVDS_TX1_TCKP
523:1C FRC_LVDS_TX1_TDN
523:1C FRC_LVDS_TX1_TDP
523:1C FRC_LVDS_TX1_TEN
523:1C FRC_LVDS_TX1_TEP
523:1C FRC_LVDS_TX2_TAN
523:1B FRC_LVDS_TX2_TAP
523:1B FRC_LVDS_TX2_TBN
523:1B FRC_LVDS_TX2_TBP
523:1B FRC_LVDS_TX2_TCN
523:1B FRC_LVDS_TX2_TCP
523:1B FRC_LVDS_TX2_TCKN
C5204
V03GD0000779
CM05W5R471K50AH
50V 1005
470pF (K)
NOT MOUNT
523:1B FRC_LVDS_TX2_TCKP
523:1B FRC_LVDS_TX2_TDN
523:1B FRC_LVDS_TX2_TDP
523:1B FRC_LVDS_TX2_TEN
523:1B FRC_LVDS_TX2_TEP
V01GD0001695 x24
ERJ2GE0R00X
1005
W5045
0 (X) 63mW
W5046
W5047
W5048
W5049
W5050
W5051
W5052
W5053
W5054
W5055
W5056
W5057
W5058
W5059
W5060
W5061
W5062
W5063
W5064
W5065
W5066
W5067
W5068
GRAPHICS LSI
[V-UT6UD8XBG]
4/11
IC500
[LVDS DATA A]
A3 TLVDS1_TA_N
B3 TLVDS1_TA_P
[LVDS DATA B]
A4 TLVDS1_TB_N
B4 TLVDS1_TB_P
[LVDS DATA C]
A5 TLVDS1_TC_N
B5 TLVDS1_TC_P
[LVDS CLOCK]
A6 TLVDS1_TCLK_N
B6 TLVDS1_TCLK_P
[LVDS DATA D]
A7 TLVDS1_TD_N
B7 TLVDS1_TD_P
[LVDS DATA E]
A8 TLVDS1_TE_N
B8 TLVDS1_TE_P
[LVDS DATA A]
C5 TLVDS2_TA_N
D5 TLVDS2_TA_P
[LVDS DATA B]
C6 TLVDS2_TB_N
D6 TLVDS2_TB_P
[LVDS DATA C]
C7 TLVDS2_TC_N
D7 TLVDS2_TC_P
[LVDS CLOCK]
C8 TLVDS2_TCLK_N
D8 TLVDS2_TCLK_P
[LVDS DATA D]
C9 TLVDS2_TD_N
D9 TLVDS2_TD_P
[LVDS DATA E]
C10 TLVDS2_TE_N
D10 TLVDS2_TE_P
[LVDS POWER SUPPLY]
A9 VDDL33
C1 VDDL33
D1 TX1AVD
D2 TX1AVS
E3 TX1DVD
E4 TX1DVS
C2 TX1VDDBGR
E10 TX1VDDD1
D11 TX1VDDD2
D12 TX1VSSD1
C12 TX1VSSD2
E11 TX1VSSD3
C11 TX1VSSD4
GND

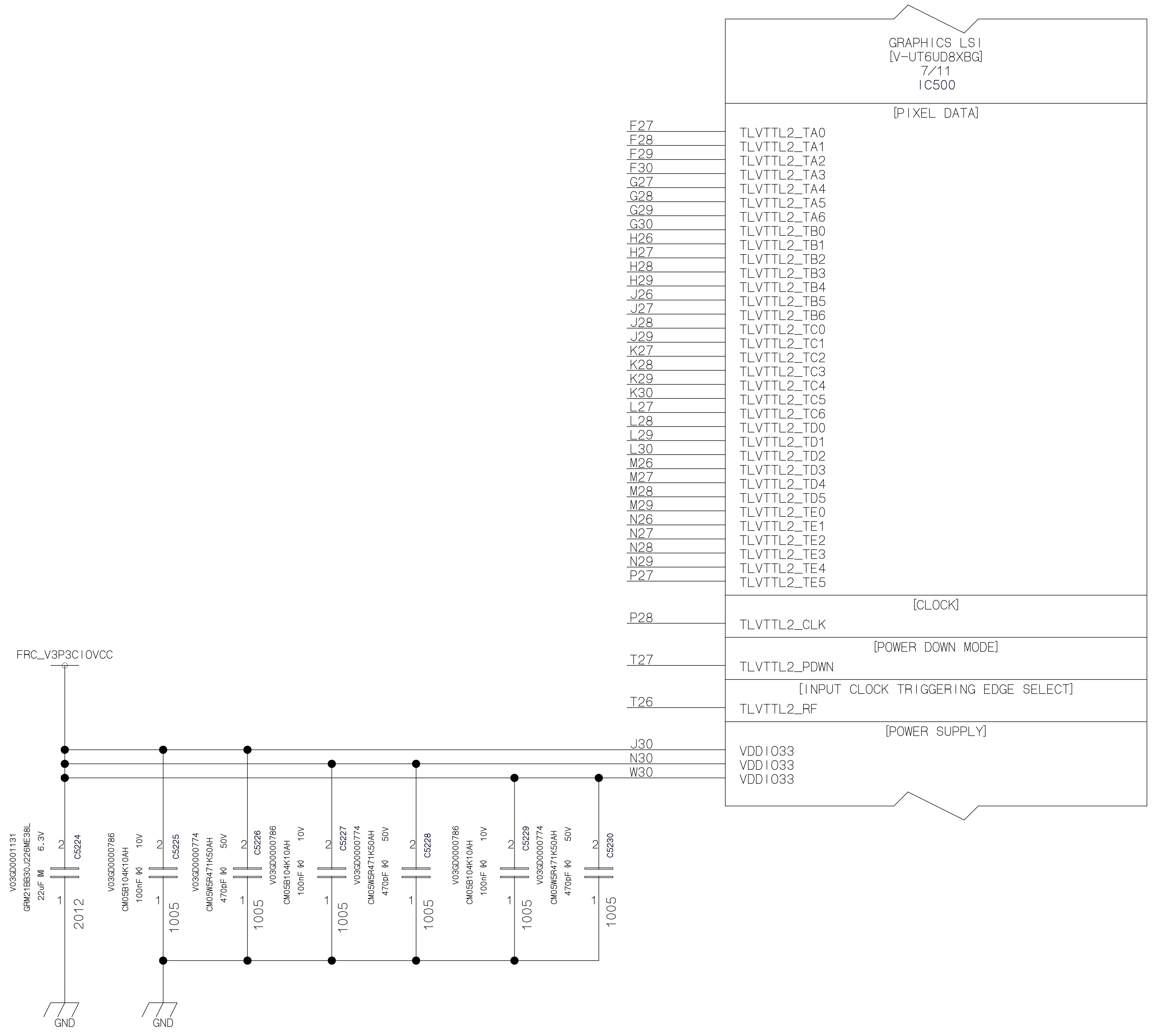
U04AS SIGNAL [515] Charis-4 (PE0399)


FRC_V3P3CLVCC
C5209 100nF 10V 1005 V03GD0000786 CM05B104K10AH
C5210 470nF 50V 1005 V03GD0000774
GND
FRC_V1P2VCC_PLL
FRC_V3P3CLVCC
V13A00003200
FL516 FB MH1608HM221-T
FL517 FB MH1608HM221-T
FL518 FB MH1608HM221-T
FL519 FB MH1608HM221-T
C5205
C5206
C5207
C5208
C5211
C5212
C5213
C5214
V01GD0001695 ERJ2GE0R00X
W5069
W5070
W5071
0 Ω 63mW 1005
523:6C FRC_LVDS_TX3_TAN
523:6C FRC_LVDS_TX3_TAP
523:6C FRC_LVDS_TX3_TBN
523:6C FRC_LVDS_TX3_TBP
523:6C FRC_LVDS_TX3_TCN
523:6C FRC_LVDS_TX3_TCP
523:6C FRC_LVDS_TX3_TCKN
523:6C FRC_LVDS_TX3_TCKP
523:6B FRC_LVDS_TX3_TDN
523:6B FRC_LVDS_TX3_TDP
523:6B FRC_LVDS_TX3_TEN
523:6B FRC_LVDS_TX3_TEP
523:6B FRC_LVDS_TX4_TAN
523:6B FRC_LVDS_TX4_TAP
523:6B FRC_LVDS_TX4_TBN
523:6B FRC_LVDS_TX4_TBP
523:6B FRC_LVDS_TX4_TCN
523:6B FRC_LVDS_TX4_TCP
523:6B FRC_LVDS_TX4_TCKN
523:6B FRC_LVDS_TX4_TCKP
523:6B FRC_LVDS_TX4_TDN
523:6A FRC_LVDS_TX4_TDP
523:6A FRC_LVDS_TX4_TEN
523:6A FRC_LVDS_TX4_TEP
NOT MOUNT
C5215 470pF V03GD0000779 CM05W5R471K50AH 50V 1005
C5216 470pF NOT MOUNT V03GD0000779 CM05W5R471K50AH 50V 1005
V01GD0001695 ERJ2GE0R00X X24
W5072 W5073 W5074 W5075 W5076 W5077 W5078 W5079 W5080 W5081 W5082 W5083 W5084 W5085 W5086 W5087 W5088 W5089 W5090 W5091 W5092 W5093 W5094 W5095
GRAPHICS LSI
[V-UT6UD8XBG]
5/11
IC500
[LVDS DATA A]
C13 TLVDS3_TA_N
D13 TLVDS3_TA_P
[LVDS DATA B]
C14 TLVDS3_TB_N
D14 TLVDS3_TB_P
[LVDS DATA C]
C15 TLVDS3_TC_N
D15 TLVDS3_TC_P
[LVDS CLOCK]
C16 TLVDS3_TCLK_N
D16 TLVDS3_TCLK_P
[LVDS DATA D]
C17 TLVDS3_TD_N
D17 TLVDS3_TD_P
[LVDS DATA E]
C18 TLVDS3_TE_N
D18 TLVDS3_TE_P
[LVDS DATA A]
A14 TLVDS4_TA_N
B14 TLVDS4_TA_P
[LVDS DATA B]
A15 TLVDS4_TB_N
B15 TLVDS4_TB_P
[LVDS DATA C]
A16 TLVDS4_TC_N
B16 TLVDS4_TC_P
[LVDS CLOCK]
A17 TLVDS4_TCLK_N
B17 TLVDS4_TCLK_P
[LVDS DATA D]
A18 TLVDS4_TD_N
B18 TLVDS4_TD_P
[LVDS DATA E]
A19 TLVDS4_TE_N
B19 TLVDS4_TE_P
[LVDS POWER SUPPLY]
A12 VDDL33
A21 VDDL33
A11 TX2AVD
B11 TX2AVS
A10 TX2DVD
B10 TX2DVS
B12 TX2VDDBGR
E19 TX2VDDD1
E18 TX2VDDD2
E17 TX2VSSD1
E15 TX2VSSD2
E16 TX2VSSD3
E14 TX2VSSD4
1 2 3 4 5 6 7 8
A B C D E F

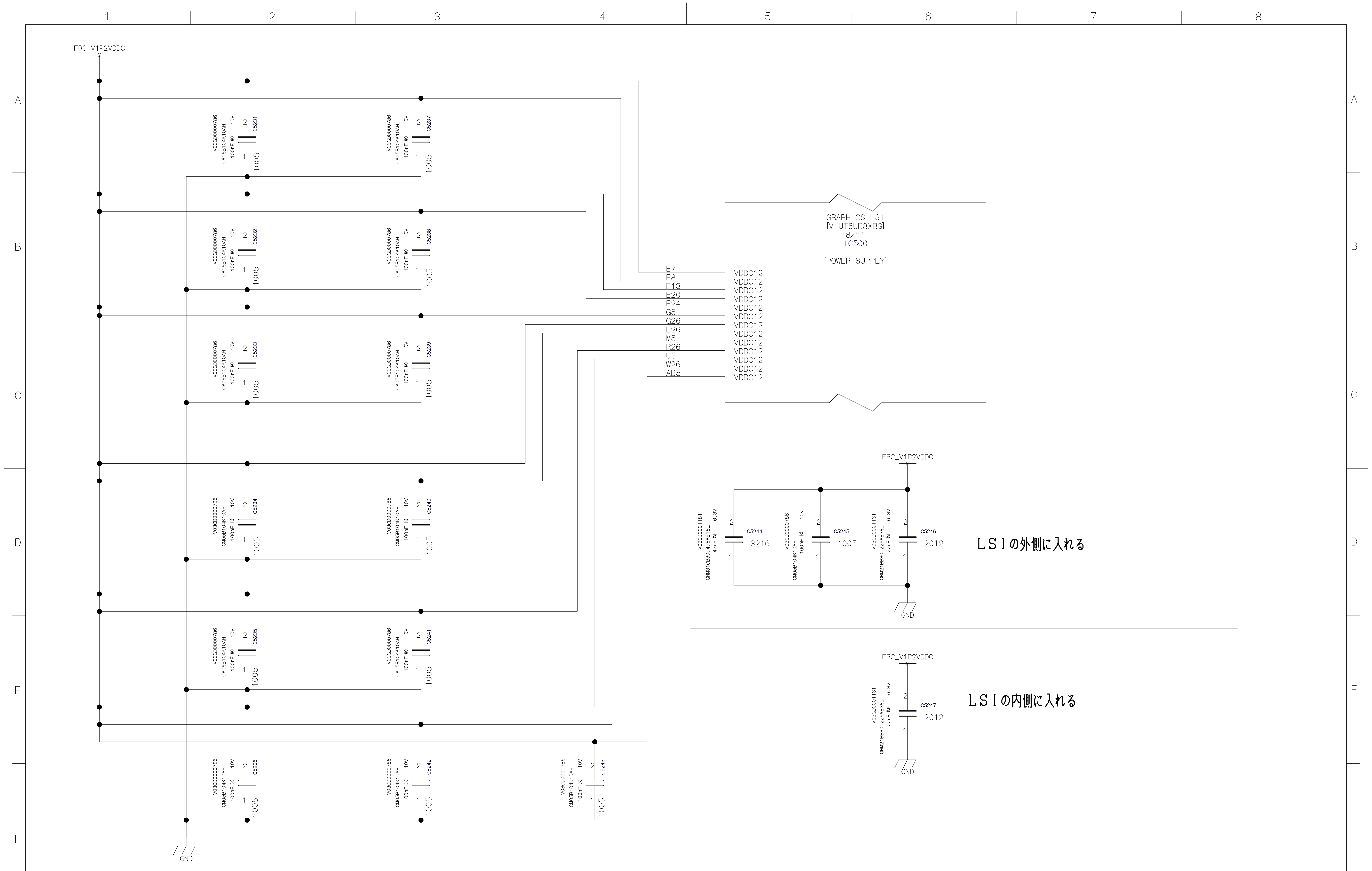
U04AS SIGNAL [516] Charis-5 (PE0399)


1
2
3
4
5
6
7
8
A
B
C
D
E
F
GRAPHICS LSI
[V-UT6UD8XBG]
6/11
IC500
[PIXEL DATA]
C20 TLVTTL1_TA0
D20 TLVTTL1_TA1
B21 TLVTTL1_TA2
C21 TLVTTL1_TA3
D21 TLVTTL1_TA4
B22 TLVTTL1_TA5
C22 TLVTTL1_TA6
D22 TLVTTL1_TB0
E22 TLVTTL1_TB1
B23 TLVTTL1_TB2
C23 TLVTTL1_TB3
D23 TLVTTL1_TB4
E23 TLVTTL1_TB5
A24 TLVTTL1_TB6
B24 TLVTTL1_TC0
C24 TLVTTL1_TC1
D24 TLVTTL1_TC2
A25 TLVTTL1_TC3
B25 TLVTTL1_TC4
C25 TLVTTL1_TC5
D25 TLVTTL1_TC6
A26 TLVTTL1_TD0
B26 TLVTTL1_TD1
C26 TLVTTL1_TD2
D26 TLVTTL1_TD3
A27 TLVTTL1_TD4
B27 TLVTTL1_TD5
C27 TLVTTL1_TD6
B28 TLVTTL1_TE0
C29 TLVTTL1_TE1
D28 TLVTTL1_TE2
D29 TLVTTL1_TE3
D30 TLVTTL1_TE4
E30 TLVTTL1_TE5
[CLOCK]
E29 TLVTTL1_CLK
[POWER DOWN MODE]
E28 TLVTTL1_PDWN
[INPUT CLOCK TRIGGERING EDGE SELECT]
E27 TLVTTL1_RF
[POWER SUPPLY]
A22 VDDIO33
A28 VDDIO33
C30 VDDIO33
2007-03-06 since PPeq
2007-06-12 since MP
R5181 C5518 MOUNT
300:5E
FRC_FILMDETECT_1
300:5F
FRC_FILMDETECT_2
100 (J) 63mW
R5180
R5181
100 (J) 63mW
C5517 100pF (J)
C5518 100pF (J)
R5178 10K (J) 63mW
R5179 10K (J) 63mW
GND
FRC_V3P3CIOVCC
C5217
V03GD0001131
GRM21BB30J226ME38L
22uF (M) 6.3V
2012
C5218
V03GD0000786
CM05B104K10AH
100nF (K) 10V
1005
C5219
V03GD0000774
CM05W5R471K50AH
470pF (K) 50V
1005
C5220
V03GD0000786
CM05B104K10AH
100nF (K) 10V
1005
C5221
V03GD0000774
CM05W5R471K50AH
470pF (K) 50V
1005
C5222
V03GD0000786
CM05B104K10AH
100nF (K) 10V
1005
C5223
V03GD0000774
CM05W5R471K50AH
470pF (K) 50V
1005

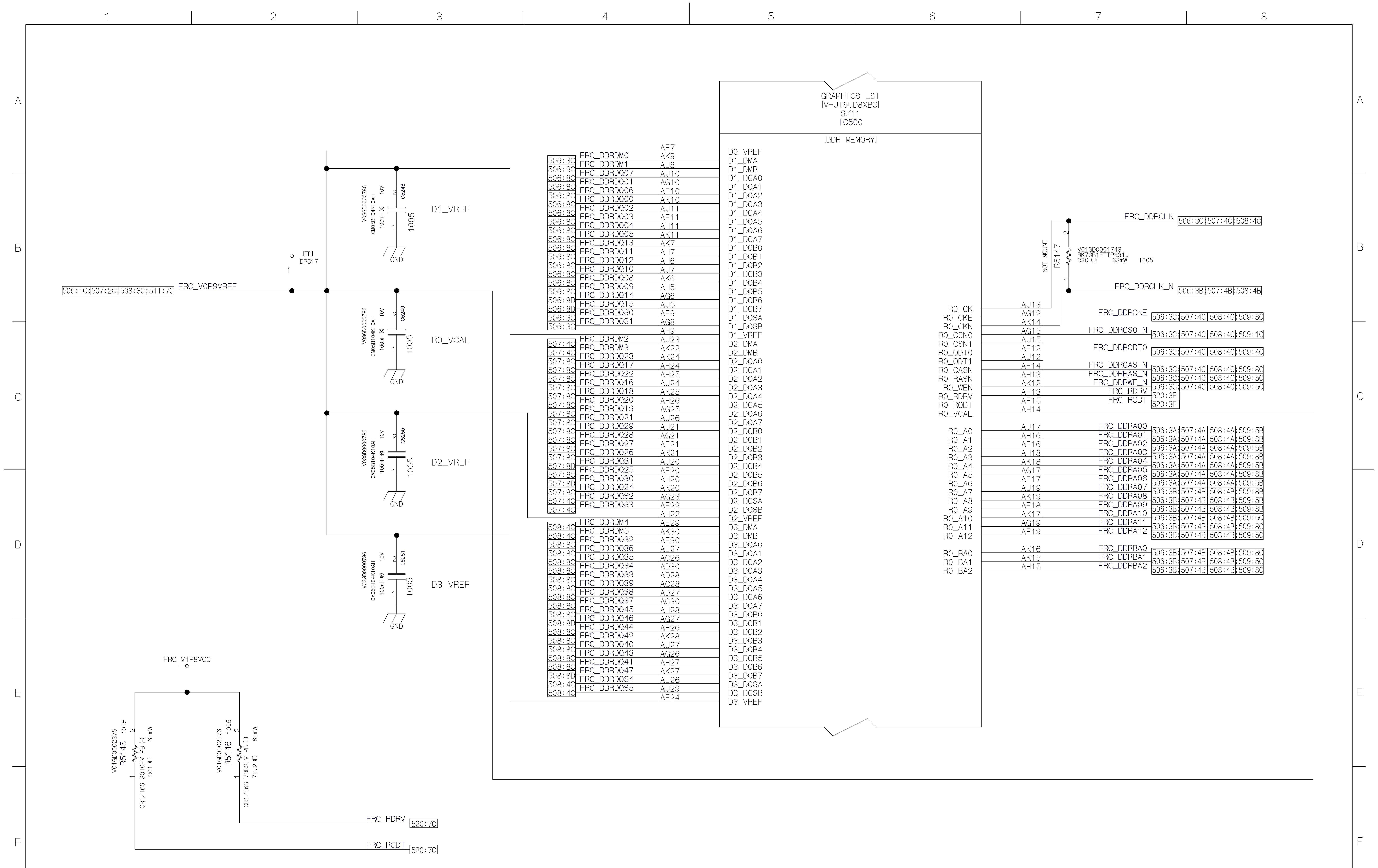
U04AS SIGNAL [517] Charis-6 (PE0399)


GRAPHICS LSI
[V-UT6UD8XBG]
7/11
IC500
[PIXEL DATA]
F27 TLVTTL2_TA0
F28 TLVTTL2_TA1
F29 TLVTTL2_TA2
F30 TLVTTL2_TA3
G27 TLVTTL2_TA4
G28 TLVTTL2_TA5
G29 TLVTTL2_TA6
G30 TLVTTL2_TB0
H26 TLVTTL2_TB1
H27 TLVTTL2_TB2
H28 TLVTTL2_TB3
H29 TLVTTL2_TB4
J26 TLVTTL2_TB5
J27 TLVTTL2_TB6
J28 TLVTTL2_TC0
J29 TLVTTL2_TC1
K27 TLVTTL2_TC2
K28 TLVTTL2_TC3
K29 TLVTTL2_TC4
K30 TLVTTL2_TC5
L27 TLVTTL2_TC6
L28 TLVTTL2_TD0
L29 TLVTTL2_TD1
L30 TLVTTL2_TD2
M26 TLVTTL2_TD3
M27 TLVTTL2_TD4
M28 TLVTTL2_TD5
M29 TLVTTL2_TE0
N26 TLVTTL2_TE1
N27 TLVTTL2_TE2
N28 TLVTTL2_TE3
N29 TLVTTL2_TE4
P27 TLVTTL2_TE5
[CLOCK]
P28 TLVTTL2_CLK
[POWER DOWN MODE]
T27 TLVTTL2_PDWN
[INPUT CLOCK TRIGGERING EDGE SELECT]
T26 TLVTTL2_RF
[POWER SUPPLY]
J30 VDDIO33
N30 VDDIO33
W30 VDDIO33
FRC_V3P3CIOVCC
V03GD0001131
GRM21BB30J226ME38L
22uF (M) 6.3V
C5224
2012
V03GD0000786
CM05B104K10AH
100nF (K) 10V
C5225
1005
V03GD0000774
CM05W5R471K50AH
470pF (K) 50V
C5226
1005
V03GD0000786
CM05B104K10AH
100nF (K) 10V
C5227
1005
V03GD0000774
CM05W5R471K50AH
470pF (K) 50V
C5228
1005
V03GD0000786
CM05B104K10AH
100nF (K) 10V
C5229
1005
V03GD0000774
CM05W5R471K50AH
470pF (K) 50V
C5230
1005
1
2
GND
GND

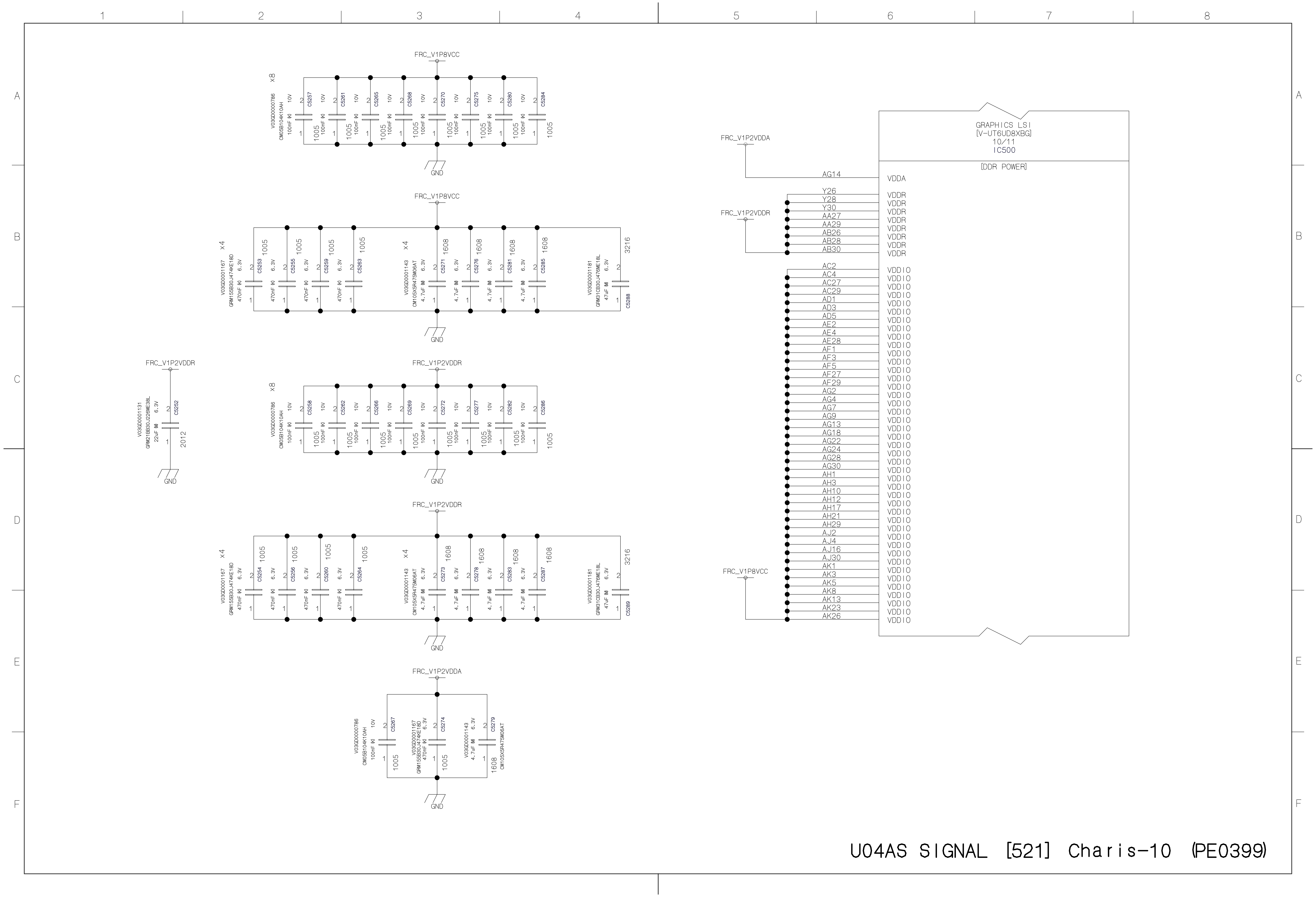
U04AS SIGNAL [518] Charis-7 (PE0399)


1
2
3
4
5
6
7
8
A
B
C
D
E
F
FRC_V1P2VDDC
V03GD0000786
CM05B104K10AH
100nF (K)
10V
C5231
C5232
C5233
C5234
C5235
C5236
C5237
C5238
C5239
C5240
C5241
C5242
C5243
1005
GND
GRAPHICS LSI
[V-UT6UD8XBG]
8/11
IC500
[POWER SUPPLY]
E7
E8
E13
E20
E24
G5
G26
L26
M5
R26
U5
W26
AB5
VDDC12
V03GD0001181
GRM31CB30J476ME18L
47uF (M)
6.3V
C5244
3216
C5245
V03GD0001131
GRM21BB30J226ME38L
22uF (M)
C5246
2012
LSIの外側に入れる
C5247
LSIの内側に入れる

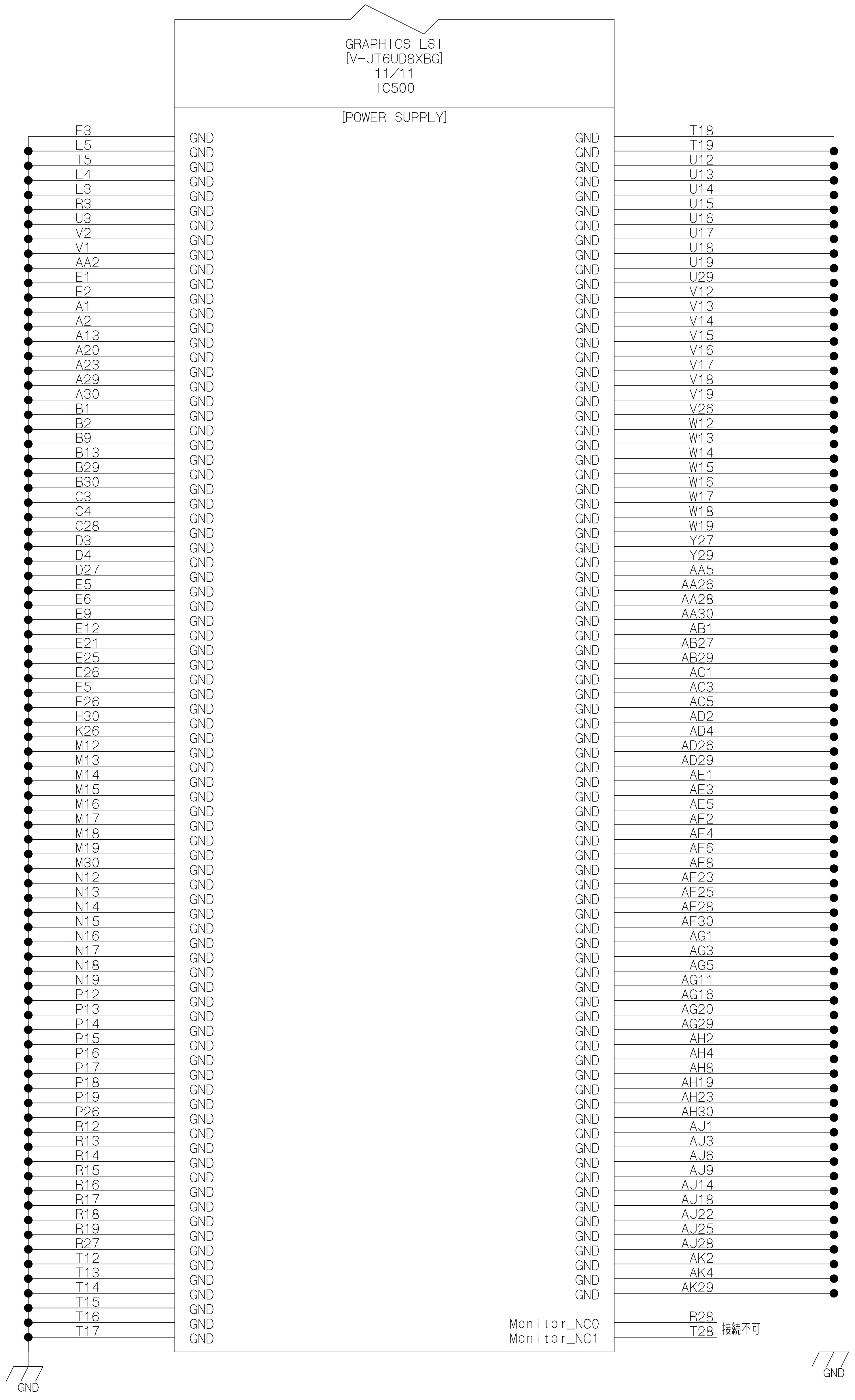
U04AS SIGNAL [519] Charis-8 (PE0399)


GRAPHICS LSI
[V-UT6UD8XBG]
9/11
IC500
[DDR MEMORY]
D0_VREF
D1_VREF
D2_VREF
D3_VREF
R0_VCAL
FRC_V0P9VREF
506:1C 507:2C 508:3C 511:7C
[TP]
DP517
C5248
C5249
C5250
C5251
100nF
10V
1005
GND
FRC_V1P8VCC
R5145
R5146
R5147
NOT MOUNT
FRC_RDRV
FRC_RODT
520:7C
FRC_DDRCLK
FRC_DDRCLK_N
FRC_DDRCKE
FRC_DDRCS0_N
FRC_DDRODT0
FRC_DDRCAS_N
FRC_DDRRAS_N
FRC_DDRWE_N
FRC_DDRA00
FRC_DDRA12
FRC_DDRBA0
FRC_DDRBA1
FRC_DDRBA2
R0_CK
R0_CKE
R0_CKN
R0_CSN0
R0_CSN1
R0_ODT0
R0_ODT1
R0_CASN
R0_RASN
R0_WEN
R0_RDRV
R0_RODT
R0_VCAL
R0_A0
R0_A12
R0_BA0
R0_BA1
R0_BA2
1
2
3
4
5
6
7
8
A
B
C
D
E
F

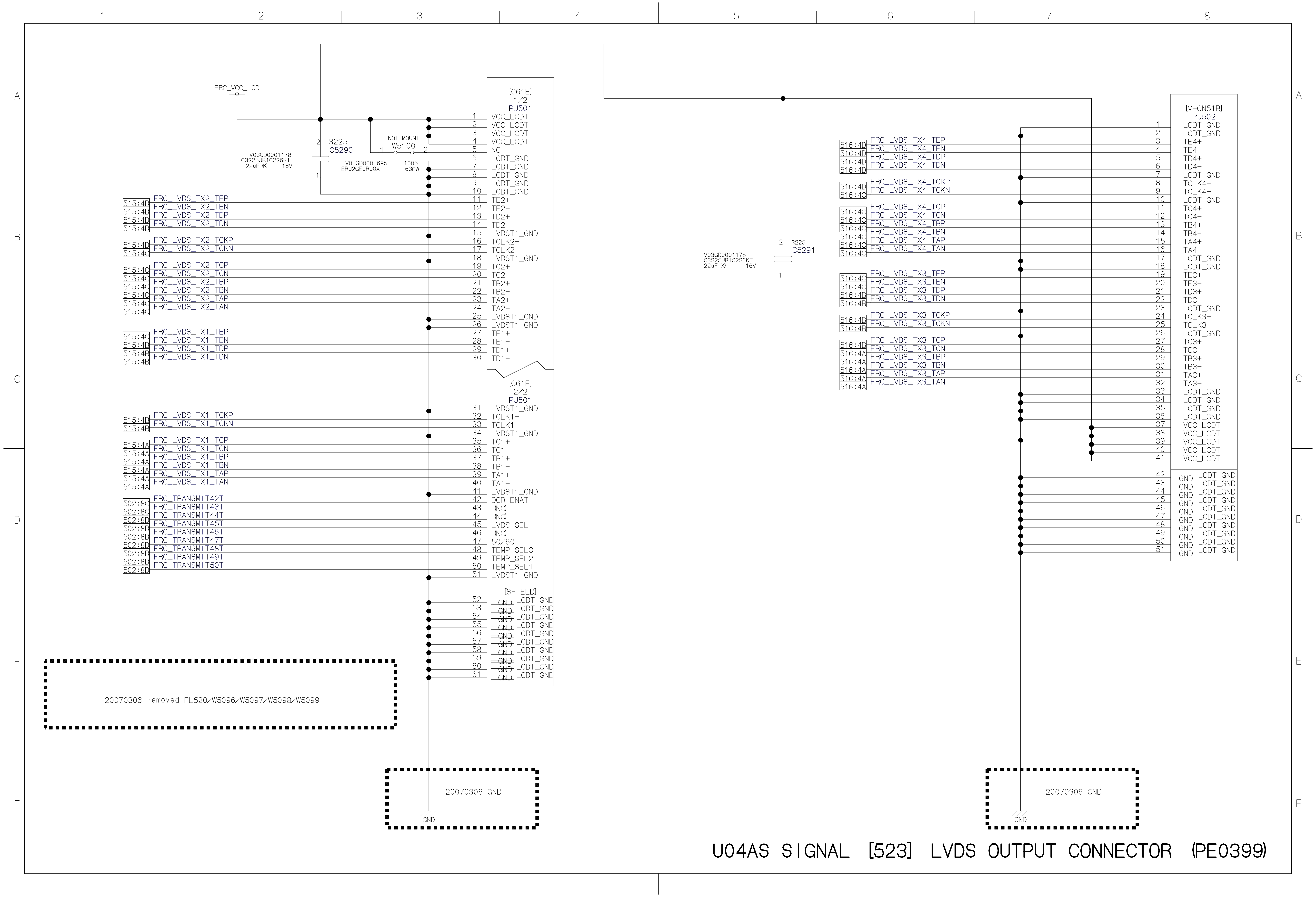
U04AS SIGNAL [520] Charis-9 (PE0399)


1
2
3
4
5
6
7
8
A
B
C
D
E
F
FRC_V1P8VCC
x8
V03GD0000786
CM05B104K10AH
100nF (K)
10V
C5257
C5261
C5265
C5268
C5270
C5275
C5280
C5284
1005
GND
FRC_V1P8VCC
x4
V03GD0001167
GRM155B30J474KE18D
470nF (K)
6.3V
C5253
C5255
C5259
C5263
1005
x4
V03GD0001143
CM105X5R475M06AT
4.7uF (M)
6.3V
C5271
C5276
C5281
C5285
1608
V03GD0001181
GRM31CB30J476ME18L
47uF (M)
6.3V
C5288
3216
GND
FRC_V1P2VDDR
V03GD0001131
GRM21BB30J226ME38L
22uF (M)
6.3V
C5252
2012
GND
FRC_V1P2VDDR
x8
V03GD0000786
CM05B104K10AH
100nF (K)
10V
C5258
C5262
C5266
C5269
C5272
C5277
C5282
C5286
1005
GND
FRC_V1P2VDDR
x4
V03GD0001167
GRM155B30J474KE18D
470nF (K)
6.3V
C5254
C5256
C5260
C5264
1005
x4
V03GD0001143
CM105X5R475M06AT
4.7uF (M)
6.3V
C5273
C5278
C5283
C5287
1608
V03GD0001181
GRM31CB30J476ME18L
47uF (M)
6.3V
C5289
3216
GND
FRC_V1P2VDDA
V03GD0000786
CM05B104K10AH
100nF (K)
10V
C5267
1005
V03GD0001167
GRM155B30J474KE18D
470nF (K)
6.3V
C5274
1005
V03GD0001143
4.7uF (M)
6.3V
C5279
1608
CM105X5R475M06AT
GND
GRAPHICS LSI
[V-UT6UD8XBG]
10/11
IC500
[DDR POWER]
FRC_V1P2VDDA
AG14 VDDA
FRC_V1P2VDDR
Y26 VDDR
Y28 VDDR
Y30 VDDR
AA27 VDDR
AA29 VDDR
AB26 VDDR
AB28 VDDR
AB30 VDDR
FRC_V1P8VCC
AC2 VDDIO
AC4 VDDIO
AC27 VDDIO
AC29 VDDIO
AD1 VDDIO
AD3 VDDIO
AD5 VDDIO
AE2 VDDIO
AE4 VDDIO
AE28 VDDIO
AF1 VDDIO
AF3 VDDIO
AF5 VDDIO
AF27 VDDIO
AF29 VDDIO
AG2 VDDIO
AG4 VDDIO
AG7 VDDIO
AG9 VDDIO
AG13 VDDIO
AG18 VDDIO
AG22 VDDIO
AG24 VDDIO
AG28 VDDIO
AG30 VDDIO
AH1 VDDIO
AH3 VDDIO
AH10 VDDIO
AH12 VDDIO
AH17 VDDIO
AH21 VDDIO
AH29 VDDIO
AJ2 VDDIO
AJ4 VDDIO
AJ16 VDDIO
AJ30 VDDIO
AK1 VDDIO
AK3 VDDIO
AK5 VDDIO
AK8 VDDIO
AK13 VDDIO
AK23 VDDIO
AK26 VDDIO

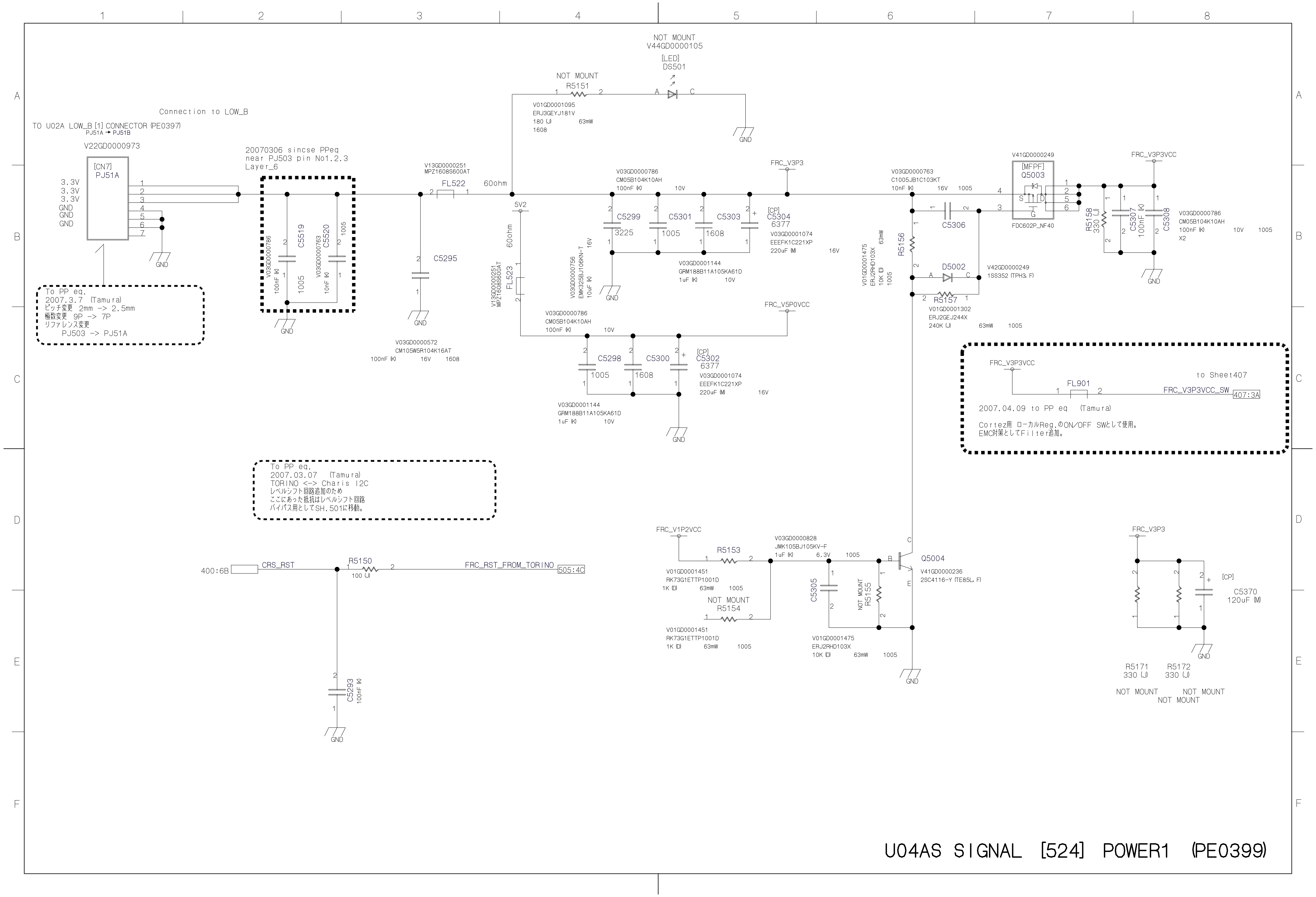
U04AS SIGNAL [521] Charis-10 (PE0399)


GRAPHICS LSI
[V-UT6UD8XBG]
11/11
IC500
[POWER SUPPLY]
F3 GND
L5 GND
T5 GND
L4 GND
L3 GND
R3 GND
U3 GND
V2 GND
V1 GND
AA2 GND
E1 GND
E2 GND
A1 GND
A2 GND
A13 GND
A20 GND
A23 GND
A29 GND
A30 GND
B1 GND
B2 GND
B9 GND
B13 GND
B29 GND
B30 GND
C3 GND
C4 GND
C28 GND
D3 GND
D4 GND
D27 GND
E5 GND
E6 GND
E9 GND
E12 GND
E21 GND
E25 GND
E26 GND
F5 GND
F26 GND
H30 GND
K26 GND
M12 GND
M13 GND
M14 GND
M15 GND
M16 GND
M17 GND
M18 GND
M19 GND
M30 GND
N12 GND
N13 GND
N14 GND
N15 GND
N16 GND
N17 GND
N18 GND
N19 GND
P12 GND
P13 GND
P14 GND
P15 GND
P16 GND
P17 GND
P18 GND
P19 GND
P26 GND
R12 GND
R13 GND
R14 GND
R15 GND
R16 GND
R17 GND
R18 GND
R19 GND
R27 GND
T12 GND
T13 GND
T14 GND
T15 GND
T16 GND
T17 GND
GND
GND T18
GND T19
GND U12
GND U13
GND U14
GND U15
GND U16
GND U17
GND U18
GND U19
GND U29
GND V12
GND V13
GND V14
GND V15
GND V16
GND V17
GND V18
GND V19
GND V26
GND W12
GND W13
GND W14
GND W15
GND W16
GND W17
GND W18
GND W19
GND Y27
GND Y29
GND AA5
GND AA26
GND AA28
GND AA30
GND AB1
GND AB27
GND AB29
GND AC1
GND AC3
GND AC5
GND AD2
GND AD4
GND AD26
GND AD29
GND AE1
GND AE3
GND AE5
GND AF2
GND AF4
GND AF6
GND AF8
GND AF23
GND AF25
GND AF28
GND AF30
GND AG1
GND AG3
GND AG5
GND AG11
GND AG16
GND AG20
GND AG29
GND AH2
GND AH4
GND AH8
GND AH19
GND AH23
GND AH30
GND AJ1
GND AJ3
GND AJ6
GND AJ9
GND AJ14
GND AJ18
GND AJ22
GND AJ25
GND AJ28
GND AK2
GND AK4
GND AK29
Monitor_NC0 R28
Monitor_NC1 T28
接続不可
GND

U04AS SIGNAL [522] Charis-11 (PE0399)


1
2
3
4
5
6
7
8
A
B
C
D
E
F
FRC_VCC_LCD
2
3225
C5290
1
V03GD0001178
C3225JB1C226KT
22uF (K) 16V
NOT MOUNT
W5100
1
2
V01GD0001695
ERJ2GE0R00X
1005
63mW
[C61E]
1/2
PJ501
1 VCC_LCDT
2 VCC_LCDT
3 VCC_LCDT
4 VCC_LCDT
5 NC
6 LCDT_GND
7 LCDT_GND
8 LCDT_GND
9 LCDT_GND
10 LCDT_GND
11 TE2+
12 TE2-
13 TD2+
14 TD2-
15 LVDST1_GND
16 TCLK2+
17 TCLK2-
18 LVDST1_GND
19 TC2+
20 TC2-
21 TB2+
22 TB2-
23 TA2+
24 TA2-
25 LVDST1_GND
26 LVDST1_GND
27 TE1+
28 TE1-
29 TD1+
30 TD1-
[C61E]
2/2
PJ501
31 LVDST1_GND
32 TCLK1+
33 TCLK1-
34 LVDST1_GND
35 TC1+
36 TC1-
37 TB1+
38 TB1-
39 TA1+
40 TA1-
41 LVDST1_GND
42 DCR_ENAT
43 (NC)
44 (NC)
45 LVDS_SEL
46 (NC)
47 50/60
48 TEMP_SEL3
49 TEMP_SEL2
50 TEMP_SEL1
51 LVDST1_GND
[SHIELD]
52 GND LCDT_GND
53 GND LCDT_GND
54 GND LCDT_GND
55 GND LCDT_GND
56 GND LCDT_GND
57 GND LCDT_GND
58 GND LCDT_GND
59 GND LCDT_GND
60 GND LCDT_GND
61 GND LCDT_GND
515:4D FRC_LVDS_TX2_TEP
515:4D FRC_LVDS_TX2_TEN
515:4D FRC_LVDS_TX2_TDP
515:4D FRC_LVDS_TX2_TDN
515:4D FRC_LVDS_TX2_TCKP
515:4C FRC_LVDS_TX2_TCKN
515:4C FRC_LVDS_TX2_TCP
515:4C FRC_LVDS_TX2_TCN
515:4C FRC_LVDS_TX2_TBP
515:4C FRC_LVDS_TX2_TBN
515:4C FRC_LVDS_TX2_TAP
515:4C FRC_LVDS_TX2_TAN
515:4C FRC_LVDS_TX1_TEP
515:4B FRC_LVDS_TX1_TEN
515:4B FRC_LVDS_TX1_TDP
515:4B FRC_LVDS_TX1_TDN
515:4B FRC_LVDS_TX1_TCKP
515:4B FRC_LVDS_TX1_TCKN
515:4A FRC_LVDS_TX1_TCP
515:4A FRC_LVDS_TX1_TCN
515:4A FRC_LVDS_TX1_TBP
515:4A FRC_LVDS_TX1_TBN
515:4A FRC_LVDS_TX1_TAP
515:4A FRC_LVDS_TX1_TAN
502:8D FRC_TRANSMIT42T
502:8D FRC_TRANSMIT43T
502:8D FRC_TRANSMIT44T
502:8D FRC_TRANSMIT45T
502:8D FRC_TRANSMIT46T
502:8D FRC_TRANSMIT47T
502:8D FRC_TRANSMIT48T
502:8D FRC_TRANSMIT49T
502:8D FRC_TRANSMIT50T
20070306 removed FL520/W5096/W5097/W5098/W5099
20070306 GND
GND
2
3225
C5291
1
V03GD0001178
C3225JB1C226KT
22uF (K) 16V
[V-CN51B]
PJ502
1 LCDT_GND
2 LCDT_GND
3 TE4+
4 TE4-
5 TD4+
6 TD4-
7 LCDT_GND
8 TCLK4+
9 TCLK4-
10 LCDT_GND
11 TC4+
12 TC4-
13 TB4+
14 TB4-
15 TA4+
16 TA4-
17 LCDT_GND
18 LCDT_GND
19 TE3+
20 TE3-
21 TD3+
22 TD3-
23 LCDT_GND
24 TCLK3+
25 TCLK3-
26 LCDT_GND
27 TC3+
28 TC3-
29 TB3+
30 TB3-
31 TA3+
32 TA3-
33 LCDT_GND
34 LCDT_GND
35 LCDT_GND
36 LCDT_GND
37 VCC_LCDT
38 VCC_LCDT
39 VCC_LCDT
40 VCC_LCDT
41 VCC_LCDT
42 GND LCDT_GND
43 GND LCDT_GND
44 GND LCDT_GND
45 GND LCDT_GND
46 GND LCDT_GND
47 GND LCDT_GND
48 GND LCDT_GND
49 GND LCDT_GND
50 GND LCDT_GND
51 GND LCDT_GND
516:4D FRC_LVDS_TX4_TEP
516:4D FRC_LVDS_TX4_TEN
516:4D FRC_LVDS_TX4_TDP
516:4D FRC_LVDS_TX4_TDN
516:4D FRC_LVDS_TX4_TCKP
516:4C FRC_LVDS_TX4_TCKN
516:4C FRC_LVDS_TX4_TCP
516:4C FRC_LVDS_TX4_TCN
516:4C FRC_LVDS_TX4_TBP
516:4C FRC_LVDS_TX4_TBN
516:4C FRC_LVDS_TX4_TAP
516:4C FRC_LVDS_TX4_TAN
516:4C FRC_LVDS_TX3_TEP
516:4C FRC_LVDS_TX3_TEN
516:4B FRC_LVDS_TX3_TDP
516:4B FRC_LVDS_TX3_TDN
516:4B FRC_LVDS_TX3_TCKP
516:4B FRC_LVDS_TX3_TCKN
516:4B FRC_LVDS_TX3_TCP
516:4A FRC_LVDS_TX3_TCN
516:4A FRC_LVDS_TX3_TBP
516:4A FRC_LVDS_TX3_TBN
516:4A FRC_LVDS_TX3_TAP
516:4A FRC_LVDS_TX3_TAN
20070306 GND
GND

U04AS SIGNAL [523] LVDS OUTPUT CONNECTOR (PE0399)


1
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7
8
A
B
C
D
E
F
Connection to LOW_B
TO U02A LOW_B [1] CONNECTOR (PE0397)
PJ51A → PJ51B
V22GD0000973
[CN7]
PJ51A
3.3V
3.3V
3.3V
GND
GND
GND
GND
20070306 sincse PPeq
near PJ503 pin No1.2.3
Layer_6
C5519
V03GD0000786
100nF (K)
1005
C5520
V03GD0000763
10nF (K)
1005
GND
C5295
V03GD0000572
CM105W5R104K16AT
100nF (K) 16V 1608
V13GD0000251
MPZ1608S600AT
FL522
60ohm
5V2
FL523
V13GD0000251
MPZ1608S600AT
60ohm
NOT MOUNT
R5151
V01GD0001095
ERJ3GEYJ181V
180 (J) 63mW
1608
NOT MOUNT
V44GD0000105
[LED]
DS501
A
C
V03GD0000786
CM05B104K10AH
100nF (K) 10V
C5299
3225
V03GD0000756
EMK325BJ106KN-T
10uF (K)
16V
C5301
1005
C5303
1608
V03GD0001144
GRM188B11A105KA61D
1uF (K) 10V
[CP]
C5304
6377
V03GD0001074
EEEFK1C221XP
220uF (M) 16V
FRC_V3P3
FRC_V5P0VCC
V03GD0000786
CM05B104K10AH
100nF (K) 10V
C5298
1005
C5300
1608
[CP]
C5302
6377
V03GD0001074
EEEFK1C221XP
220uF (M) 16V
V03GD0001144
GRM188B11A105KA61D
1uF (K) 10V
V03GD0000763
C1005JB1C103KT
10nF (K) 16V 1005
C5306
R5156
V01GD0001475
ERJ2RHD103X
10K (D)
1005
63mW
D5002
V42GD0000249
1SS352 (TPH3, F)
R5157
V01GD0001302
ERJ2GEJ244X
240K (J) 63mW 1005
V41GD0000249
[MFPF]
Q5003
S
D
G
FDC602P_NF40
FRC_V3P3VCC
R5158
330 (J)
C5307
100nF (K)
C5308
V03GD0000786
CM05B104K10AH
100nF (K) 10V 1005
X2
FRC_V3P3VCC
FL901
to Sheet407
FRC_V3P3VCC_SW
407:3A
400:6B
CRS_RST
R5150
100 (J)
FRC_RST_FROM_TORINO
505:4C
C5293
100nF (K)
FRC_V1P2VCC
R5153
V01GD0001451
RK73G1ETTP1001D
1K (D) 63mW 1005
NOT MOUNT
R5154
V01GD0001451
RK73G1ETTP1001D
1K (D) 63mW 1005
V03GD0000828
JMK105BJ105KV-F
1uF (K) 6.3V 1005
C5305
NOT MOUNT
R5155
V01GD0001475
ERJ2RHD103X
10K (D) 63mW 1005
Q5004
B
C
E
V41GD0000236
2SC4116-Y (TE85L, F)
FRC_V3P3
[CP]
C5370
120uF (M)
R5171
330 (J)
R5172
330 (J)
NOT MOUNT
NOT MOUNT
NOT MOUNT

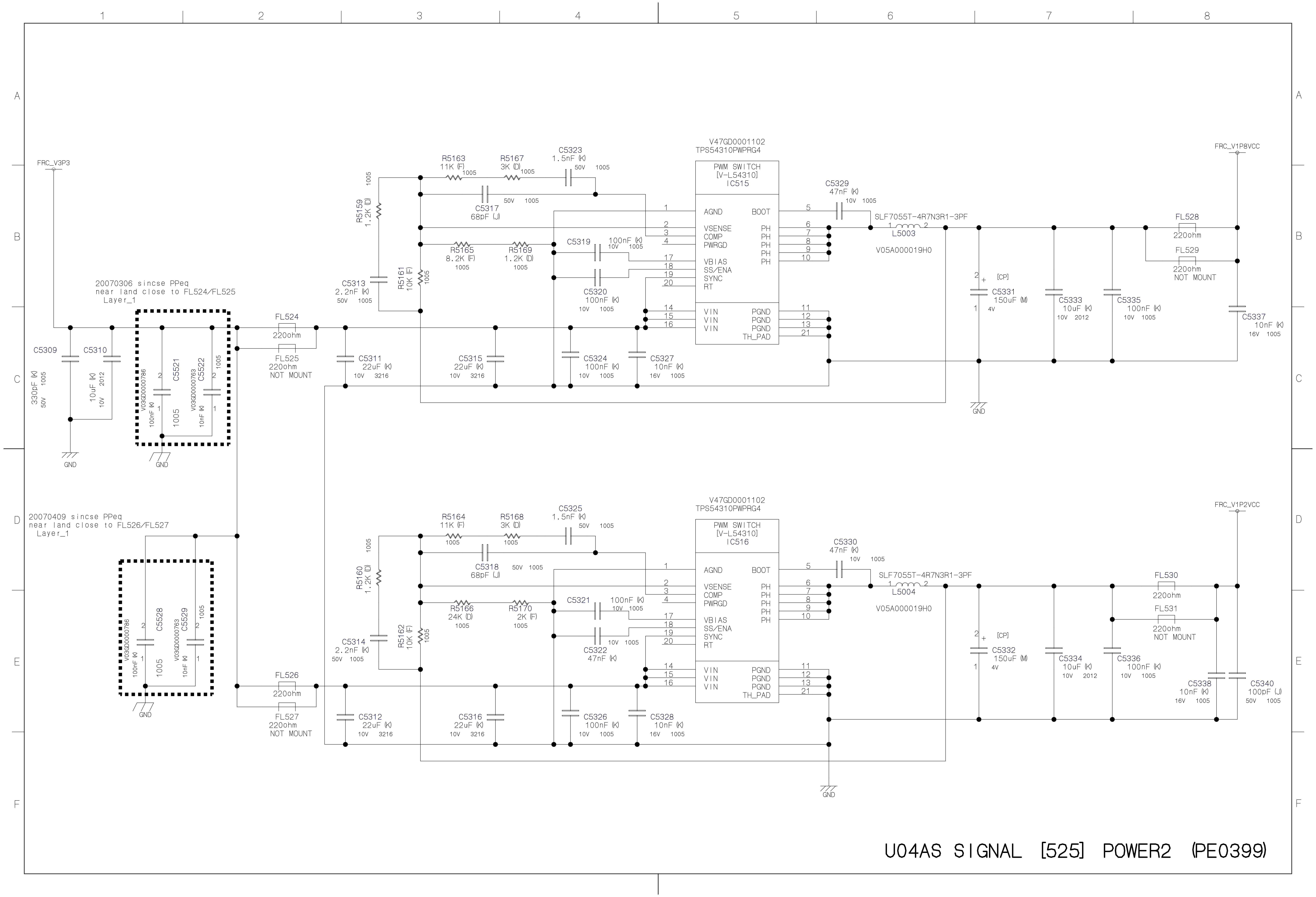
To PP eq.
2007.3.7 (Tamura)
ピッチ変更 2mm -> 2.5mm
極数変更 9P -> 7P
リファレンス変更
PJ503 -> PJ51A

2007.04.09 to PP eq (Tamura)

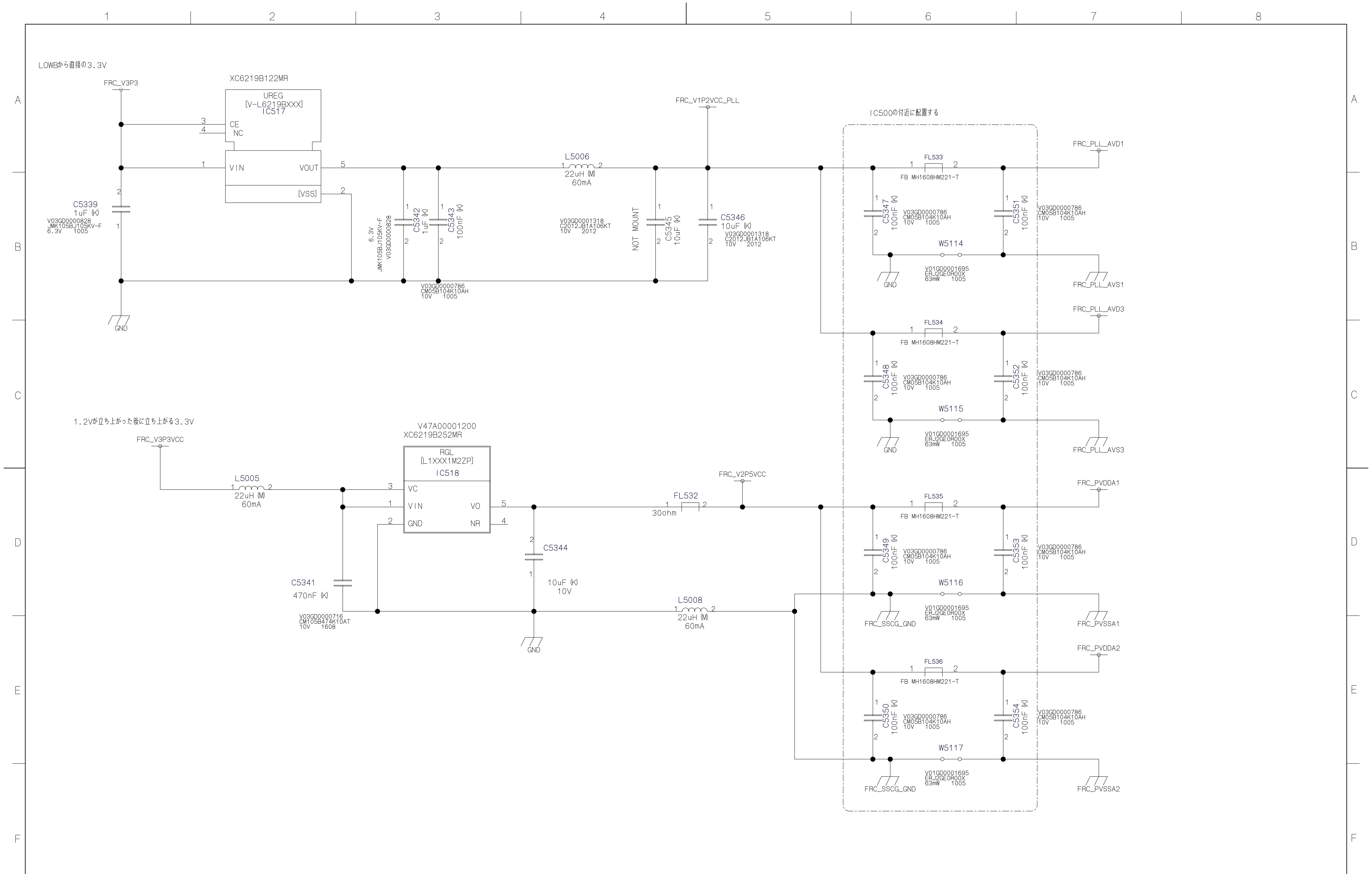
Cortez用 ローカルReg.のON/OFF SWとして使用。
EMC対策としてFilter追加。

To PP eq.
2007.03.07 (Tamura)
TORINO <-> Charis I2C
レベルシフト回路追加のため
ここにあった抵抗はレベルシフト回路
バイパス用としてSH.501に移動。

U04AS SIGNAL [524] POWER1 (PE0399)

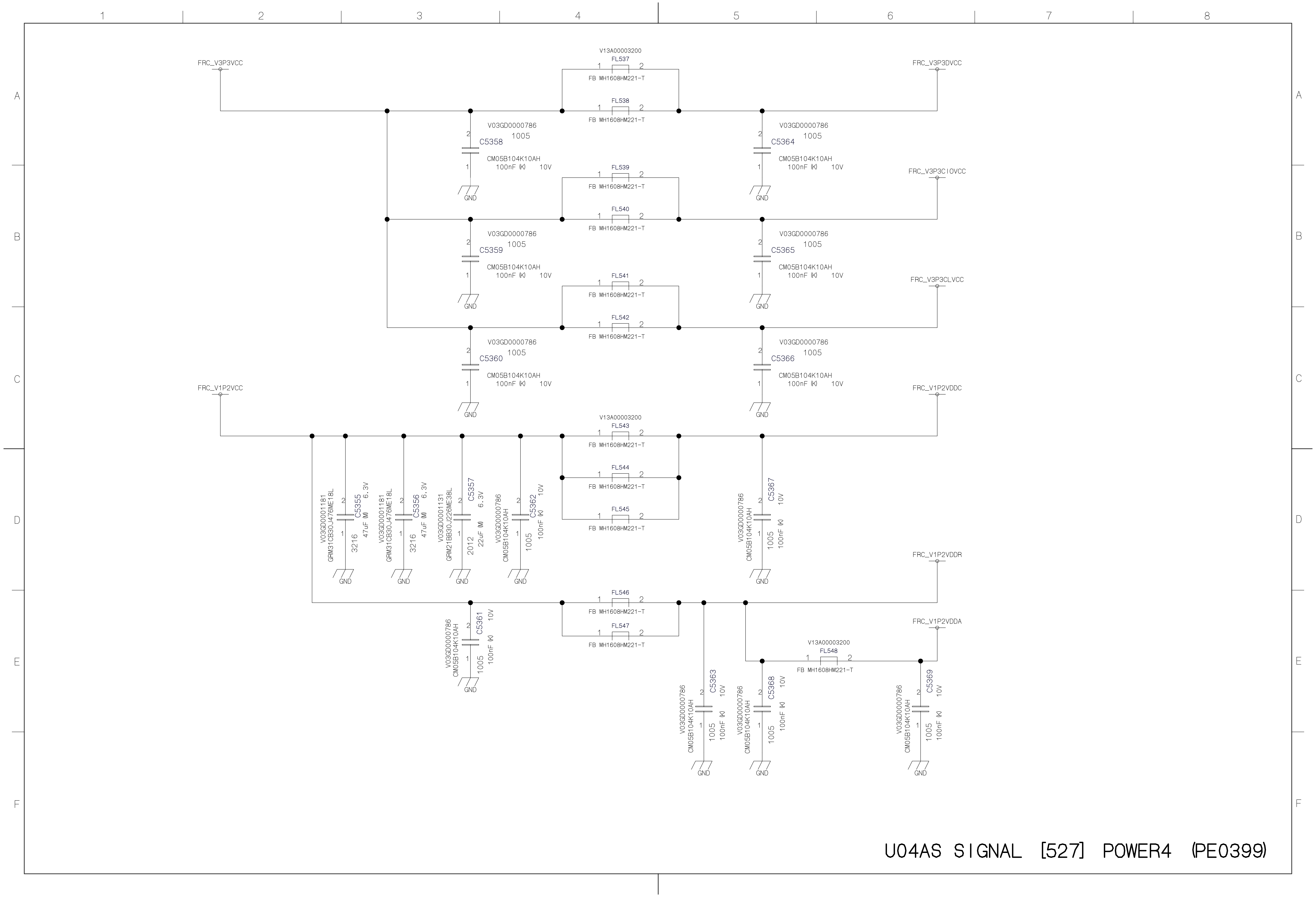

1
2
3
4
5
6
7
8
A
B
C
D
E
F
FRC_V3P3
20070306 sincse PPeq
near land close to FL524/FL525
Layer_1
C5309
330pF (K)
50V 1005
C5310
10uF (K)
10V 2012
C5521
V03GD0000786
100nF (K)
1005
C5522
V03GD0000763
10nF (K)
1005
GND
FL524
220ohm
FL525
220ohm
NOT MOUNT
C5311
22uF (K)
10V 3216
R5159
1.2K (D)
1005
C5313
2.2nF (K)
50V 1005
R5161
10K (F)
1005
R5163
11K (F)
1005
R5167
3K (D)
1005
C5323
1.5nF (K)
50V 1005
C5317
68pF (J)
50V 1005
R5165
8.2K (F)
1005
R5169
1.2K (D)
1005
C5319
100nF (K)
10V 1005
C5320
100nF (K)
10V 1005
C5315
22uF (K)
10V 3216
C5324
100nF (K)
10V 1005
C5327
10nF (K)
16V 1005
V47GD0001102
TPS54310PWPRG4
PWM SWITCH
[V-L54310]
IC515
AGND
VSENSE
COMP
PWRGD
VBIAS
SS/ENA
SYNC
RT
VIN
BOOT
PH
PGND
TH_PAD
C5329
47nF (K)
10V 1005
SLF7055T-4R7N3R1-3PF
L5003
V05A000019H0
C5331
150uF (M)
4V
[CP]
C5333
10uF (K)
10V 2012
C5335
100nF (K)
10V 1005
FL528
220ohm
FL529
220ohm
NOT MOUNT
C5337
10nF (K)
16V 1005
FRC_V1P8VCC
20070409 sincse PPeq
near land close to FL526/FL527
Layer_1
C5528
V03GD0000786
100nF (K)
1005
C5529
V03GD0000763
10nF (K)
1005
FL526
220ohm
FL527
220ohm
NOT MOUNT
C5312
22uF (K)
10V 3216
R5160
1.2K (D)
1005
C5314
2.2nF (K)
50V 1005
R5162
10K (F)
1005
R5164
11K (F)
1005
R5168
3K (D)
1005
C5325
1.5nF (K)
50V 1005
C5318
68pF (J)
50V 1005
R5166
24K (D)
1005
R5170
2K (F)
1005
C5321
100nF (K)
10V 1005
C5322
47nF (K)
10V 1005
C5316
22uF (K)
10V 3216
C5326
100nF (K)
10V 1005
C5328
10nF (K)
16V 1005
V47GD0001102
TPS54310PWPRG4
PWM SWITCH
[V-L54310]
IC516
C5330
47nF (K)
10V 1005
SLF7055T-4R7N3R1-3PF
L5004
V05A000019H0
C5332
150uF (M)
4V
[CP]
C5334
10uF (K)
10V 2012
C5336
100nF (K)
10V 1005
FL530
220ohm
FL531
220ohm
NOT MOUNT
C5338
10nF (K)
16V 1005
C5340
100pF (J)
50V 1005
FRC_V1P2VCC
GND

U04AS SIGNAL [525] POWER2 (PE0399)

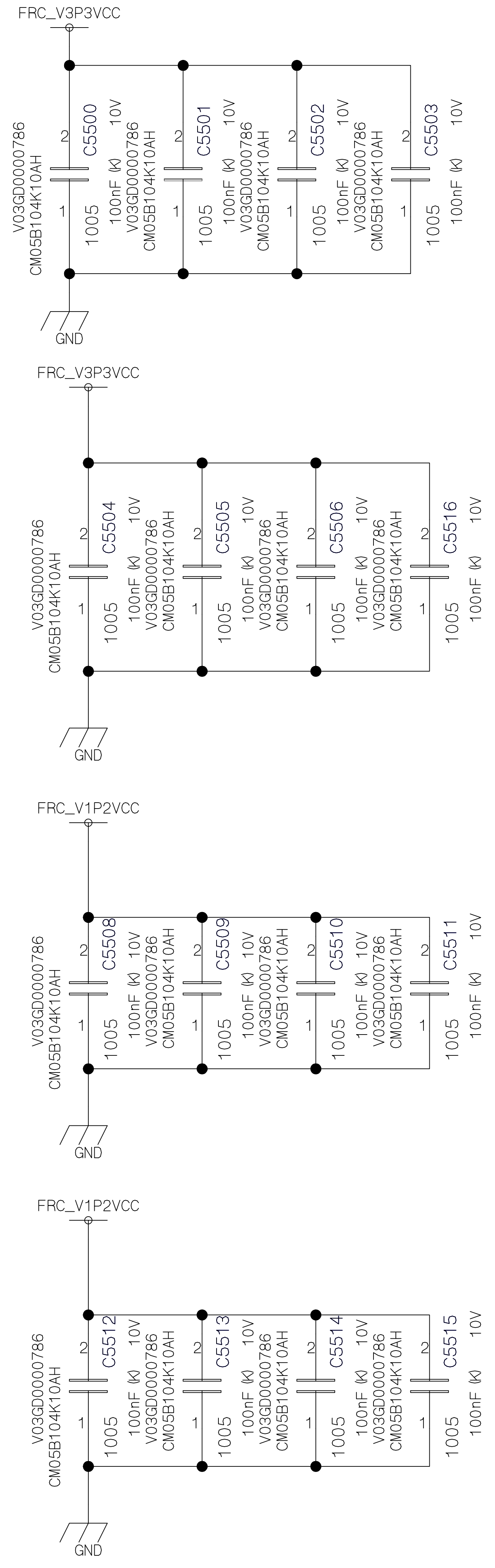


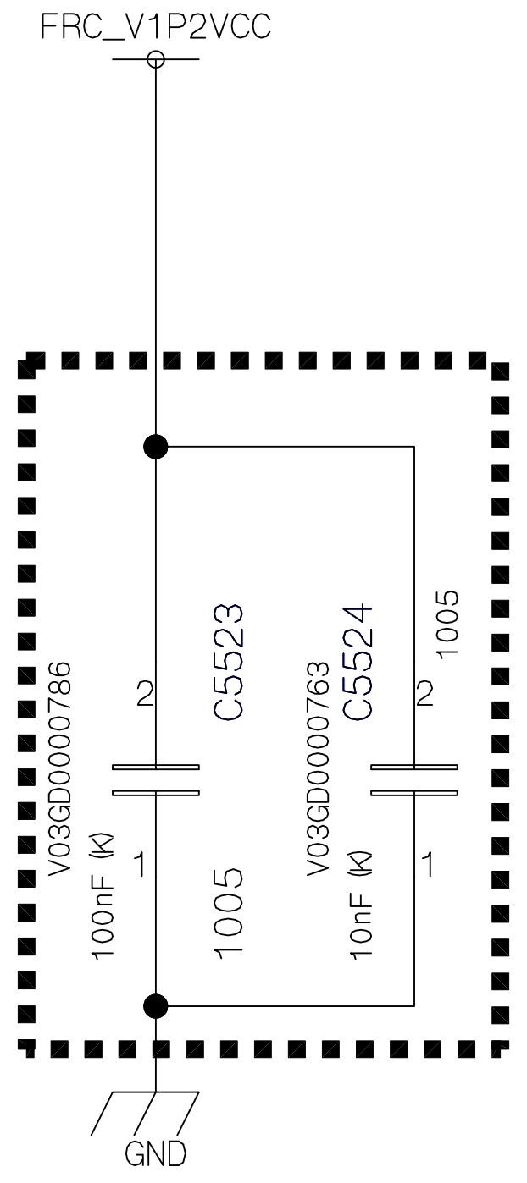
1
2
3
4
5
6
7
8
A
B
C
D
E
F
LOWBから直接の3.3V
FRC_V3P3
XC6219B122MR
UREG
[V-L6219BXXX]
IC517
CE
NC
VIN
VOUT
[VSS]
C5339
1uF (K)
V03GD0000828
JMK105BJ105KV-F
6.3V 1005
GND
C5342
1uF (K)
6.3V
JMK105BJ105KV-F
V03GD0000828
C5343
100nF (K)
V03GD0000786
CM05B104K10AH
10V 1005
L5006
22uH (M)
60mA
V03GD0001318
C2012JB1A106KT
10V 2012
NOT MOUNT
C5345
10uF (K)
FRC_V1P2VCC_PLL
C5346
10uF (K)
V03GD0001318
C2012JB1A106KT
10V 2012
IC500の付近に配置する
FL533
FB MH1608HM221-T
C5347
100nF (K)
V03GD0000786
CM05B104K10AH
10V 1005
C5351
100nF (K)
W5114
V01GD0001695
ERJ2GE0R00X
63mW 1005
FRC_PLL_AVD1
FRC_PLL_AVS1
FRC_PLL_AVD3
FL534
C5348
C5352
W5115
FRC_PLL_AVS3
1.2Vが立ち上がった後に立ち上がる3.3V
FRC_V3P3VCC
L5005
22uH (M)
60mA
V47A00001200
XC6219B252MR
RGL
[L1XXX1M2ZP]
IC518
VC
VIN
GND
VO
NR
C5341
470nF (K)
V03GD0000716
CM105B474K10AT
10V 1608
C5344
10uF (K)
10V
FRC_V2P5VCC
FL532
30ohm
L5008
22uH (M)
60mA
FRC_PVDDA1
FL535
C5349
C5353
W5116
FRC_SSCG_GND
FRC_PVSSA1
FRC_PVDDA2
FL536
C5350
C5354
W5117
FRC_PVSSA2

U04AS SIGNAL [526] POWER3 (PE0399)

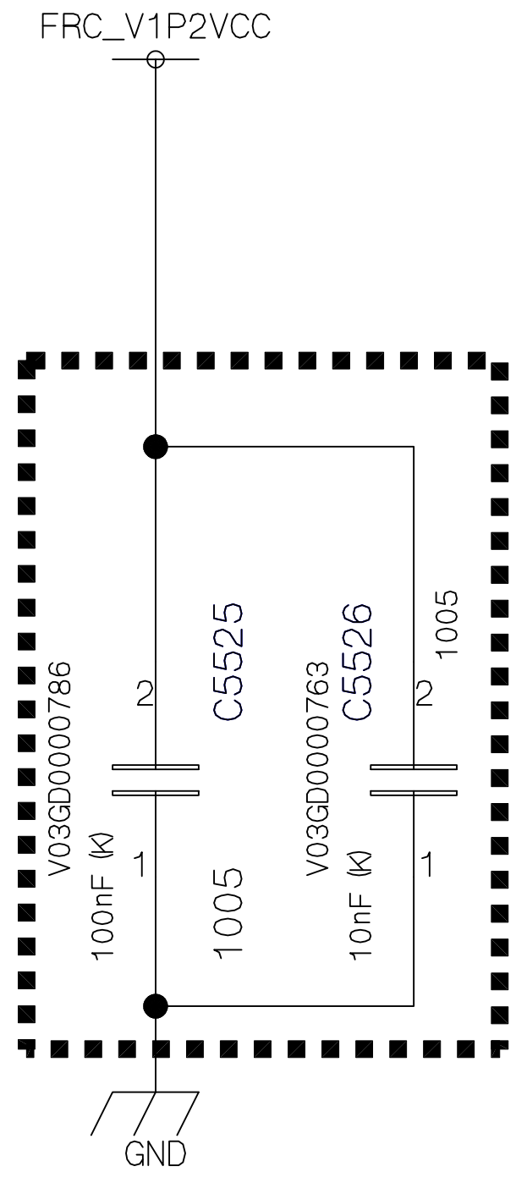

FRC_V3P3VCC
FRC_V3P3DVCC
FRC_V3P3CIOVCC
FRC_V3P3CLVCC
FRC_V1P2VCC
FRC_V1P2VDDC
FRC_V1P2VDDR
FRC_V1P2VDDA
V13A00003200
FL537
FB MH1608HM221-T
FL538
FB MH1608HM221-T
FL539
FB MH1608HM221-T
FL540
FB MH1608HM221-T
FL541
FB MH1608HM221-T
FL542
FB MH1608HM221-T
V13A00003200
FL543
FB MH1608HM221-T
FL544
FB MH1608HM221-T
FL545
FB MH1608HM221-T
FL546
FB MH1608HM221-T
FL547
FB MH1608HM221-T
V13A00003200
FL548
FB MH1608HM221-T
V03GD0000786
C5358
1005
CM05B104K10AH
100nF (K) 10V
V03GD0000786
C5359
1005
CM05B104K10AH
100nF (K) 10V
V03GD0000786
C5360
1005
CM05B104K10AH
100nF (K) 10V
V03GD0000786
C5364
1005
CM05B104K10AH
100nF (K) 10V
V03GD0000786
C5365
1005
CM05B104K10AH
100nF (K) 10V
V03GD0000786
C5366
1005
CM05B104K10AH
100nF (K) 10V
V03GD0001181
GRM31CB30J476ME18L
C5355
6.3V
47uF (M)
3216
V03GD0001181
GRM31CB30J476ME18L
C5356
6.3V
47uF (M)
3216
V03GD0001131
GRM21BB30J226ME38L
C5357
6.3V
22uF (M)
2012
V03GD0000786
CM05B104K10AH
C5362
10V
100nF (K)
1005
V03GD0000786
CM05B104K10AH
C5367
10V
100nF (K)
1005
V03GD0000786
CM05B104K10AH
C5361
10V
100nF (K)
1005
V03GD0000786
CM05B104K10AH
C5363
10V
100nF (K)
1005
V03GD0000786
CM05B104K10AH
C5368
10V
100nF (K)
1005
V03GD0000786
CM05B104K10AH
C5369
10V
100nF (K)
1005
GND
1
2
3
4
5
6
7
8
A
B
C
D
E
F

U04AS SIGNAL [527] POWER4 (PE0399)


FRC_V3P3VCC
C5500
C5501
C5502
C5503
100nF (K) 10V
V03GD0000786
CM05B104K10AH
1005
GND
FRC_V3P3VCC
C5504
C5505
C5506
C5516
GND
FRC_V1P2VCC
C5508
C5509
C5510
C5511
GND
FRC_V1P2VCC
C5512
C5513
C5514
C5515
GND

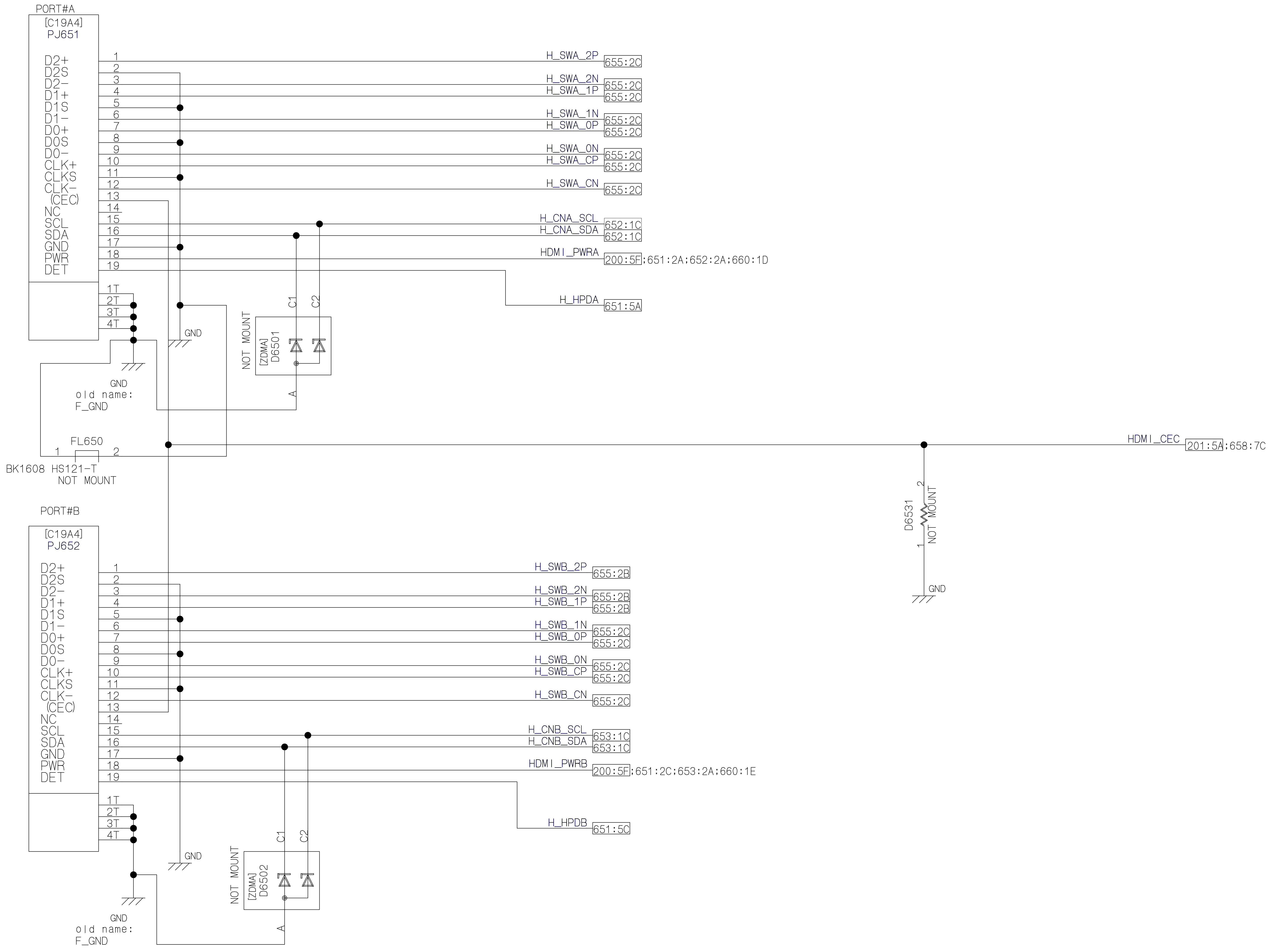

FRC_V1P2VCC
C5523
V03GD0000786
100nF (K)
1005
C5524
V03GD0000763
10nF (K)
1005
GND

20070306 sincse PPeq
Layer No.1
near land close to FL530/FL531

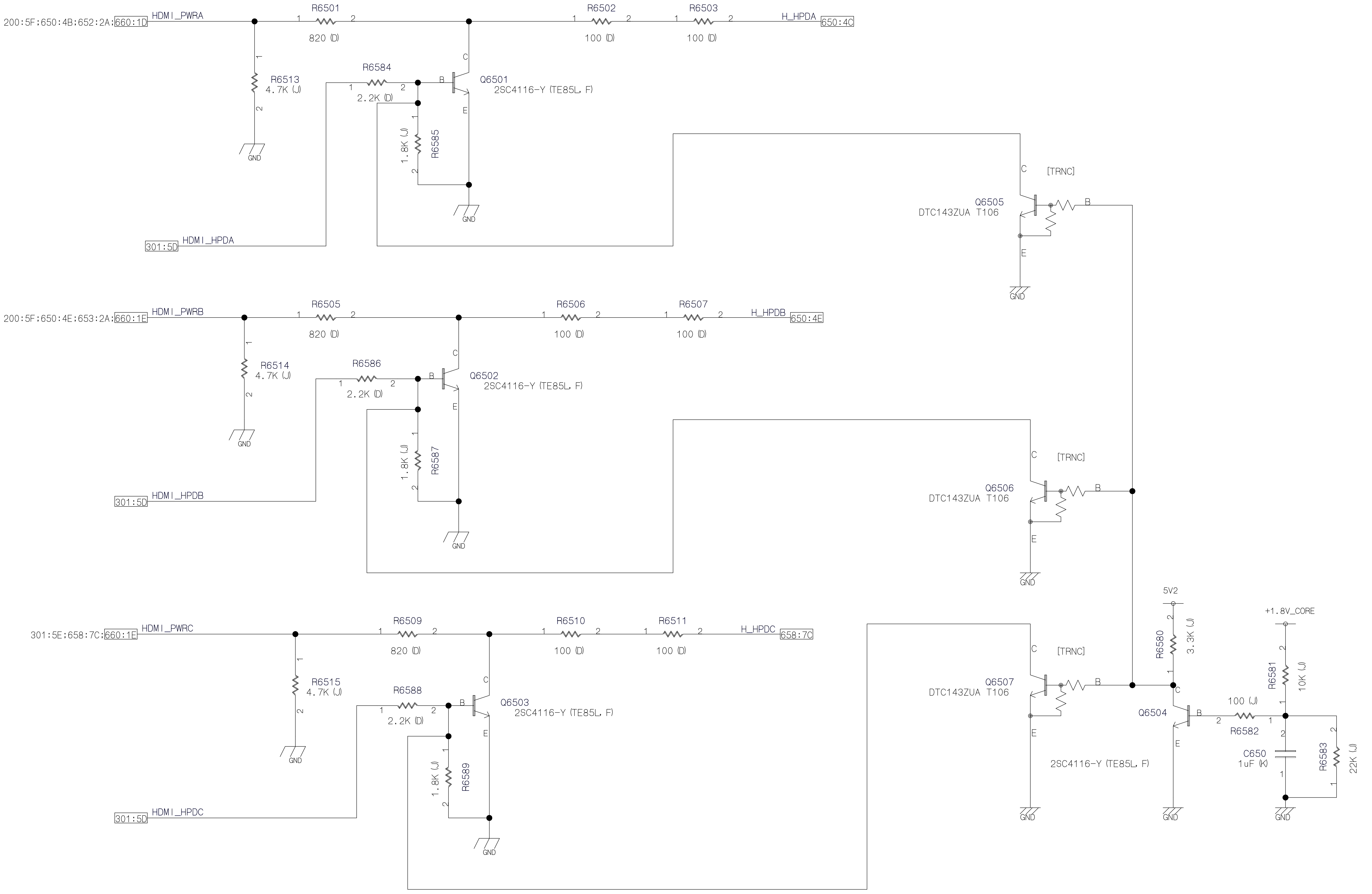

FRC_V1P2VCC
C5525
V03GD0000786
100nF (K)
1005
C5526
V03GD0000763
10nF (K)
1005
GND

20070306 sincse PPeq
Layer No.6
between new land and GND

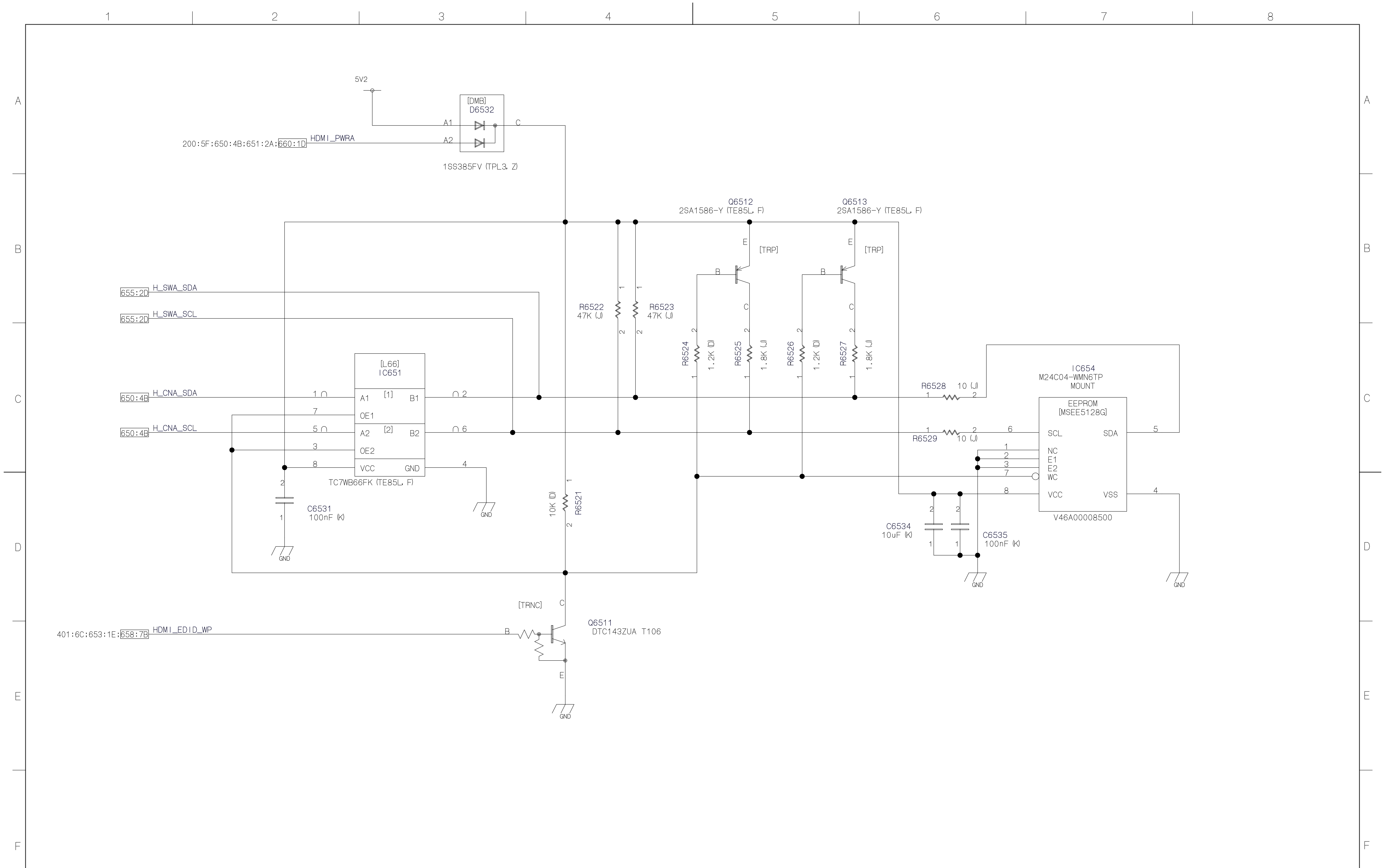
U04AS SIGNAL [528] CAPACITOR (PE0399)


PORT#A
[C19A4]
PJ651
D2+
D2S
D2−
D1+
D1S
D1−
D0+
D0S
D0−
CLK+
CLKS
CLK−
(CEC)
NC
SCL
SDA
GND
PWR
DET
1
2
3
4
5
6
7
8
9
10
11
12
13
14
15
16
17
18
19
1T
2T
3T
4T
H_SWA_2P 655:2C
H_SWA_2N 655:2C
H_SWA_1P 655:2C
H_SWA_1N 655:2C
H_SWA_0P 655:2C
H_SWA_0N 655:2C
H_SWA_CP 655:2C
H_SWA_CN 655:2C
H_CNA_SCL 652:1C
H_CNA_SDA 652:1C
HDMI_PWRA 200:5F;651:2A;652:2A;660:1D
H_HPDA 651:5A
GND
GND
old name:
F_GND
C1
C2
NOT MOUNT
[ZDMA]
D6501
A
FL650
1
2
BK1608 HS121-T
NOT MOUNT
HDMI_CEC 201:5A;658:7C
D6531
NOT MOUNT
2
1
GND
PORT#B
[C19A4]
PJ652
D2+
D2S
D2−
D1+
D1S
D1−
D0+
D0S
D0−
CLK+
CLKS
CLK−
(CEC)
NC
SCL
SDA
GND
PWR
DET
1
2
3
4
5
6
7
8
9
10
11
12
13
14
15
16
17
18
19
1T
2T
3T
4T
H_SWB_2P 655:2B
H_SWB_2N 655:2B
H_SWB_1P 655:2B
H_SWB_1N 655:2C
H_SWB_0P 655:2C
H_SWB_0N 655:2C
H_SWB_CP 655:2C
H_SWB_CN 655:2C
H_CNB_SCL 653:1C
H_CNB_SDA 653:1C
HDMI_PWRB 200:5F;651:2C;653:2A;660:1E
H_HPDB 651:5C
GND
GND
old name:
F_GND
C1
C2
NOT MOUNT
[ZDMA]
D6502
A

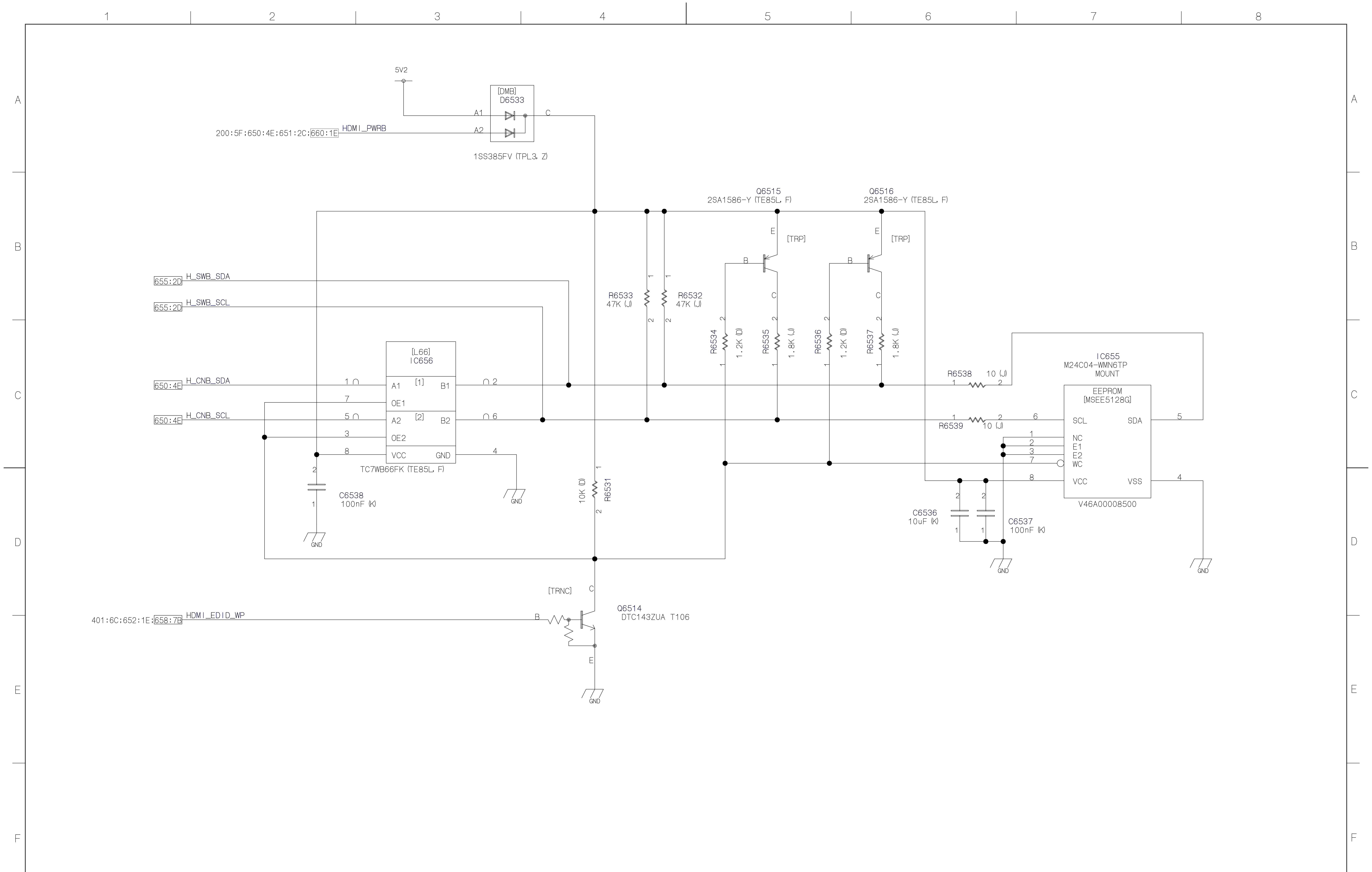
U04AS SIGNAL [650] HDMI (CONNECTOR), HPD (PE0399)


200:5F:650:4B:652:2A:660:1D
HDMI_PWRA
R6501
820 (D)
R6502
100 (D)
R6503
100 (D)
H_HPDA
650:4C
R6513
4.7K (J)
R6584
2.2K (D)
Q6501
2SC4116-Y (TE85L, F)
R6585
1.8K (J)
GND
301:5D
HDMI_HPDA
Q6505
DTC143ZUA T106
[TRNC]
200:5F:650:4E:653:2A:660:1E
HDMI_PWRB
R6505
820 (D)
R6506
100 (D)
R6507
100 (D)
H_HPDB
650:4E
R6514
4.7K (J)
R6586
2.2K (D)
Q6502
2SC4116-Y (TE85L, F)
R6587
1.8K (J)
301:5D
HDMI_HPDB
Q6506
DTC143ZUA T106
[TRNC]
301:5E:658:7C:660:1E
HDMI_PWRC
R6509
820 (D)
R6510
100 (D)
R6511
100 (D)
H_HPDC
658:7C
R6515
4.7K (J)
R6588
2.2K (D)
Q6503
2SC4116-Y (TE85L, F)
R6589
1.8K (J)
301:5D
HDMI_HPDC
Q6507
DTC143ZUA T106
[TRNC]
5V2
R6580
3.3K (J)
Q6504
2SC4116-Y (TE85L, F)
R6582
100 (J)
+1.8V_CORE
R6581
10K (J)
C650
1uF (K)
R6583
22K (J)

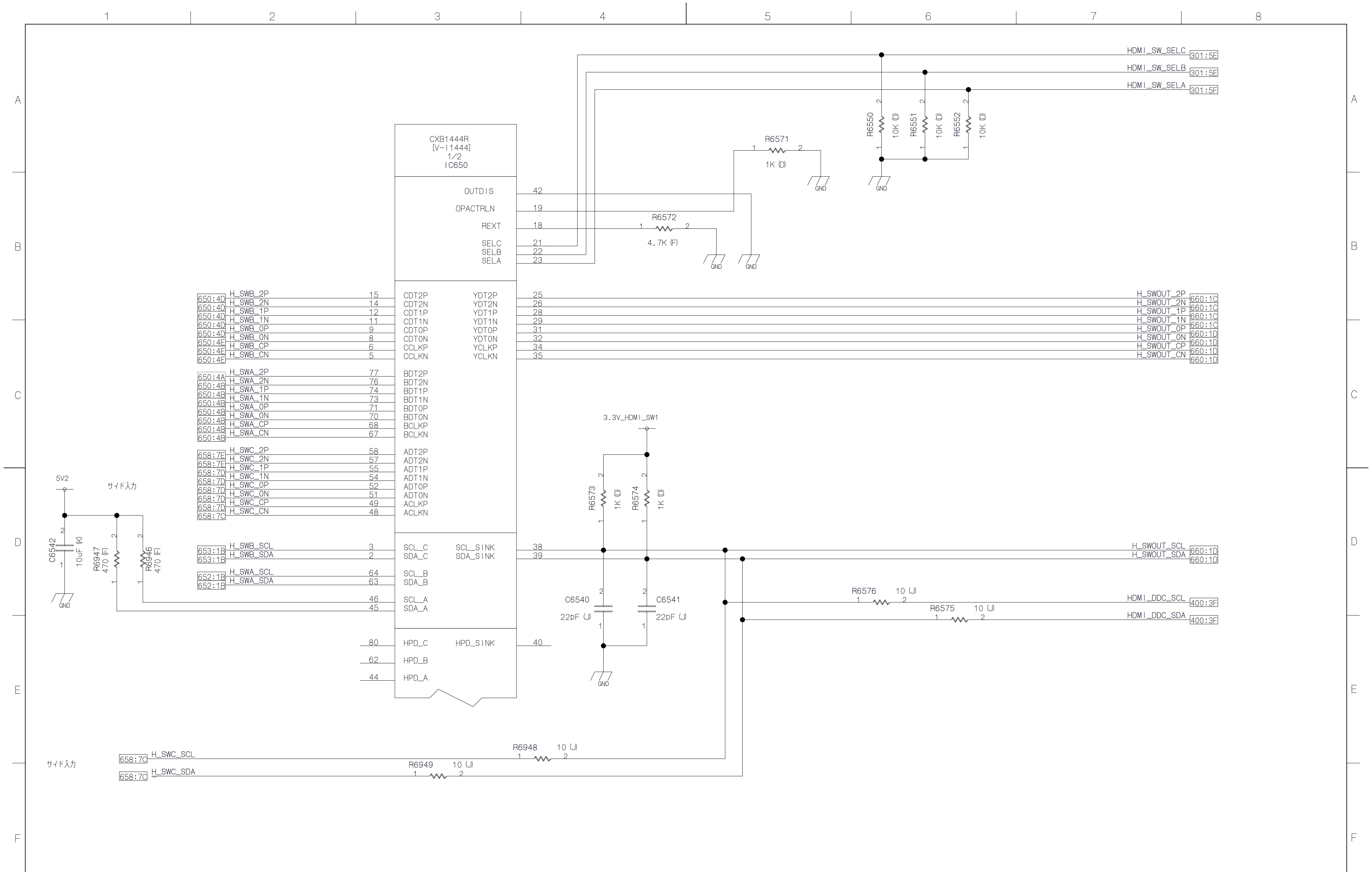
U04AS SIGNAL [651] HDMI#0 (TMDS_SW) (PE0399)


1
2
3
4
5
6
7
8
A
B
C
D
E
F
5V2
[DMB]
D6532
A1
A2
C
200:5F;650:4B;651:2A;660:1D
HDMI_PWRA
1SS385FV (TPL3, Z)
Q6512
2SA1586-Y (TE85L, F)
Q6513
2SA1586-Y (TE85L, F)
E
B
C
[TRP]
655:2D
H_SWA_SDA
655:2D
H_SWA_SCL
R6522
47K (J)
R6523
47K (J)
R6524
1.2K (D)
R6525
1.8K (J)
R6526
1.2K (D)
R6527
1.8K (J)
[L66]
IC651
650:4B
H_CNA_SDA
650:4B
H_CNA_SCL
A1
[1]
B1
OE1
A2
[2]
B2
OE2
VCC
GND
TC7WB66FK (TE85L, F)
C6531
100nF (K)
GND
10K (D)
R6521
R6528
10 (J)
R6529
10 (J)
IC654
M24C04-WMN6TP
MOUNT
EEPROM
[MSEE5128G]
SCL
SDA
NC
E1
E2
WC
VCC
VSS
V46A00008500
C6534
10uF (K)
C6535
100nF (K)
[TRNC]
Q6511
DTC143ZUA T106
401:6C;653:1E;658:7B
HDMI_EDID_WP

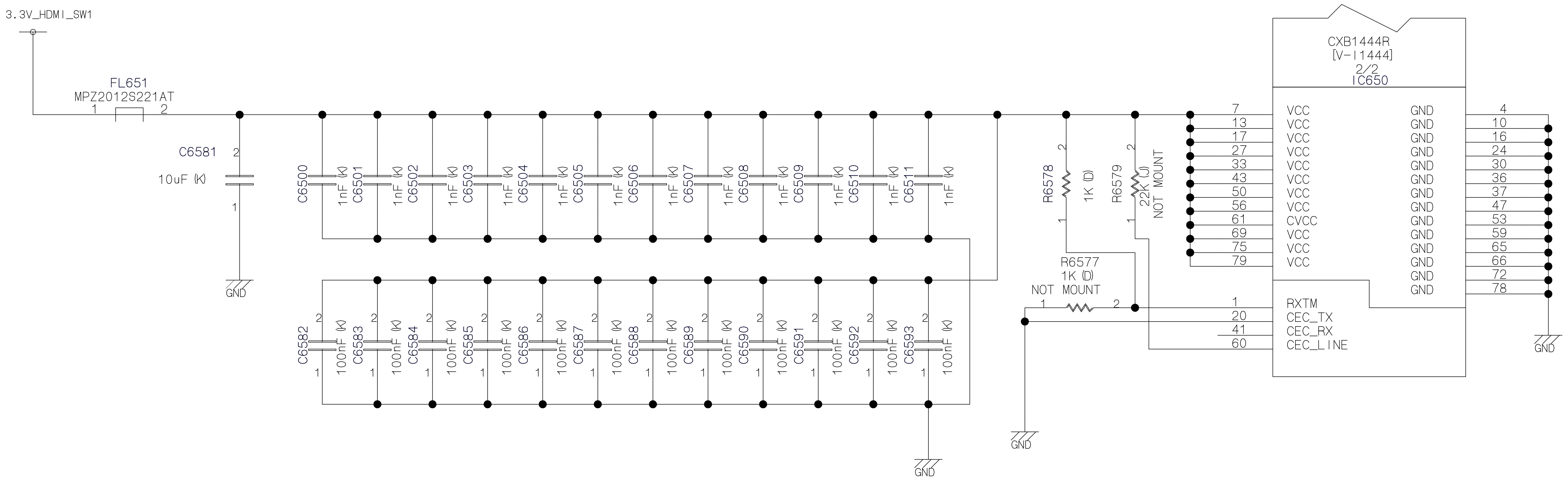
U04AS SIGNAL [652] HDMI EEPROM[EDID] (PE0399)


1
2
3
4
5
6
7
8
A
B
C
D
E
F
5V2
[DMB]
D6533
A1
A2
C
200:5F:650:4E:651:2C:660:1E
HDMI_PWRB
1SS385FV (TPL3, Z)
Q6515
2SA1586-Y (TE85L, F)
Q6516
2SA1586-Y (TE85L, F)
E
B
C
[TRP]
655:2D
H_SWB_SDA
655:2D
H_SWB_SCL
R6533
47K (J)
R6532
47K (J)
R6534
1.2K (D)
R6535
1.8K (J)
R6536
1.2K (D)
R6537
1.8K (J)
[L66]
IC656
650:4E
H_CNB_SDA
650:4E
H_CNB_SCL
A1
[1]
B1
OE1
A2
[2]
B2
OE2
VCC
GND
TC7WB66FK (TE85L, F)
C6538
100nF (K)
GND
R6538
10 (J)
R6539
10 (J)
IC655
M24C04-WMN6TP
MOUNT
EEPROM
[MSEE5128G]
SCL
SDA
NC
E1
E2
WC
VCC
VSS
V46A00008500
C6536
10uF (K)
C6537
100nF (K)
10K (D)
R6531
[TRNC]
Q6514
DTC143ZUA T106
401:6C:652:1E:658:7B
HDMI_EDID_WP

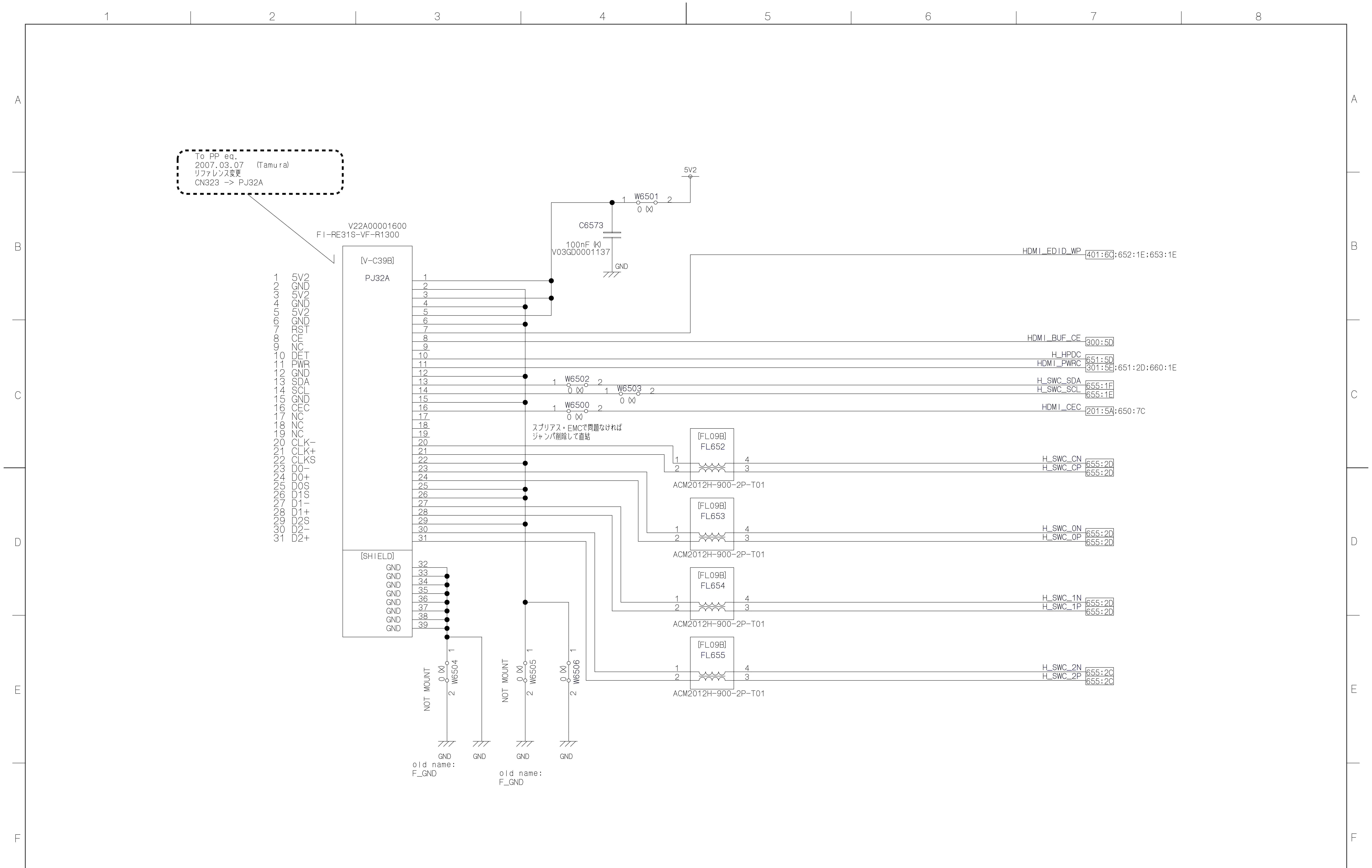
U04AS SIGNAL [653] HDMI EEPROM [EDID] (PE0399)


1
2
3
4
5
6
7
8
A
B
C
D
E
F
CXB1444R
[V-I1444]
1/2
IC650
OUTDIS 42
OPACTRLN 19
REXT 18
SELC 21
SELB 22
SELA 23
R6571 1 2
1K (D)
R6572 1 2
4.7K (F)
GND
HDMI_SW_SELC 301:5E
HDMI_SW_SELB 301:5E
HDMI_SW_SELA 301:5F
R6550 10K (D)
R6551 10K (D)
R6552 10K (D)
650:4D H_SWB_2P 15 CDT2P
650:4D H_SWB_2N 14 CDT2N
650:4D H_SWB_1P 12 CDT1P
650:4D H_SWB_1N 11 CDT1N
650:4D H_SWB_0P 9 CDT0P
650:4E H_SWB_0N 8 CDT0N
650:4E H_SWB_CP 6 CCLKP
650:4E H_SWB_CN 5 CCLKN
650:4A H_SWA_2P 77 BDT2P
650:4B H_SWA_2N 76 BDT2N
650:4B H_SWA_1P 74 BDT1P
650:4B H_SWA_1N 73 BDT1N
650:4B H_SWA_0P 71 BDT0P
650:4B H_SWA_0N 70 BDT0N
650:4B H_SWA_CP 68 BCLKP
650:4B H_SWA_CN 67 BCLKN
658:7E H_SWC_2P 58 ADT2P
658:7E H_SWC_2N 57 ADT2N
658:7D H_SWC_1P 55 ADT1P
658:7D H_SWC_1N 54 ADT1N
658:7D H_SWC_0P 52 ADT0P
658:7D H_SWC_0N 51 ADT0N
658:7D H_SWC_CP 49 ACLKP
658:7C H_SWC_CN 48 ACLKN
YDT2P 25 H_SWOUT_2P 660:1C
YDT2N 26 H_SWOUT_2N 660:1C
YDT1P 28 H_SWOUT_1P 660:1C
YDT1N 29 H_SWOUT_1N 660:1C
YDT0P 31 H_SWOUT_0P 660:1D
YDT0N 32 H_SWOUT_0N 660:1D
YCLKP 34 H_SWOUT_CP 660:1D
YCLKN 35 H_SWOUT_CN 660:1D
653:1B H_SWB_SCL 3 SCL_C
653:1B H_SWB_SDA 2 SDA_C
652:1B H_SWA_SCL 64 SCL_B
652:1B H_SWA_SDA 63 SDA_B
46 SCL_A
45 SDA_A
SCL_SINK 38 H_SWOUT_SCL 660:1D
SDA_SINK 39 H_SWOUT_SDA 660:1D
80 HPD_C
62 HPD_B
44 HPD_A
HPD_SINK 40
5V2
サイド入力
C6542 10uF (K)
R6947 470 (F)
R6946 470 (F)
3.3V_HDMI_SW1
R6573 1K (D)
R6574 1K (D)
C6540 22pF (J)
C6541 22pF (J)
R6576 10 (J)
HDMI_DDC_SCL 400:3F
R6575 10 (J)
HDMI_DDC_SDA 400:3F
サイド入力
658:7C H_SWC_SCL
658:7C H_SWC_SDA
R6948 10 (J)
R6949 10 (J)

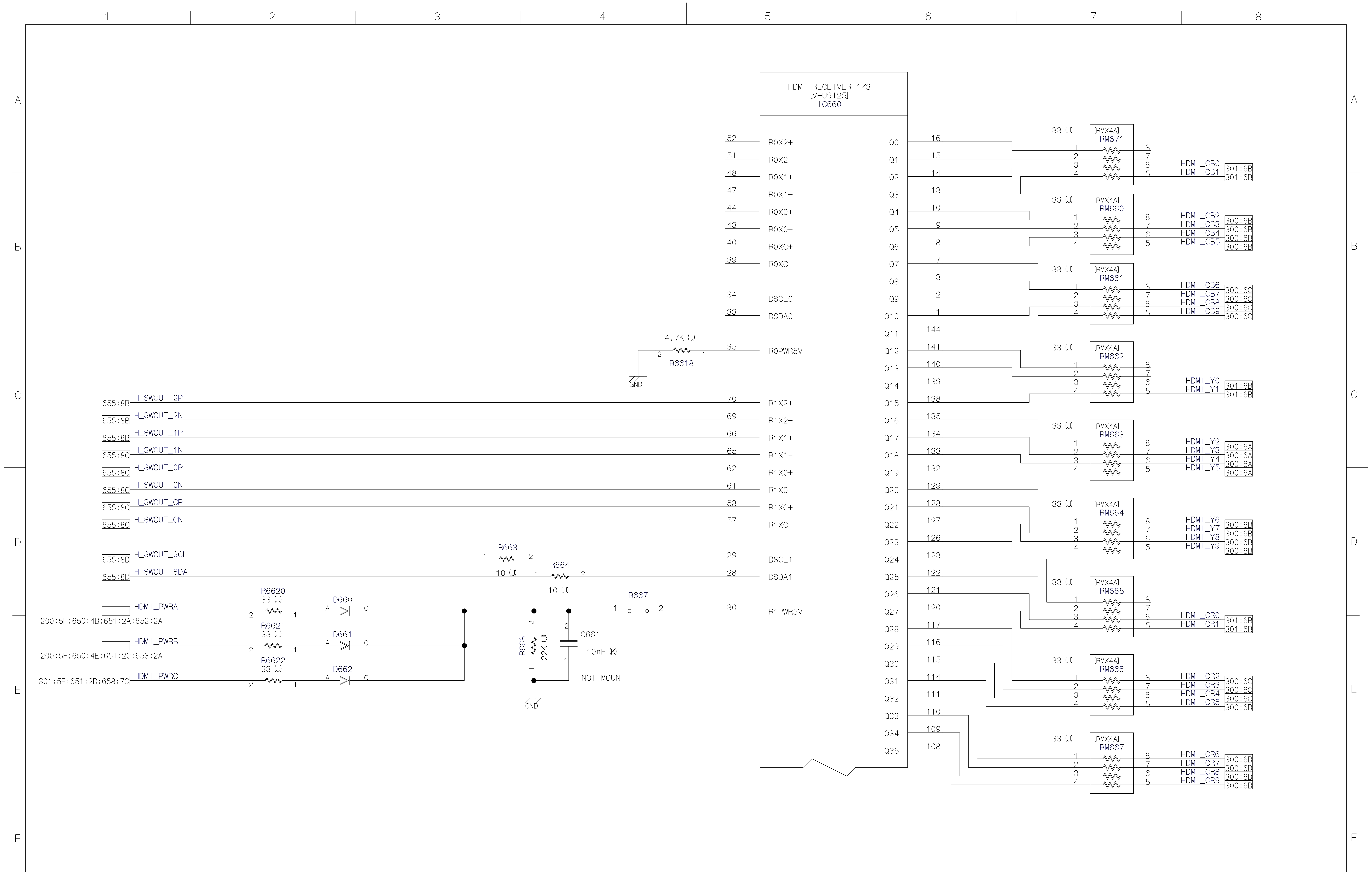
U04AS SIGNAL [655] TMDS SW (PE0399)


3.3V_HDMI_SW1
FL651
MPZ2012S221AT
C6581
10uF (K)
GND
C6500 1nF (K)
C6501 1nF (K)
C6502 1nF (K)
C6503 1nF (K)
C6504 1nF (K)
C6505 1nF (K)
C6506 1nF (K)
C6507 1nF (K)
C6508 1nF (K)
C6509 1nF (K)
C6510 1nF (K)
C6511 1nF (K)
C6582 100nF (K)
C6583 100nF (K)
C6584 100nF (K)
C6585 100nF (K)
C6586 100nF (K)
C6587 100nF (K)
C6588 100nF (K)
C6589 100nF (K)
C6590 100nF (K)
C6591 100nF (K)
C6592 100nF (K)
C6593 100nF (K)
R6578 1K (D)
R6579 22K (J) NOT MOUNT
R6577 1K (D) NOT MOUNT
CXB1444R
[V-I1444]
2/2
IC650
7 VCC
13 VCC
17 VCC
27 VCC
33 VCC
43 VCC
50 VCC
56 VCC
61 CVCC
69 VCC
75 VCC
79 VCC
1 RXTM
20 CEC_TX
41 CEC_RX
60 CEC_LINE
GND 4
GND 10
GND 16
GND 24
GND 30
GND 36
GND 37
GND 47
GND 53
GND 59
GND 65
GND 66
GND 72
GND 78

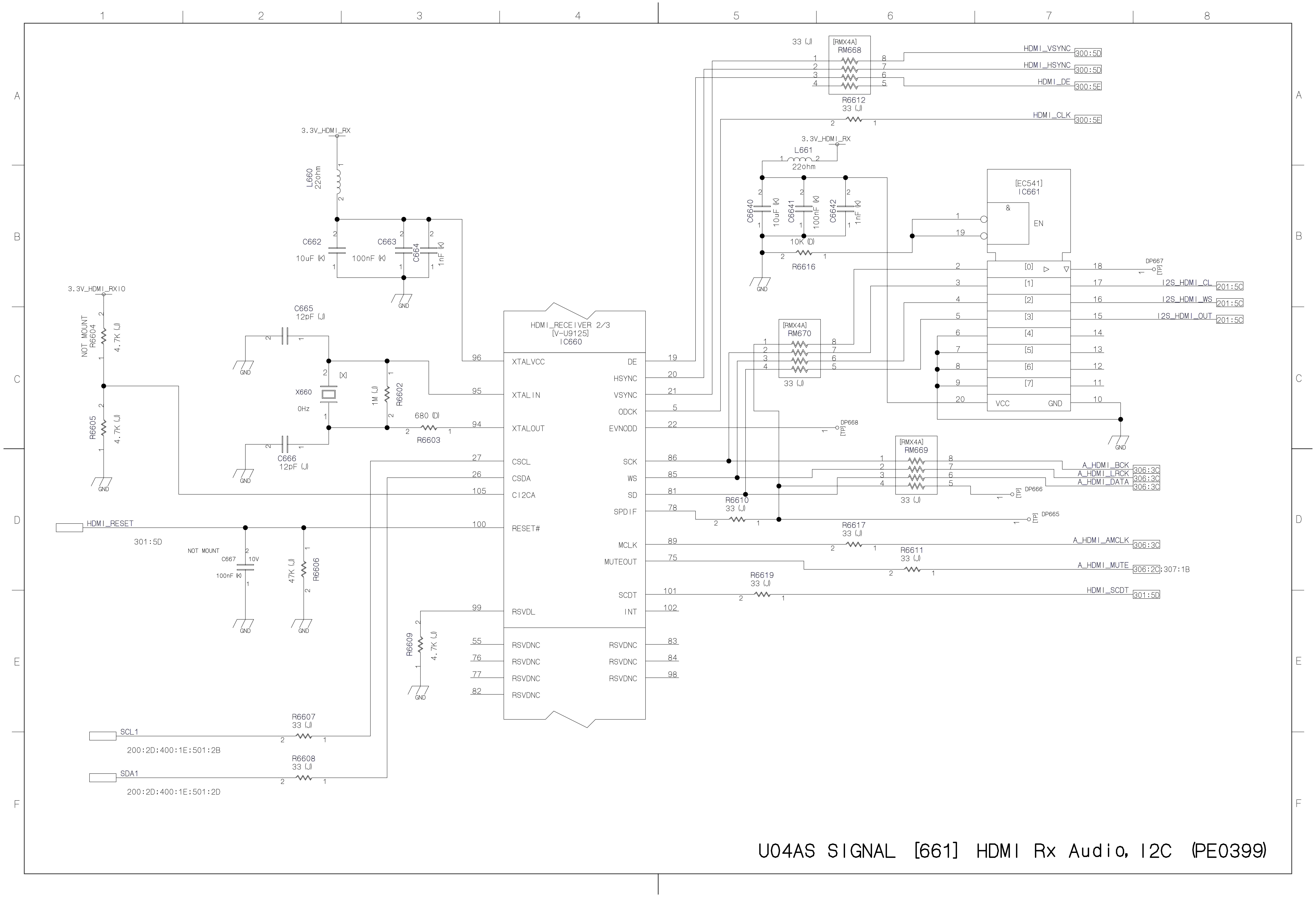
U04AS SIGNAL [657] TMDS SW (PE0399)


1
2
3
4
5
6
7
8
A
B
C
D
E
F
To PP eq.
2007.03.07 (Tamura)
リファレンス変更
CN323 -> PJ32A
V22A00001600
FI-RE31S-VF-R1300
[V-C39B]
PJ32A
1 5V2
2 GND
3 5V2
4 GND
5 5V2
6 GND
7 RST
8 CE
9 NC
10 DET
11 PWR
12 GND
13 SDA
14 SCL
15 GND
16 CEC
17 NC
18 NC
19 NC
20 CLK-
21 CLK+
22 CLKS
23 D0-
24 D0+
25 D0S
26 D1S
27 D1-
28 D1+
29 D2S
30 D2-
31 D2+
[SHIELD]
GND 32
GND 33
GND 34
GND 35
GND 36
GND 37
GND 38
GND 39
5V2
W6501
0 (X)
C6573
100nF (K)
V03GD0001137
GND
W6502
0 (X)
W6503
0 (X)
W6500
0 (X)
スプリアス・EMCで問題なければ
ジャンパ削除して直結
HDMI_EDID_WP
401:6C;652:1E;653:1E
HDMI_BUF_CE
300:5D
H_HPDC
651:5D
HDMI_PWRC
301:5E;651:2D;660:1E
H_SWC_SDA
655:1F
H_SWC_SCL
655:1E
HDMI_CEC
201:5A;650:7C
[FL09B]
FL652
ACM2012H-900-2P-T01
H_SWC_CN
655:2D
H_SWC_CP
655:2D
[FL09B]
FL653
ACM2012H-900-2P-T01
H_SWC_0N
655:2D
H_SWC_0P
655:2D
[FL09B]
FL654
ACM2012H-900-2P-T01
H_SWC_1N
655:2D
H_SWC_1P
655:2D
[FL09B]
FL655
ACM2012H-900-2P-T01
H_SWC_2N
655:2C
H_SWC_2P
655:2C
NOT MOUNT
0 (X)
W6504
NOT MOUNT
0 (X)
W6505
0 (X)
W6506
GND
GND
GND
GND
old name:
F_GND
old name:
F_GND

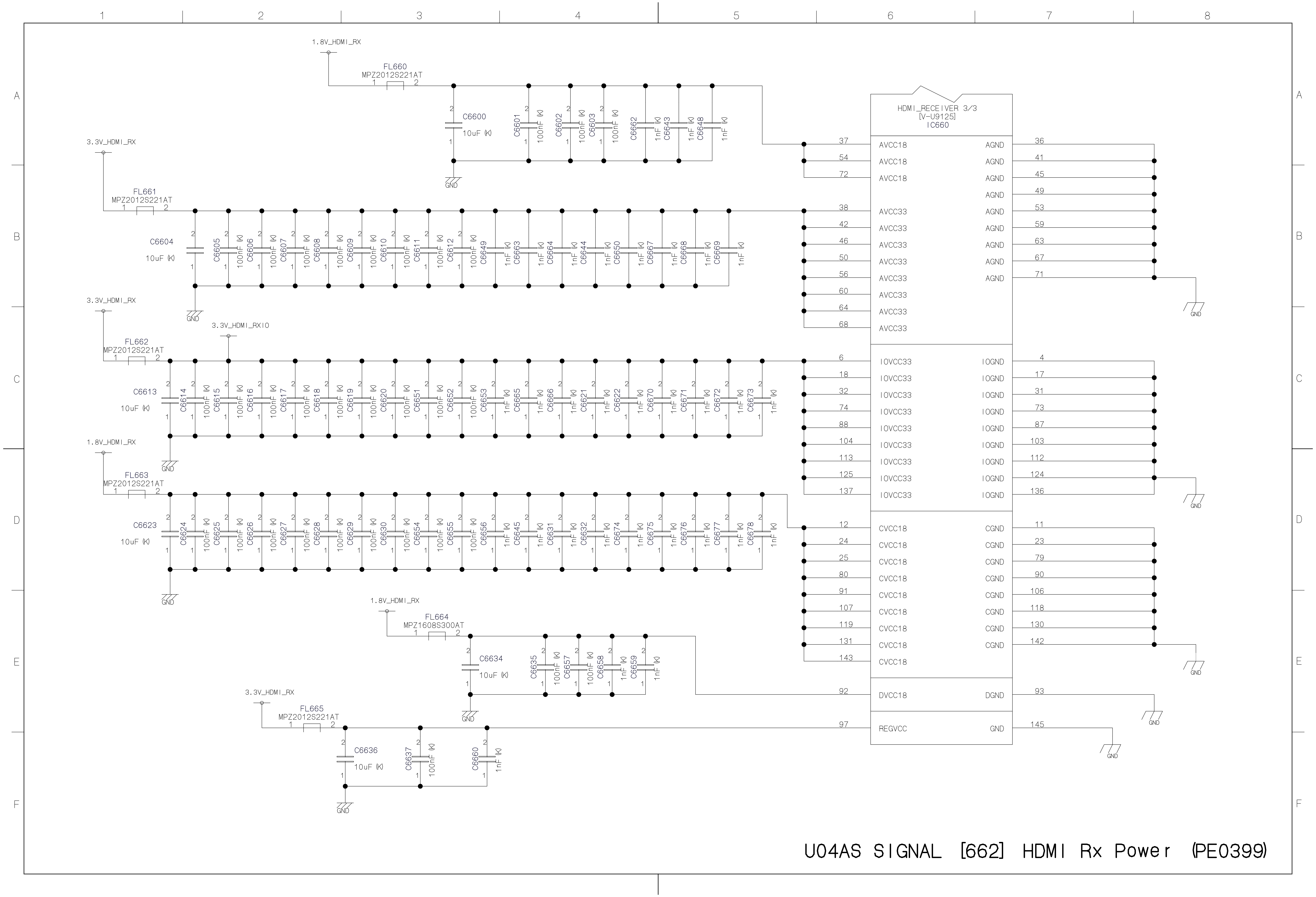
U04AS SIGNAL [658] HDMI (To_Side_CONNECTOR) (PE0399)


HDMI_RECEIVER 1/3
[V-U9125]
IC660
52 R0X2+
51 R0X2-
48 R0X1+
47 R0X1-
44 R0X0+
43 R0X0-
40 R0XC+
39 R0XC-
34 DSCL0
33 DSDA0
35 R0PWR5V
70 R1X2+
69 R1X2-
66 R1X1+
65 R1X1-
62 R1X0+
61 R1X0-
58 R1XC+
57 R1XC-
29 DSCL1
28 DSDA1
30 R1PWR5V
Q0 16
Q1 15
Q2 14
Q3 13
Q4 10
Q5 9
Q6 8
Q7 7
Q8 3
Q9 2
Q10 1
Q11 144
Q12 141
Q13 140
Q14 139
Q15 138
Q16 135
Q17 134
Q18 133
Q19 132
Q20 129
Q21 128
Q22 127
Q23 126
Q24 123
Q25 122
Q26 121
Q27 120
Q28 117
Q29 116
Q30 115
Q31 114
Q32 111
Q33 110
Q34 109
Q35 108
4.7K (J)
R6618
GND
33 (J) [RMX4A] RM671
HDMI_CB0 301:6B
HDMI_CB1 301:6B
33 (J) [RMX4A] RM660
HDMI_CB2 300:6B
HDMI_CB3 300:6B
HDMI_CB4 300:6B
HDMI_CB5 300:6B
33 (J) [RMX4A] RM661
HDMI_CB6 300:6C
HDMI_CB7 300:6C
HDMI_CB8 300:6C
HDMI_CB9 300:6C
33 (J) [RMX4A] RM662
HDMI_Y0 301:6B
HDMI_Y1 301:6B
33 (J) [RMX4A] RM663
HDMI_Y2 300:6A
HDMI_Y3 300:6A
HDMI_Y4 300:6A
HDMI_Y5 300:6A
33 (J) [RMX4A] RM664
HDMI_Y6 300:6B
HDMI_Y7 300:6B
HDMI_Y8 300:6B
HDMI_Y9 300:6B
33 (J) [RMX4A] RM665
HDMI_CR0 301:6B
HDMI_CR1 301:6B
33 (J) [RMX4A] RM666
HDMI_CR2 300:6C
HDMI_CR3 300:6C
HDMI_CR4 300:6C
HDMI_CR5 300:6D
33 (J) [RMX4A] RM667
HDMI_CR6 300:6D
HDMI_CR7 300:6D
HDMI_CR8 300:6D
HDMI_CR9 300:6D
655:8B H_SWOUT_2P
655:8B H_SWOUT_2N
655:8B H_SWOUT_1P
655:8C H_SWOUT_1N
655:8C H_SWOUT_0P
655:8C H_SWOUT_0N
655:8C H_SWOUT_CP
655:8C H_SWOUT_CN
655:8D H_SWOUT_SCL
655:8D H_SWOUT_SDA
R663
10 (J)
R664
10 (J)
R667
HDMI_PWRA 200:5F:650:4B:651:2A:652:2A
HDMI_PWRB 200:5F:650:4E:651:2C:653:2A
HDMI_PWRC 301:5E:651:2D:658:7C
R6620 33 (J)
R6621 33 (J)
R6622 33 (J)
D660
D661
D662
A
C
R668 22K (J)
C661 10nF (K)
NOT MOUNT
GND

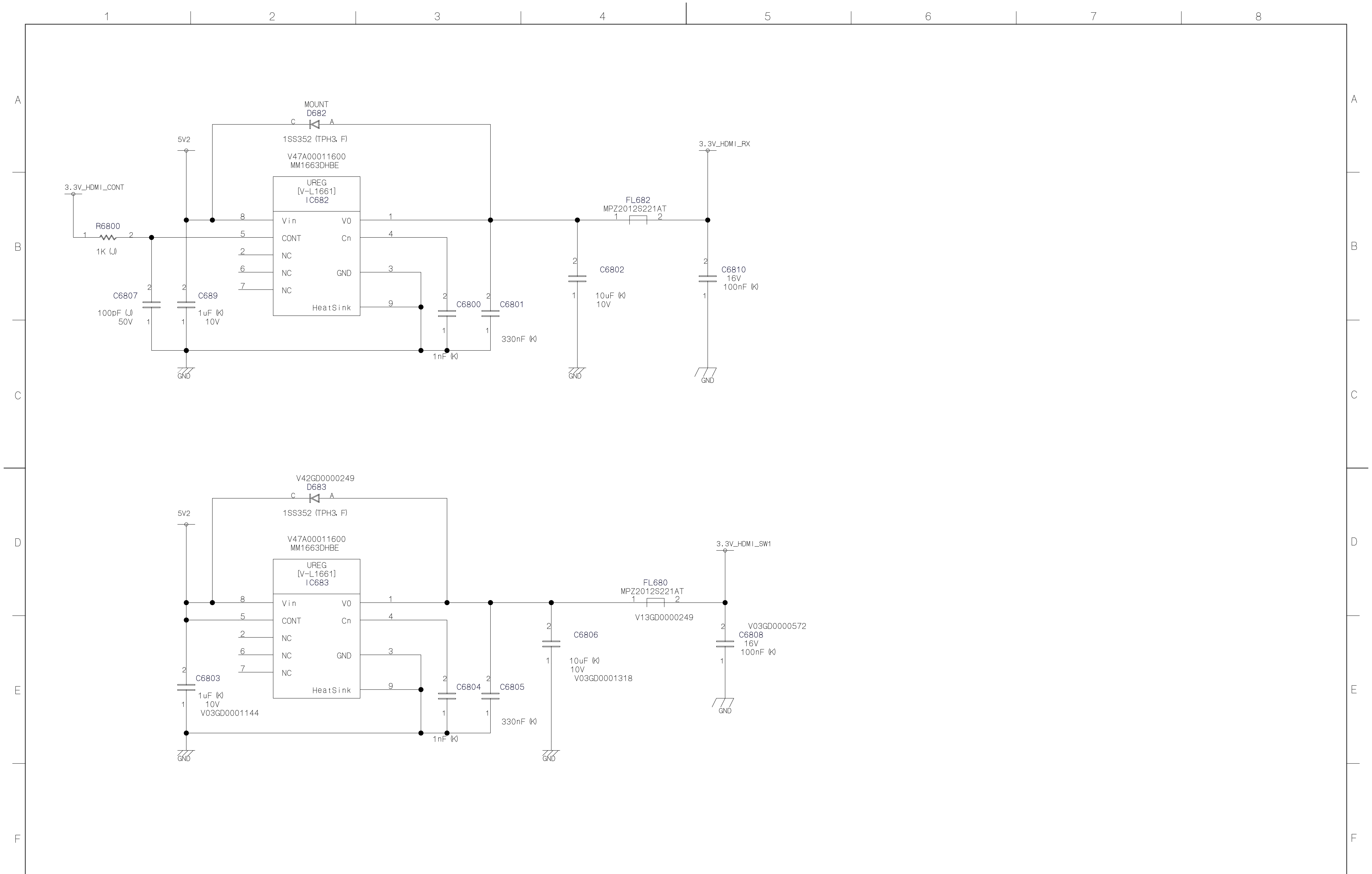
U04AS SIGNAL [660] HDMI Rx Video (PE0399)


1
2
3
4
5
6
7
8
A
B
C
D
E
F
HDMI_RECEIVER 2/3
[V-U9125]
IC660
XTALVCC
XTALIN
XTALOUT
CSCL
CSDA
CI2CA
RESET#
RSVDL
RSVDNC
DE
HSYNC
VSYNC
ODCK
EVNODD
SCK
WS
SD
SPDIF
MCLK
MUTEOUT
SCDT
INT
3.3V_HDMI_RX
L660
22ohm
C662
10uF (K)
C663
100nF (K)
C664
1nF (K)
GND
3.3V_HDMI_RXIO
NOT MOUNT
R6604
4.7K (J)
R6605
4.7K (J)
C665
12pF (J)
C666
12pF (J)
X660
0Hz
[X]
1M (J)
R6602
680 (D)
R6603
HDMI_RESET
301:5D
NOT MOUNT
C667
10V
100nF (K)
47K (J)
R6606
R6609
4.7K (J)
SCL1
200:2D:400:1E:501:2B
R6607
33 (J)
SDA1
200:2D:400:1E:501:2D
R6608
33 (J)
[RMX4A]
RM668
33 (J)
R6612
33 (J)
HDMI_VSYNC
300:5D
HDMI_HSYNC
300:5D
HDMI_DE
300:5E
HDMI_CLK
300:5E
3.3V_HDMI_RX
L661
22ohm
C6640
10uF (K)
C6641
100nF (K)
C6642
1nF (K)
10K (D)
R6616
[EC541]
IC661
&
EN
[0]
[1]
[2]
[3]
[4]
[5]
[6]
[7]
VCC
GND
DP667
[TP]
I2S_HDMI_CL
201:5C
I2S_HDMI_WS
201:5C
I2S_HDMI_OUT
201:5C
[RMX4A]
RM670
33 (J)
DP668
[TP]
[RMX4A]
RM669
33 (J)
A_HDMI_BCK
306:3C
A_HDMI_LRCK
306:3C
A_HDMI_DATA
306:3C
DP666
[TP]
R6610
33 (J)
DP665
[TP]
R6617
33 (J)
A_HDMI_AMCLK
306:3C
R6611
33 (J)
A_HDMI_MUTE
306:2C:307:1B
R6619
33 (J)
HDMI_SCDT
301:5D

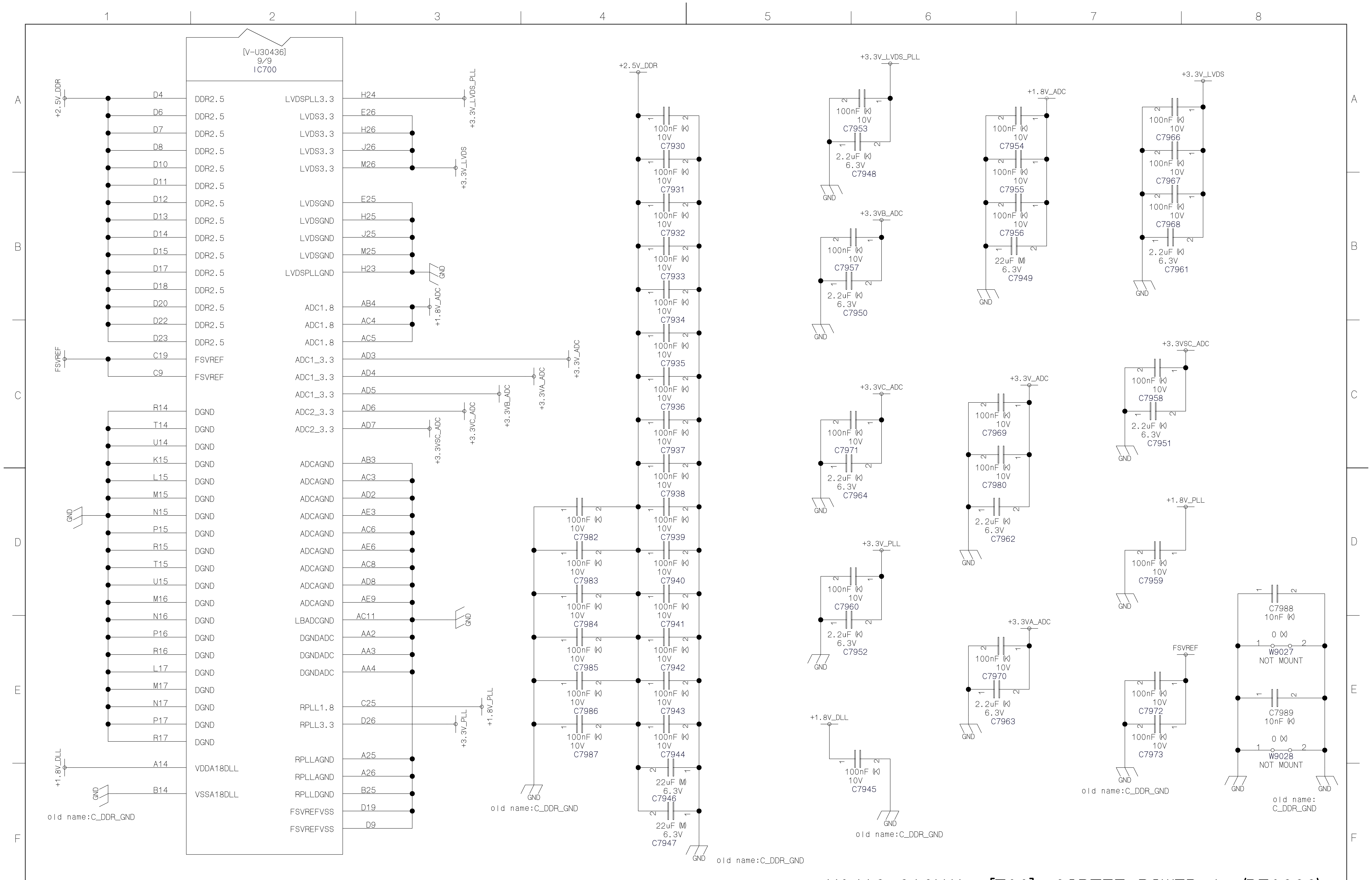
U04AS SIGNAL [661] HDMI Rx Audio, I2C (PE0399)


1.8V_HDMI_RX
FL660
MPZ2012S221AT
C6600
10uF (K)
C6601 C6602 C6603 100nF (K)
C6662 C6643 C6648 1nF (K)
GND
3.3V_HDMI_RX
FL661
MPZ2012S221AT
C6604
10uF (K)
C6605 C6606 C6607 C6608 C6609 C6610 C6611 C6612 100nF (K)
C6649 C6663 C6664 C6644 C6650 C6667 C6668 C6669 1nF (K)
3.3V_HDMI_RX
3.3V_HDMI_RXIO
FL662
MPZ2012S221AT
C6613
10uF (K)
C6614 C6615 C6616 C6617 C6618 C6619 C6620 C6651 C6652 100nF (K)
C6653 C6665 C6666 C6621 C6622 C6670 C6671 C6672 C6673 1nF (K)
1.8V_HDMI_RX
FL663
MPZ2012S221AT
C6623
10uF (K)
C6624 C6625 C6626 C6627 C6628 C6629 C6630 C6654 C6655 100nF (K)
C6656 C6645 C6631 C6632 C6674 C6675 C6676 C6677 C6678 1nF (K)
1.8V_HDMI_RX
FL664
MPZ1608S300AT
C6634
10uF (K)
C6635 C6657 100nF (K)
C6658 C6659 1nF (K)
3.3V_HDMI_RX
FL665
MPZ2012S221AT
C6636
10uF (K)
C6637 100nF (K)
C6660 1nF (K)
HDMI_RECEIVER 3/3
[V-U9125]
IC660
37 54 72 AVCC18
38 42 46 50 56 60 64 68 AVCC33
AGND 36 41 45 49 53 59 63 67 71
6 18 32 74 88 104 113 125 137 IOVCC33
IOGND 4 17 31 73 87 103 112 124 136
12 24 25 80 91 107 119 131 143 CVCC18
CGND 11 23 79 90 106 118 130 142
92 DVCC18
DGND 93
97 REGVCC
GND 145
1 2 3 4 5 6 7 8
A B C D E F

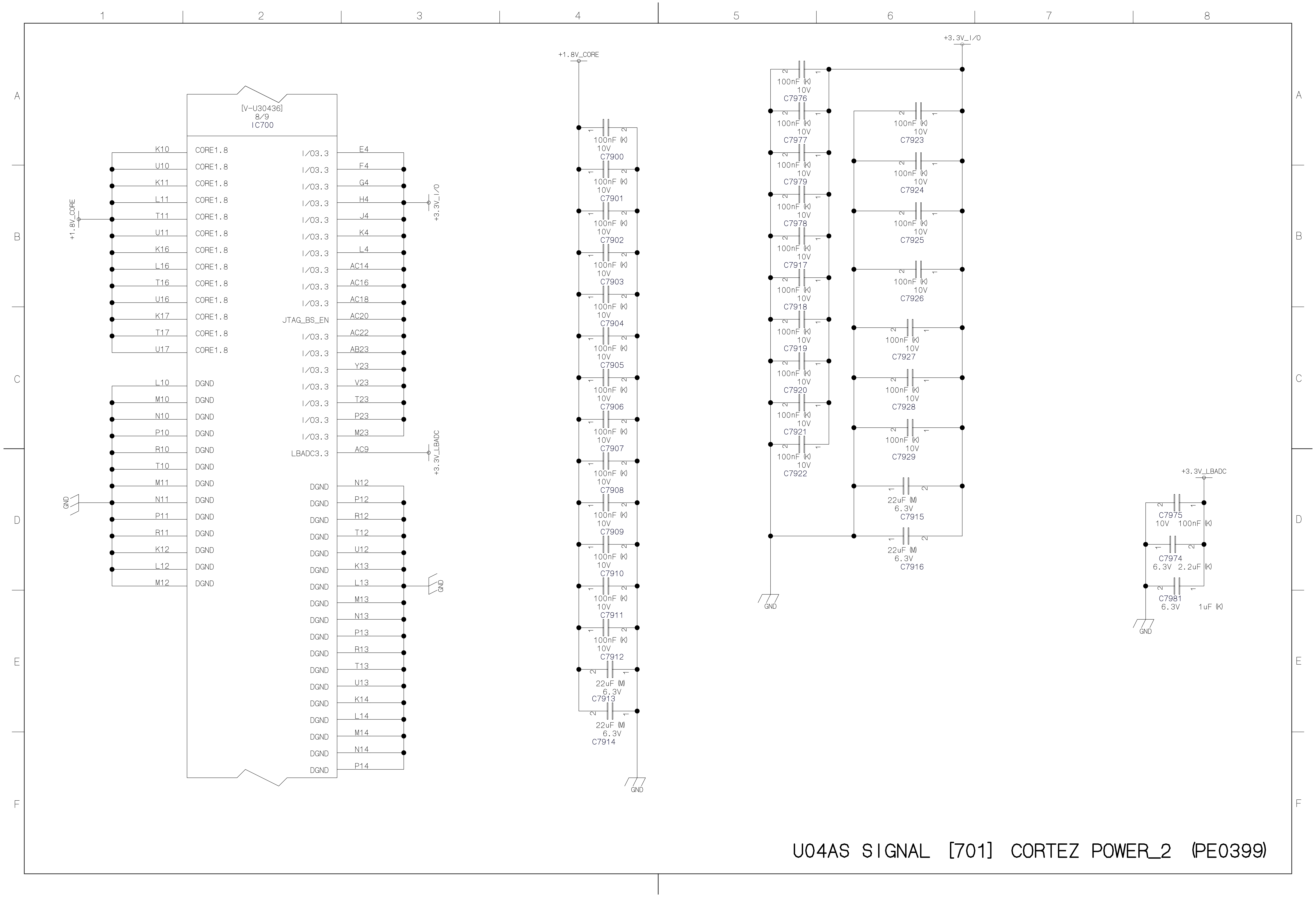
U04AS SIGNAL [662] HDMI Rx Power (PE0399)


1
2
3
4
5
6
7
8
A
B
C
D
E
F
MOUNT
D682
C
A
1SS352 (TPH3, F)
V47A00011600
MM1663DHBE
5V2
3.3V_HDMI_CONT
UREG
[V-L1661]
IC682
8
Vin
VO
1
5
CONT
Cn
4
2
NC
6
NC
GND
3
7
NC
HeatSink
9
R6800
1
2
1K (J)
C6807
100pF (J)
50V
C689
1uF (K)
10V
GND
C6800
1nF (K)
C6801
330nF (K)
FL682
MPZ2012S221AT
3.3V_HDMI_RX
C6802
10uF (K)
10V
C6810
16V
100nF (K)
GND
GND
V42GD0000249
D683
C
A
1SS352 (TPH3, F)
V47A00011600
MM1663DHBE
5V2
UREG
[V-L1661]
IC683
8
Vin
VO
1
5
CONT
Cn
4
2
NC
6
NC
GND
3
7
NC
HeatSink
9
C6803
1uF (K)
10V
V03GD0001144
GND
C6804
1nF (K)
C6805
330nF (K)
C6806
10uF (K)
10V
V03GD0001318
GND
FL680
MPZ2012S221AT
V13GD0000249
3.3V_HDMI_SW1
V03GD0000572
C6808
16V
100nF (K)
GND

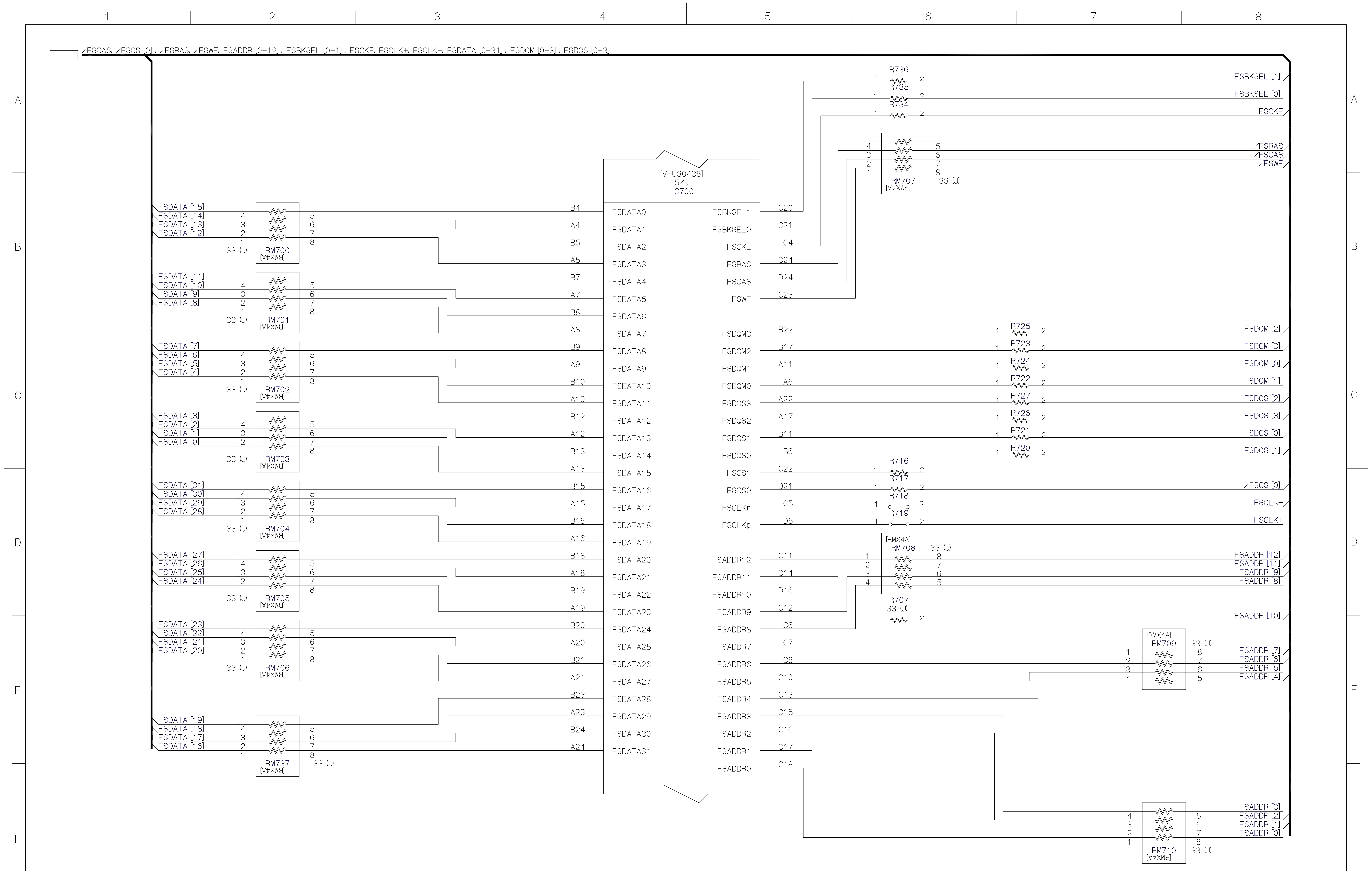
U04AS SIGNAL [680] HDMI Power Reg. (PE0399)


[V-U30436]
9/9
IC700
+2.5V_DDR
D4 D6 D7 D8 D10 D11 D12 D13 D14 D15 D17 D18 D20 D22 D23
DDR2.5
FSVREF
C19 C9
FSVREF
R14 T14 U14 K15 L15 M15 N15 P15 R15 T15 U15 M16 N16 P16 R16 L17 M17 N17 P17 R17
DGND
GND
+1.8V_DLL
A14
VDDA18DLL
B14
VSSA18DLL
old name:C_DDR_GND
H24 LVDSPLL3.3
+3.3V_LVDS_PLL
E26 H26 J26 M26 LVDS3.3
+3.3V_LVDS
E25 H25 J25 M25 LVDSGND
H23 LVDSPLLGND
AB4 AC4 AC5 ADC1.8
+1.8V_ADC
AD3 AD4 AD5 ADC1_3.3
+3.3V_ADC
+3.3VA_ADC
+3.3VB_ADC
AD6 AD7 ADC2_3.3
+3.3VC_ADC
+3.3VSC_ADC
AB3 AC3 AD2 AE3 AC6 AE6 AC8 AD8 AE9 ADCAGND
AC11 LBADCGND
AA2 AA3 AA4 DGNDADC
C25 RPLL1.8
+1.8V_PLL
D26 RPLL3.3
+3.3V_PLL
A25 A26 RPLLAGND
B25 RPLLDGND
D19 D9 FSVREFVSS
+2.5V_DDR
C7930 C7931 C7932 C7933 C7934 C7935 C7936 C7937 C7938 C7939 C7940 C7941 C7942 C7943 C7944
100nF (K) 10V
C7946 C7947
22uF (M) 6.3V
C7982 C7983 C7984 C7985 C7986 C7987
100nF (K) 10V
GND
old name:C_DDR_GND
+3.3V_LVDS_PLL
C7953 100nF (K) 10V
C7948 2.2uF (K) 6.3V
+3.3VB_ADC
C7957 100nF (K) 10V
C7950 2.2uF (K) 6.3V
+3.3VC_ADC
C7971 100nF (K) 10V
C7964 2.2uF (K) 6.3V
+3.3V_PLL
C7960 100nF (K) 10V
C7952 2.2uF (K) 6.3V
+1.8V_DLL
C7945 100nF (K) 10V
old name:C_DDR_GND
+1.8V_ADC
C7954 C7955 C7956 100nF (K) 10V
C7949 22uF (M) 6.3V
+3.3V_ADC
C7969 C7980 100nF (K) 10V
C7962 2.2uF (K) 6.3V
+3.3VA_ADC
C7970 100nF (K) 10V
C7963 2.2uF (K) 6.3V
+3.3V_LVDS
C7966 C7967 C7968 100nF (K) 10V
C7961 2.2uF (K) 6.3V
+3.3VSC_ADC
C7958 100nF (K) 10V
C7951 2.2uF (K) 6.3V
+1.8V_PLL
C7959 100nF (K) 10V
FSVREF
C7972 C7973 100nF (K) 10V
old name:C_DDR_GND
C7988 10nF (K)
0 (K) W9027 NOT MOUNT
C7989 10nF (K)
0 (K) W9028 NOT MOUNT
old name: C_DDR_GND
1 2 3 4 5 6 7 8
A B C D E F

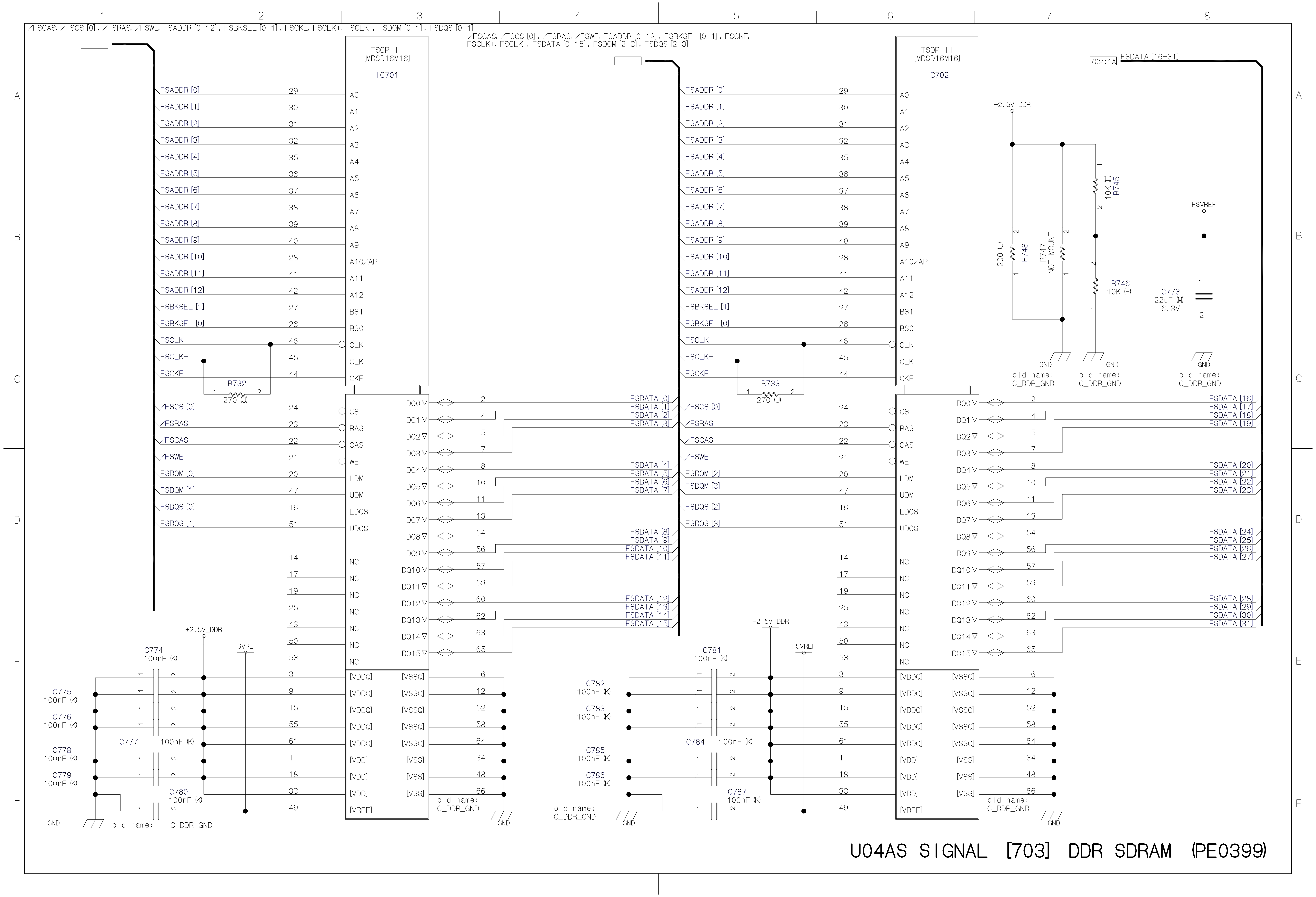
U04AS SIGNAL [700] CORTEZ POWER_1 (PE0399)


1
2
3
4
5
6
7
8
A
B
C
D
E
F
[V-U30436]
8/9
IC700
+1.8V_CORE
K10 CORE1.8
U10 CORE1.8
K11 CORE1.8
L11 CORE1.8
T11 CORE1.8
U11 CORE1.8
K16 CORE1.8
L16 CORE1.8
T16 CORE1.8
U16 CORE1.8
K17 CORE1.8
T17 CORE1.8
U17 CORE1.8
L10 DGND
M10 DGND
N10 DGND
P10 DGND
R10 DGND
T10 DGND
M11 DGND
N11 DGND
P11 DGND
R11 DGND
K12 DGND
L12 DGND
M12 DGND
GND
I/O3.3 E4
I/O3.3 F4
I/O3.3 G4
I/O3.3 H4
I/O3.3 J4
I/O3.3 K4
I/O3.3 L4
I/O3.3 AC14
I/O3.3 AC16
I/O3.3 AC18
JTAG_BS_EN AC20
I/O3.3 AC22
I/O3.3 AB23
I/O3.3 Y23
I/O3.3 V23
I/O3.3 T23
I/O3.3 P23
I/O3.3 M23
LBADC3.3 AC9
+3.3V_I/O
+3.3V_LBADC
DGND N12
DGND P12
DGND R12
DGND T12
DGND U12
DGND K13
DGND L13
DGND M13
DGND N13
DGND P13
DGND R13
DGND T13
DGND U13
DGND K14
DGND L14
DGND M14
DGND N14
DGND P14
GND
+1.8V_CORE
100nF (K) 10V C7900
100nF (K) 10V C7901
100nF (K) 10V C7902
100nF (K) 10V C7903
100nF (K) 10V C7904
100nF (K) 10V C7905
100nF (K) 10V C7906
100nF (K) 10V C7907
100nF (K) 10V C7908
100nF (K) 10V C7909
100nF (K) 10V C7910
100nF (K) 10V C7911
100nF (K) 10V C7912
22uF (M) 6.3V C7913
22uF (M) 6.3V C7914
GND
+3.3V_I/O
100nF (K) 10V C7976
100nF (K) 10V C7977
100nF (K) 10V C7979
100nF (K) 10V C7978
100nF (K) 10V C7917
100nF (K) 10V C7918
100nF (K) 10V C7919
100nF (K) 10V C7920
100nF (K) 10V C7921
100nF (K) 10V C7922
100nF (K) 10V C7923
100nF (K) 10V C7924
100nF (K) 10V C7925
100nF (K) 10V C7926
100nF (K) 10V C7927
100nF (K) 10V C7928
100nF (K) 10V C7929
22uF (M) 6.3V C7915
22uF (M) 6.3V C7916
GND
+3.3V_LBADC
C7975 10V 100nF (K)
C7974 6.3V 2.2uF (K)
C7981 6.3V 1uF (K)
GND
1
2

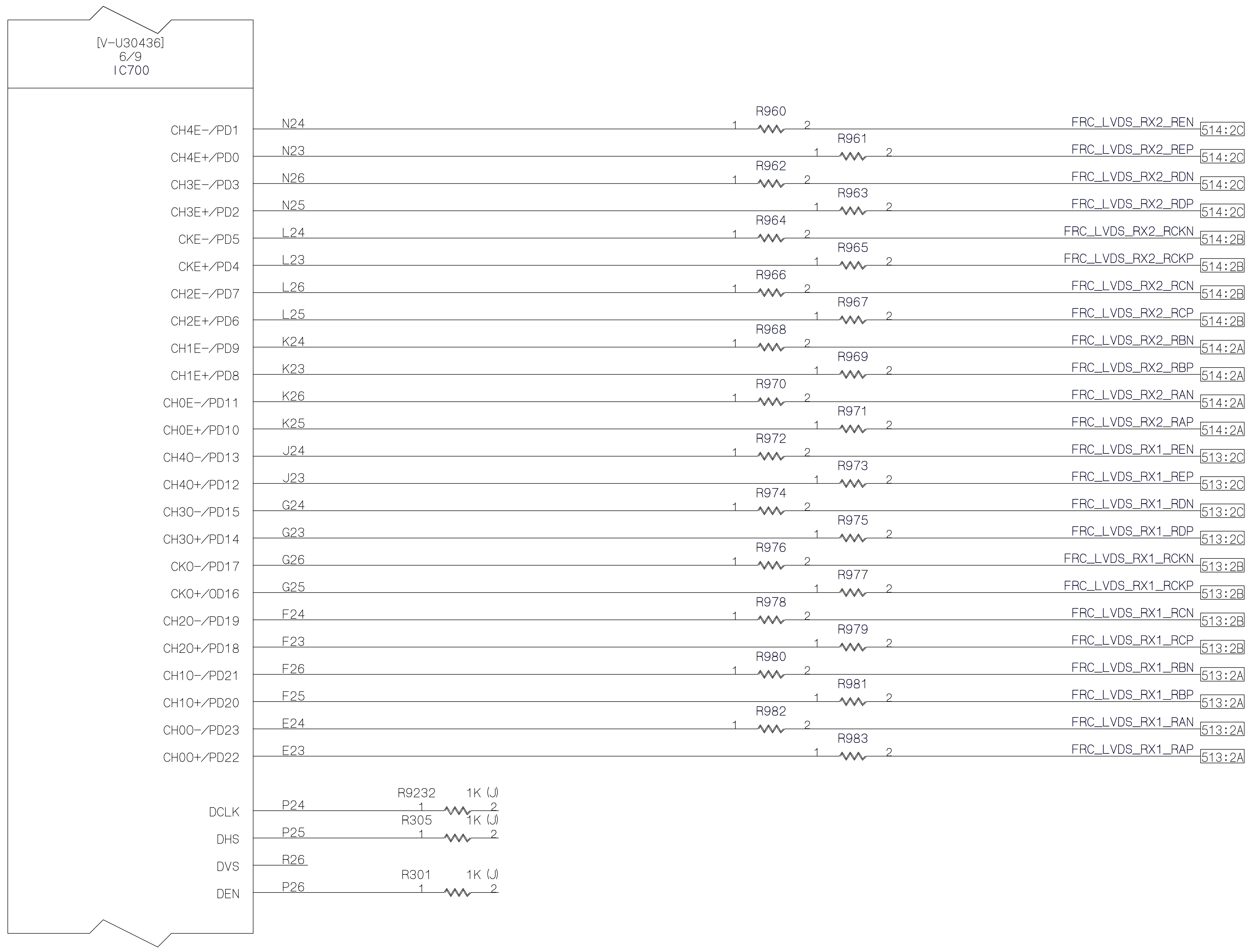
U04AS SIGNAL [701] CORTEZ POWER_2 (PE0399)


1
2
3
4
5
6
7
8
A
B
C
D
E
F
/FSCAS, /FSCS [0], /FSRAS, /FSWE, FSADDR [0-12], FSBKSEL [0-1], FSCKE, FSCLK+, FSCLK-, FSDATA [0-31], FSDQM [0-3], FSDQS [0-3]
[V-U30436]
5/9
IC700
FSDATA0
FSDATA1
FSDATA2
FSDATA3
FSDATA4
FSDATA5
FSDATA6
FSDATA7
FSDATA8
FSDATA9
FSDATA10
FSDATA11
FSDATA12
FSDATA13
FSDATA14
FSDATA15
FSDATA16
FSDATA17
FSDATA18
FSDATA19
FSDATA20
FSDATA21
FSDATA22
FSDATA23
FSDATA24
FSDATA25
FSDATA26
FSDATA27
FSDATA28
FSDATA29
FSDATA30
FSDATA31
FSBKSEL1
FSBKSEL0
FSCKE
FSRAS
FSCAS
FSWE
FSDQM3
FSDQM2
FSDQM1
FSDQM0
FSDQS3
FSDQS2
FSDQS1
FSDQS0
FSCS1
FSCS0
FSCLKn
FSCLKp
FSADDR12
FSADDR11
FSADDR10
FSADDR9
FSADDR8
FSADDR7
FSADDR6
FSADDR5
FSADDR4
FSADDR3
FSADDR2
FSADDR1
FSADDR0
B4
A4
B5
A5
B7
A7
B8
A8
B9
A9
B10
A10
B12
A12
B13
A13
B15
A15
B16
A16
B18
A18
B19
A19
B20
A20
B21
A21
B23
A23
B24
A24
C20
C21
C4
C24
D24
C23
B22
B17
A11
A6
A22
A17
B11
B6
C22
D21
C5
D5
C11
C14
D16
C12
C6
C7
C8
C10
C13
C15
C16
C17
C18
FSDATA [15]
FSDATA [14]
FSDATA [13]
FSDATA [12]
RM700
FSDATA [11]
FSDATA [10]
FSDATA [9]
FSDATA [8]
RM701
FSDATA [7]
FSDATA [6]
FSDATA [5]
FSDATA [4]
RM702
FSDATA [3]
FSDATA [2]
FSDATA [1]
FSDATA [0]
RM703
FSDATA [31]
FSDATA [30]
FSDATA [29]
FSDATA [28]
RM704
FSDATA [27]
FSDATA [26]
FSDATA [25]
FSDATA [24]
RM705
FSDATA [23]
FSDATA [22]
FSDATA [21]
FSDATA [20]
RM706
FSDATA [19]
FSDATA [18]
FSDATA [17]
FSDATA [16]
RM737
[RMX4A]
33 (J)
R736
R735
R734
FSBKSEL [1]
FSBKSEL [0]
FSCKE
RM707
/FSRAS
/FSCAS
/FSWE
R725
R723
R724
R722
R727
R726
R721
R720
FSDQM [2]
FSDQM [3]
FSDQM [0]
FSDQM [1]
FSDQS [2]
FSDQS [3]
FSDQS [0]
FSDQS [1]
R716
R717
R718
R719
/FSCS [0]
FSCLK-
FSCLK+
RM708
FSADDR [12]
FSADDR [11]
FSADDR [9]
FSADDR [8]
R707
FSADDR [10]
RM709
FSADDR [7]
FSADDR [6]
FSADDR [5]
FSADDR [4]
RM710
FSADDR [3]
FSADDR [2]
FSADDR [1]
FSADDR [0]

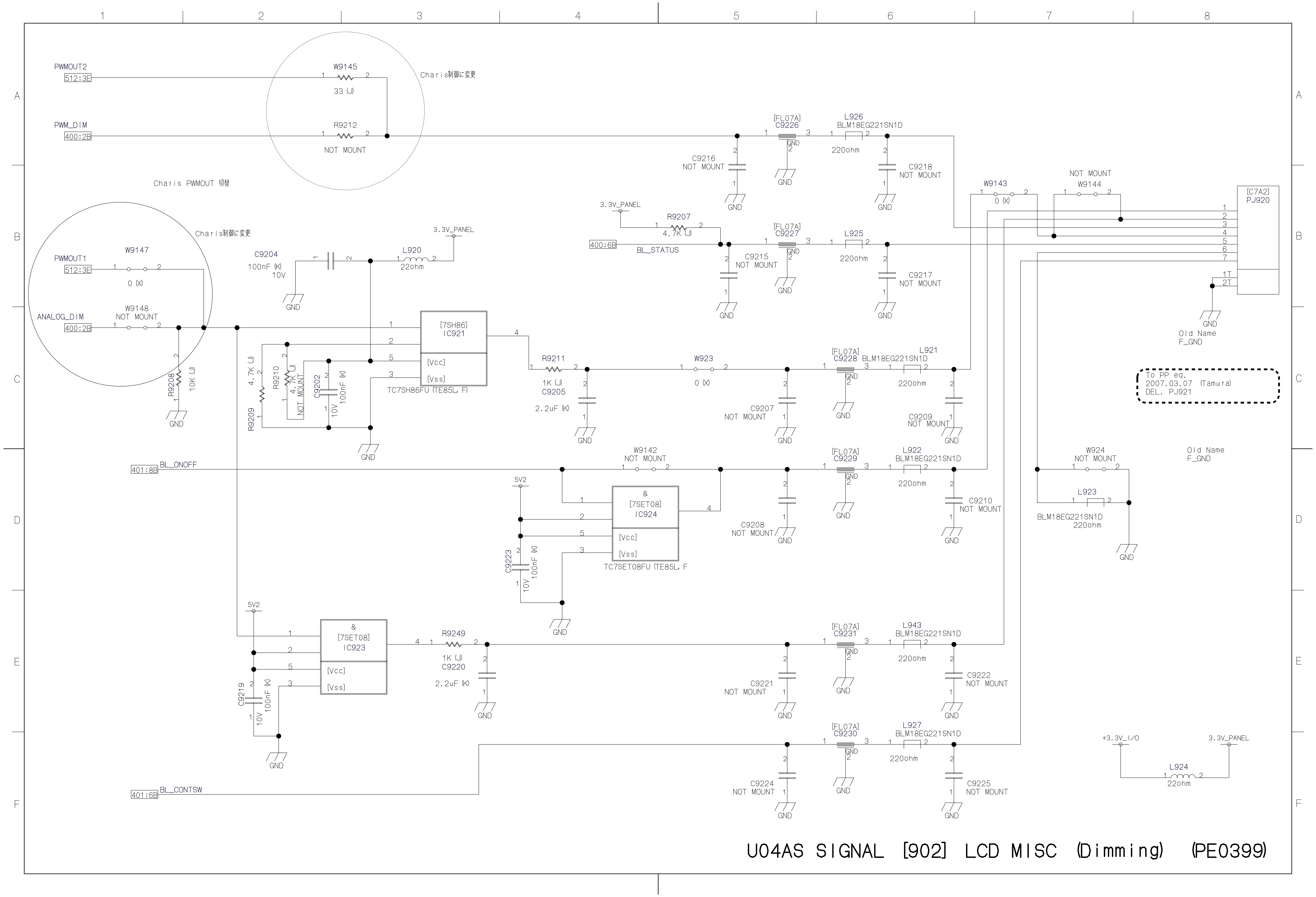
U04AS SIGNAL [702] CORTEZ DDR I/F (PE0399)


/FSCAS, /FSCS [0], /FSRAS, /FSWE, FSADDR [0-12], FSBKSEL [0-1], FSCKE, FSCLK+, FSCLK-, FSDQM [0-1], FSDQS [0-1]
/FSCAS, /FSCS [0], /FSRAS, /FSWE, FSADDR [0-12], FSBKSEL [0-1], FSCKE, FSCLK+, FSCLK-, FSDATA [0-15], FSDQM [2-3], FSDQS [2-3]
TSOP II [MDSD16M16] IC701
TSOP II [MDSD16M16] IC702
702:1A FSDATA [16-31]
FSADDR [0] … FSADDR [12]
FSBKSEL [1]
FSBKSEL [0]
FSCLK-
FSCLK+
FSCKE
/FSCS [0]
/FSRAS
/FSCAS
/FSWE
FSDQM [0]
FSDQM [1]
FSDQS [0]
FSDQS [1]
FSDQM [2]
FSDQM [3]
FSDQS [2]
FSDQS [3]
A0 A1 A2 A3 A4 A5 A6 A7 A8 A9 A10/AP A11 A12 BS1 BS0 CLK CLK CKE
CS RAS CAS WE LDM UDM LDQS UDQS NC
DQ0 … DQ15
[VDDQ] [VSSQ] [VDD] [VSS] [VREF]
FSDATA [0] … FSDATA [15]
FSDATA [16] … FSDATA [31]
R732 270 (J)
R733 270 (J)
+2.5V_DDR
FSVREF
R745 10K (F)
R746 10K (F)
R747 NOT MOUNT
R748 200 (J)
C773 22uF (M) 6.3V
C774 100nF (K)
C775 100nF (K)
C776 100nF (K)
C777 100nF (K)
C778 100nF (K)
C779 100nF (K)
C780 100nF (K)
C781 100nF (K)
C782 100nF (K)
C783 100nF (K)
C784 100nF (K)
C785 100nF (K)
C786 100nF (K)
C787 100nF (K)
GND
old name: C_DDR_GND
1 2 3 4 5 6 7 8
A B C D E F

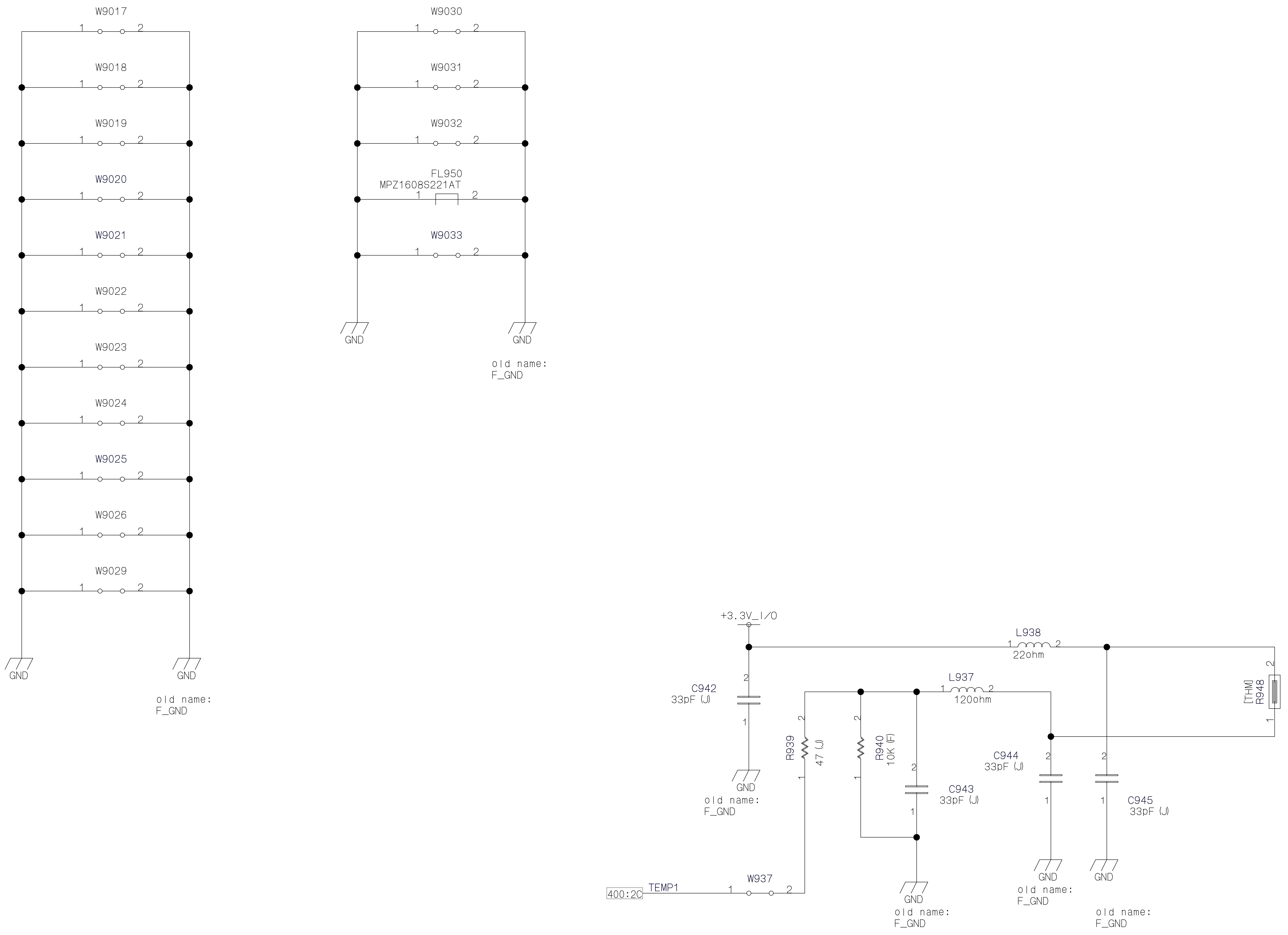
U04AS SIGNAL [703] DDR SDRAM (PE0399)


[V-U30436]
6/9
IC700
CH4E-/PD1 N24 R960 FRC_LVDS_RX2_REN 514:2C
CH4E+/PD0 N23 R961 FRC_LVDS_RX2_REP 514:2C
CH3E-/PD3 N26 R962 FRC_LVDS_RX2_RDN 514:2C
CH3E+/PD2 N25 R963 FRC_LVDS_RX2_RDP 514:2C
CKE-/PD5 L24 R964 FRC_LVDS_RX2_RCKN 514:2B
CKE+/PD4 L23 R965 FRC_LVDS_RX2_RCKP 514:2B
CH2E-/PD7 L26 R966 FRC_LVDS_RX2_RCN 514:2B
CH2E+/PD6 L25 R967 FRC_LVDS_RX2_RCP 514:2B
CH1E-/PD9 K24 R968 FRC_LVDS_RX2_RBN 514:2A
CH1E+/PD8 K23 R969 FRC_LVDS_RX2_RBP 514:2A
CH0E-/PD11 K26 R970 FRC_LVDS_RX2_RAN 514:2A
CH0E+/PD10 K25 R971 FRC_LVDS_RX2_RAP 514:2A
CH4O-/PD13 J24 R972 FRC_LVDS_RX1_REN 513:2C
CH4O+/PD12 J23 R973 FRC_LVDS_RX1_REP 513:2C
CH3O-/PD15 G24 R974 FRC_LVDS_RX1_RDN 513:2C
CH3O+/PD14 G23 R975 FRC_LVDS_RX1_RDP 513:2C
CKO-/PD17 G26 R976 FRC_LVDS_RX1_RCKN 513:2B
CKO+/OD16 G25 R977 FRC_LVDS_RX1_RCKP 513:2B
CH2O-/PD19 F24 R978 FRC_LVDS_RX1_RCN 513:2B
CH2O+/PD18 F23 R979 FRC_LVDS_RX1_RCP 513:2B
CH1O-/PD21 F26 R980 FRC_LVDS_RX1_RBN 513:2A
CH1O+/PD20 F25 R981 FRC_LVDS_RX1_RBP 513:2A
CH0O-/PD23 E24 R982 FRC_LVDS_RX1_RAN 513:2A
CH0O+/PD22 E23 R983 FRC_LVDS_RX1_RAP 513:2A
DCLK P24 R9232 1K (J)
DHS P25 R305 1K (J)
DVS R26
DEN P26 R301 1K (J)
1
2

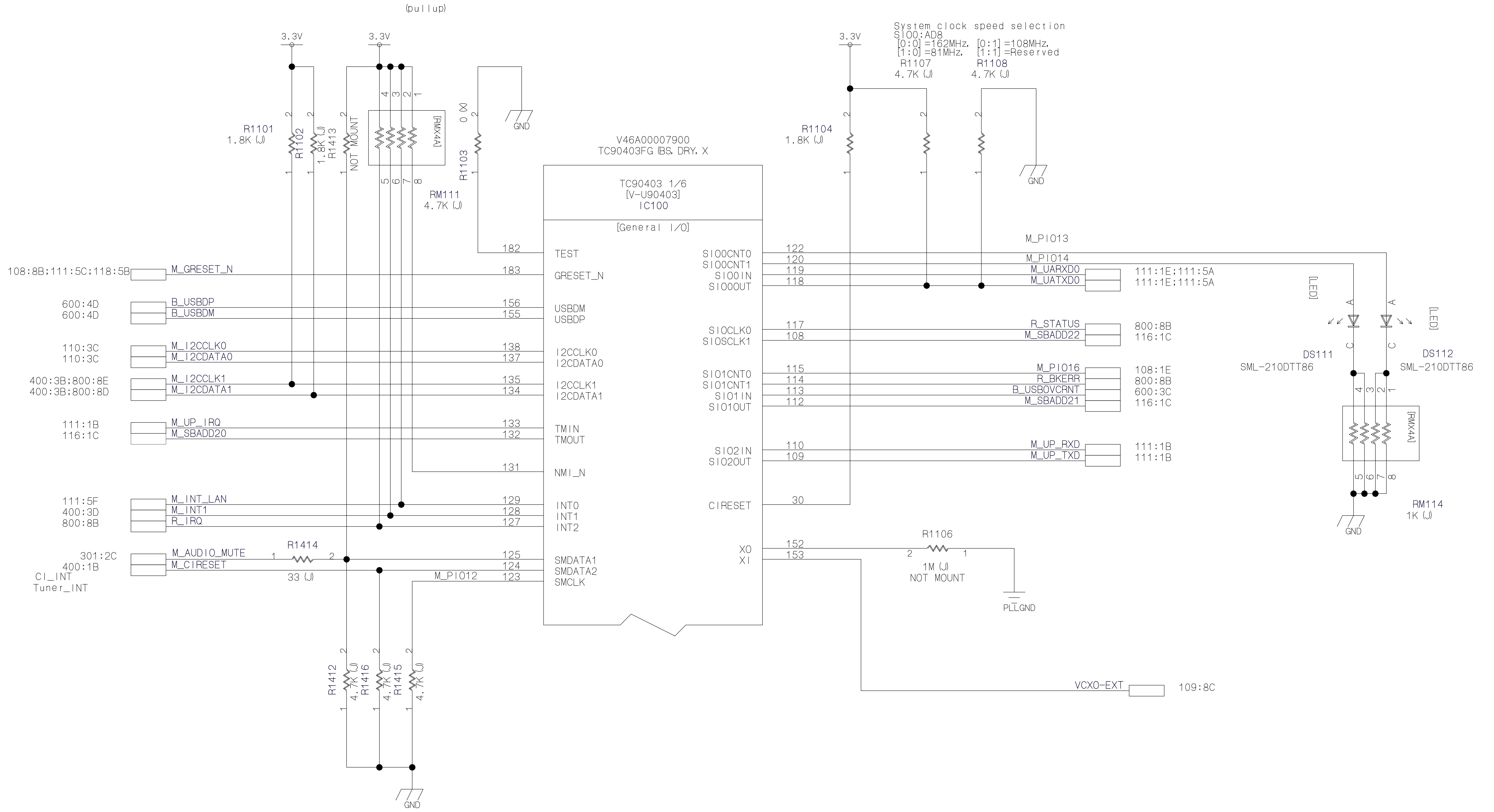
U04AS SIGNAL [900] CORTEZ LVDS_OUT (PE0399)


1
2
3
4
5
6
7
8
A
B
C
D
E
F
PWMOUT2
512:3E
W9145
33 (J)
Charis制御に変更
PWM_DIM
400:2B
R9212
NOT MOUNT
Charis PWMOUT 切替
Charis制御に変更
PWMOUT1
512:3E
W9147
0 (X)
ANALOG_DIM
400:2B
W9148
NOT MOUNT
R9208
10K (J)
GND
C9204
100nF (K)
10V
L920
22ohm
3.3V_PANEL
[7SH86]
IC921
[Vcc]
[Vss]
TC7SH86FU (TE85L, F)
R9209
4.7K (J)
R9210
4.7K (J)
NOT MOUNT
C9202
100nF (K)
10V
R9211
1K (J)
C9205
2.2uF (K)
W923
0 (X)
3.3V_PANEL
R9207
4.7K (J)
400:6B
BL_STATUS
C9215
NOT MOUNT
C9216
NOT MOUNT
[FL07A]
C9226
[FL07A]
C9227
L926
BLM18EG221SN1D
220ohm
L925
220ohm
C9218
NOT MOUNT
C9217
NOT MOUNT
W9143
0 (X)
NOT MOUNT
W9144
[C7A2]
PJ920
1T
2T
Old Name
F_GND
To PP eq.
2007.03.07 (Tamura)
DEL. PJ921
C9207
NOT MOUNT
[FL07A]
C9228
L921
BLM18EG221SN1D
220ohm
C9209
NOT MOUNT
401:8B
BL_ONOFF
W9142
NOT MOUNT
5V2
&
[7SET08]
IC924
TC7SET08FU (TE85L, F
C9223
100nF (K)
10V
C9208
NOT MOUNT
[FL07A]
C9229
L922
BLM18EG221SN1D
220ohm
C9210
NOT MOUNT
W924
NOT MOUNT
L923
BLM18EG221SN1D
220ohm
Old Name
F_GND
5V2
&
[7SET08]
IC923
C9219
100nF (K)
10V
R9249
1K (J)
C9220
2.2uF (K)
C9221
NOT MOUNT
[FL07A]
C9231
L943
BLM18EG221SN1D
220ohm
C9222
NOT MOUNT
401:6B
BL_CONTSW
C9224
NOT MOUNT
[FL07A]
C9230
L927
BLM18EG221SN1D
220ohm
C9225
NOT MOUNT
+3.3V_I/O
3.3V_PANEL
L924
22ohm

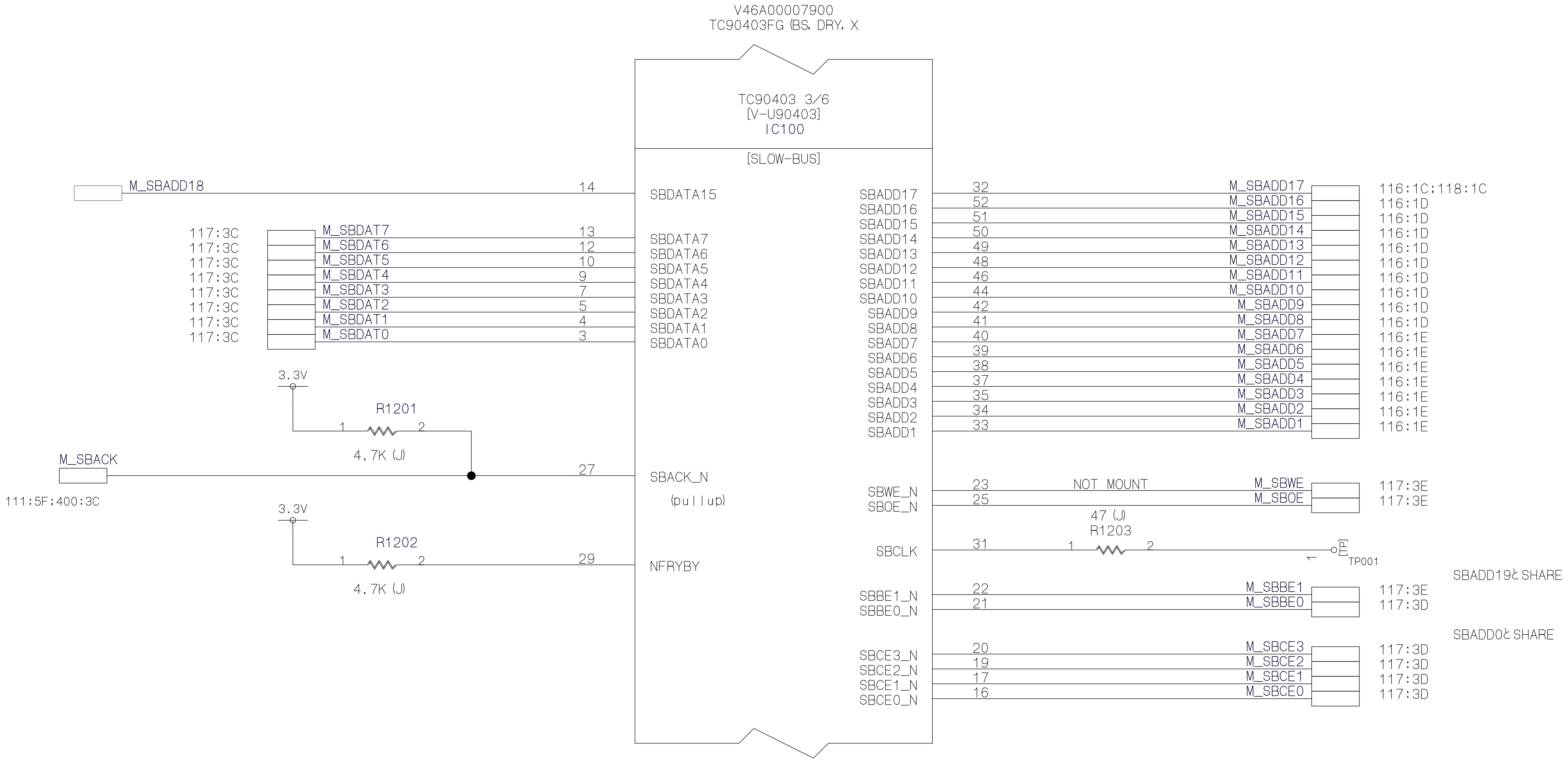
U04AS SIGNAL [902] LCD MISC (Dimming) (PE0399)


W9017
W9018
W9019
W9020
W9021
W9022
W9023
W9024
W9025
W9026
W9029
GND
GND
old name:
F_GND
W9030
W9031
W9032
FL950
MPZ1608S221AT
W9033
GND
GND
old name:
F_GND
+3.3V_I/O
L938
22ohm
C942
33pF (J)
L937
120ohm
[THM]
R948
R939
47 (J)
R940
10K (F)
C943
33pF (J)
C944
33pF (J)
C945
33pF (J)
GND
old name:
F_GND
400:2C
TEMP1
W937
GND
old name:
F_GND
GND
old name:
F_GND
GND
old name:
F_GND

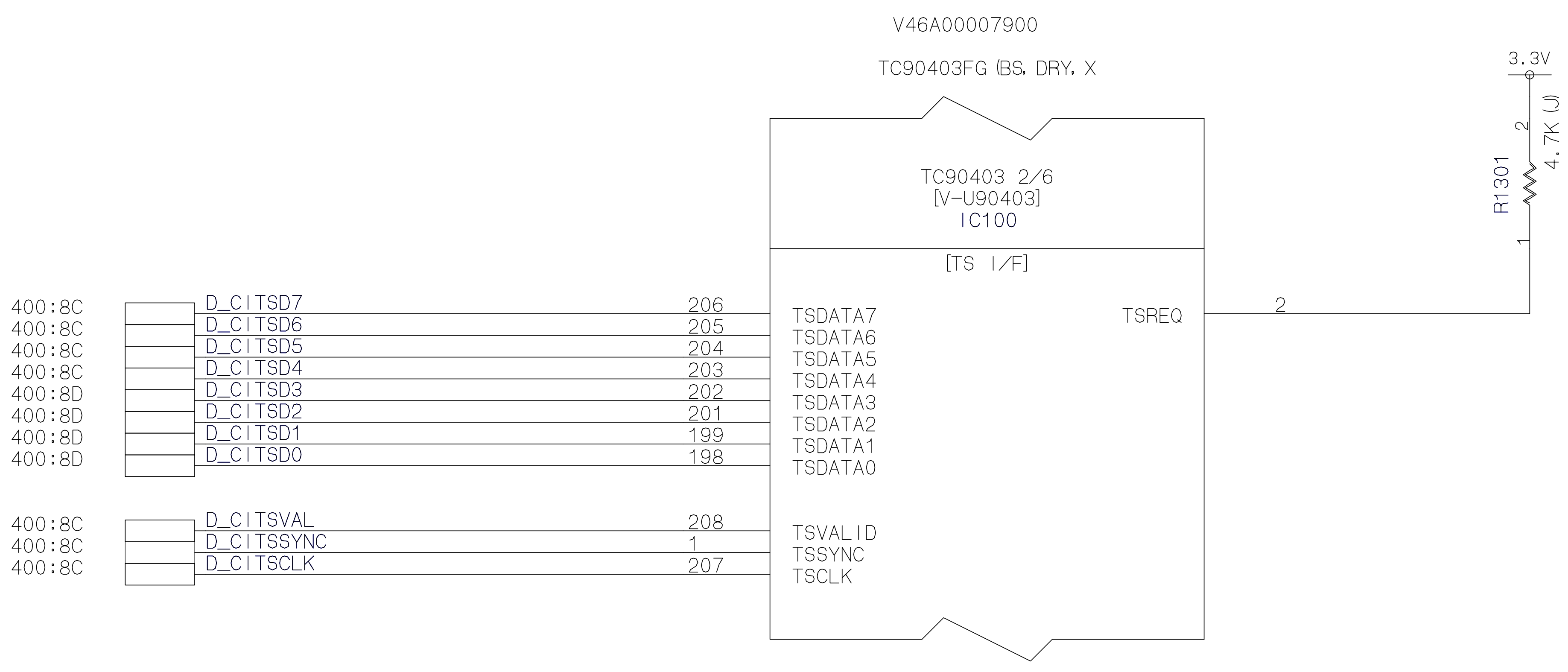
U04AS SIGNAL [903] Temperature sensor (SHARP) (PE0399)


(pullup)
3.3V
3.3V
3.3V
R1101
1.8K (J)
R1102
1.8K (J)
R1413
NOT MOUNT
[RMX4A]
RM111
4.7K (J)
0 (X)
R1103
GND
V46A00007900
TC90403FG (BS, DRY, X
TC90403 1/6
[V-U90403]
IC100
[General I/O]
182 TEST
183 GRESET_N
156 USBDM
155 USBDP
138 I2CCLK0
137 I2CDATA0
135 I2CCLK1
134 I2CDATA1
133 TMIN
132 TMOUT
131 NMI_N
129 INT0
128 INT1
127 INT2
125 SMDATA1
124 SMDATA2
123 SMCLK
SIO0CNT0 122
SIO0CNT1 120
SIO0IN 119
SIO0OUT 118
SIOCLK0 117
SIOSCLK1 108
SIO1CNT0 115
SIO1CNT1 114
SIO1IN 113
SIO1OUT 112
SIO2IN 110
SIO2OUT 109
CIRESET 30
X0 152
XI 153
108:8B;111:5C;118:5B M_GRESET_N
600:4D B_USBDP
600:4D B_USBDM
110:3C M_I2CCLK0
110:3C M_I2CDATA0
400:3B;800:8E M_I2CCLK1
400:3B;800:8D M_I2CDATA1
111:1B M_UP_IRQ
116:1C M_SBADD20
111:5F M_INT_LAN
400:3D M_INT1
800:8B R_IRQ
301:2C M_AUDIO_MUTE
400:1B M_CIRESET
CI_INT
Tuner_INT
R1414
33 (J)
M_PIO12
R1412
4.7K (J)
R1416
4.7K (J)
R1415
4.7K (J)
GND
System clock speed selection
SIO0:AD8
[0:0] =162MHz, [0:1] =108MHz,
[1:0] =81MHz, [1:1] =Reserved
R1107
4.7K (J)
R1108
4.7K (J)
R1104
1.8K (J)
GND
M_PIO13
M_PIO14
M_UARXD0 111:1E;111:5A
M_UATXD0 111:1E;111:5A
R_STATUS 800:8B
M_SBADD22 116:1C
M_PIO16 108:1E
R_BKERR 800:8B
B_USBOVCRNT 600:3C
M_SBADD21 116:1C
M_UP_RXD 111:1B
M_UP_TXD 111:1B
[LED]
[LED]
A
C
DS111
SML-210DTT86
DS112
SML-210DTT86
[RMX4A]
RM114
1K (J)
GND
R1106
1M (J)
NOT MOUNT
PLLGND
VCXO-EXT 109:8C

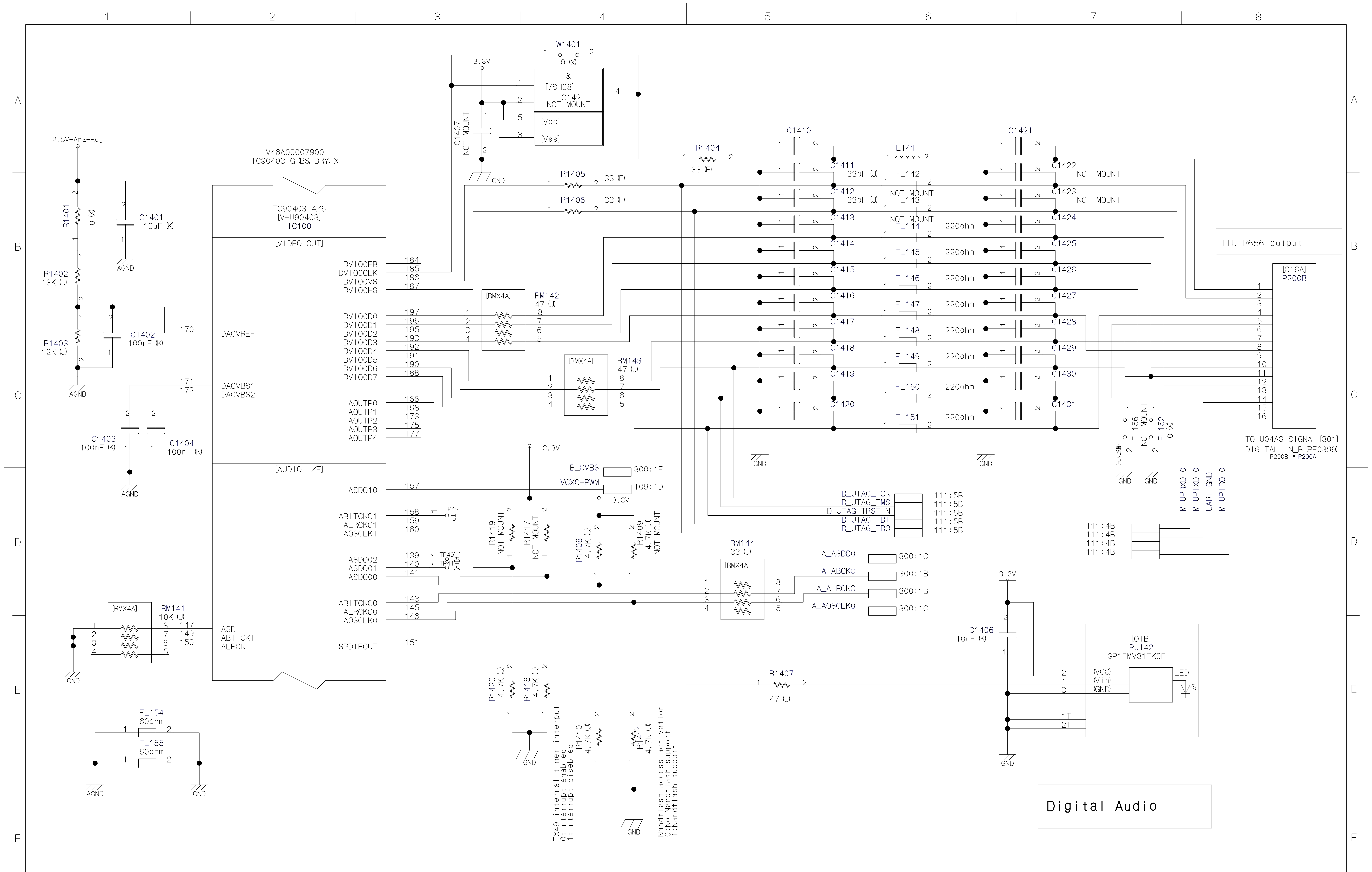
U05A DIGITAL [101] Donau SIO/I2C/RST etc (PE0284)


V46A00007900
TC90403FG (BS, DRY, X
TC90403 3/6
[V-U90403]
IC100
[SLOW-BUS]
M_SBADD18
14
SBDATA15
117:3C
M_SBDAT7
13
SBDATA7
117:3C
M_SBDAT6
12
SBDATA6
117:3C
M_SBDAT5
10
SBDATA5
117:3C
M_SBDAT4
9
SBDATA4
117:3C
M_SBDAT3
7
SBDATA3
117:3C
M_SBDAT2
5
SBDATA2
117:3C
M_SBDAT1
4
SBDATA1
117:3C
M_SBDAT0
3
SBDATA0
3.3V
R1201
1
2
4.7K (J)
M_SBACK
111:5F;400:3C
27
SBACK_N
(pullup)
3.3V
R1202
1
2
4.7K (J)
29
NFRYBY
SBADD17
32
M_SBADD17
116:1C;118:1C
SBADD16
52
M_SBADD16
116:1D
SBADD15
51
M_SBADD15
116:1D
SBADD14
50
M_SBADD14
116:1D
SBADD13
49
M_SBADD13
116:1D
SBADD12
48
M_SBADD12
116:1D
SBADD11
46
M_SBADD11
116:1D
SBADD10
44
M_SBADD10
116:1D
SBADD9
42
M_SBADD9
116:1D
SBADD8
41
M_SBADD8
116:1D
SBADD7
40
M_SBADD7
116:1E
SBADD6
39
M_SBADD6
116:1E
SBADD5
38
M_SBADD5
116:1E
SBADD4
37
M_SBADD4
116:1E
SBADD3
35
M_SBADD3
116:1E
SBADD2
34
M_SBADD2
116:1E
SBADD1
33
M_SBADD1
116:1E
SBWE_N
23
NOT MOUNT
M_SBWE
117:3E
SBOE_N
25
M_SBOE
117:3E
47 (J)
R1203
SBCLK
31
1
2
[TP]
1
TP001
SBADD19と SHARE
SBBE1_N
22
M_SBBE1
117:3E
SBBE0_N
21
M_SBBE0
117:3D
SBADD0と SHARE
SBCE3_N
20
M_SBCE3
117:3D
SBCE2_N
19
M_SBCE2
117:3D
SBCE1_N
17
M_SBCE1
117:3D
SBCE0_N
16
M_SBCE0
117:3D

U05A DIGITAL [102] Donau SB (PE0284)

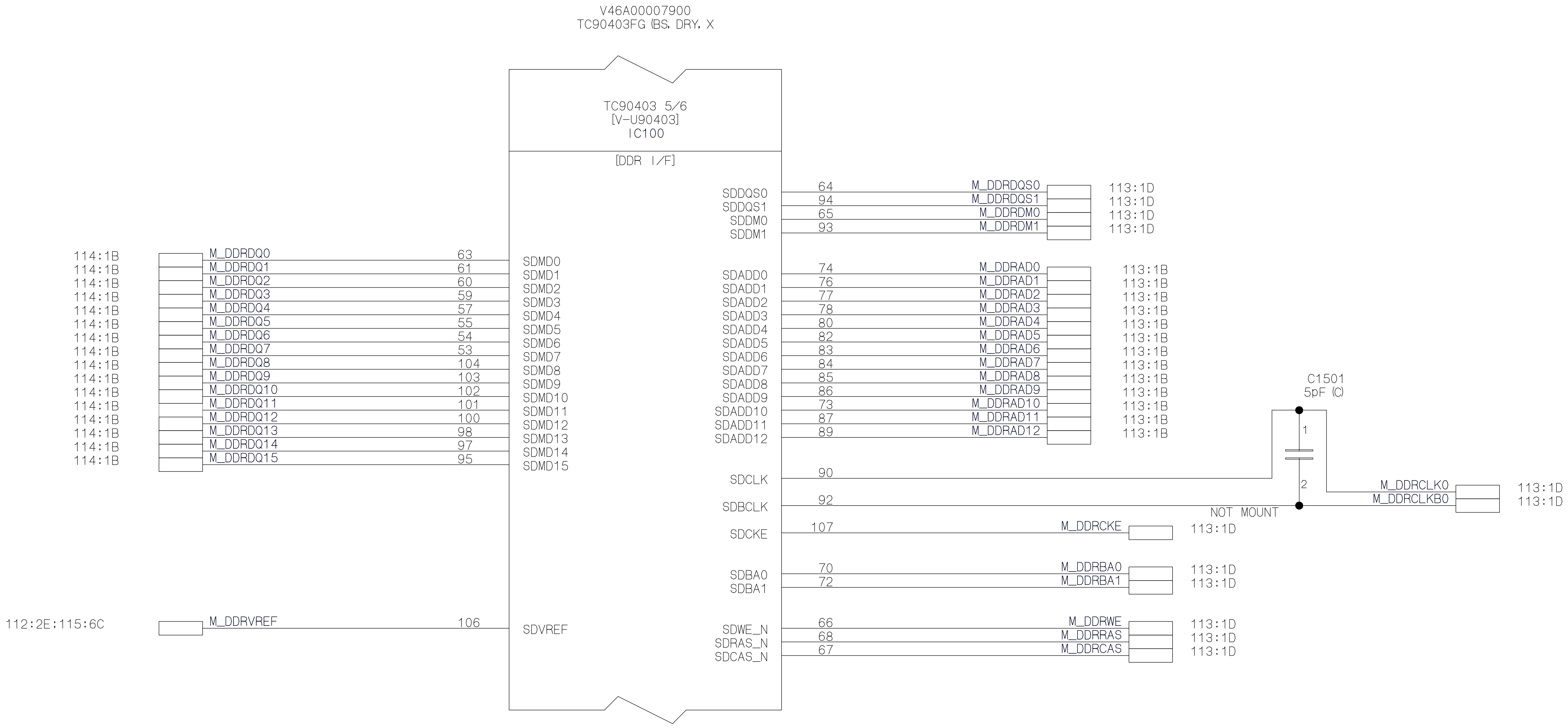

V46A00007900
TC90403FG (BS, DRY, X
TC90403 2/6
[V-U90403]
IC100
[TS I/F]
400:8C
400:8C
400:8C
400:8C
400:8D
400:8D
400:8D
400:8D
D_CITSD7
D_CITSD6
D_CITSD5
D_CITSD4
D_CITSD3
D_CITSD2
D_CITSD1
D_CITSD0
206
205
204
203
202
201
199
198
TSDATA7
TSDATA6
TSDATA5
TSDATA4
TSDATA3
TSDATA2
TSDATA1
TSDATA0
400:8C
400:8C
400:8C
D_CITSVAL
D_CITSSYNC
D_CITSCLK
208
1
207
TSVALID
TSSYNC
TSCLK
TSREQ
2
3.3V
2
1
R1301
4.7K (J)

U05A DIGITAL [103] TS (PE0284)

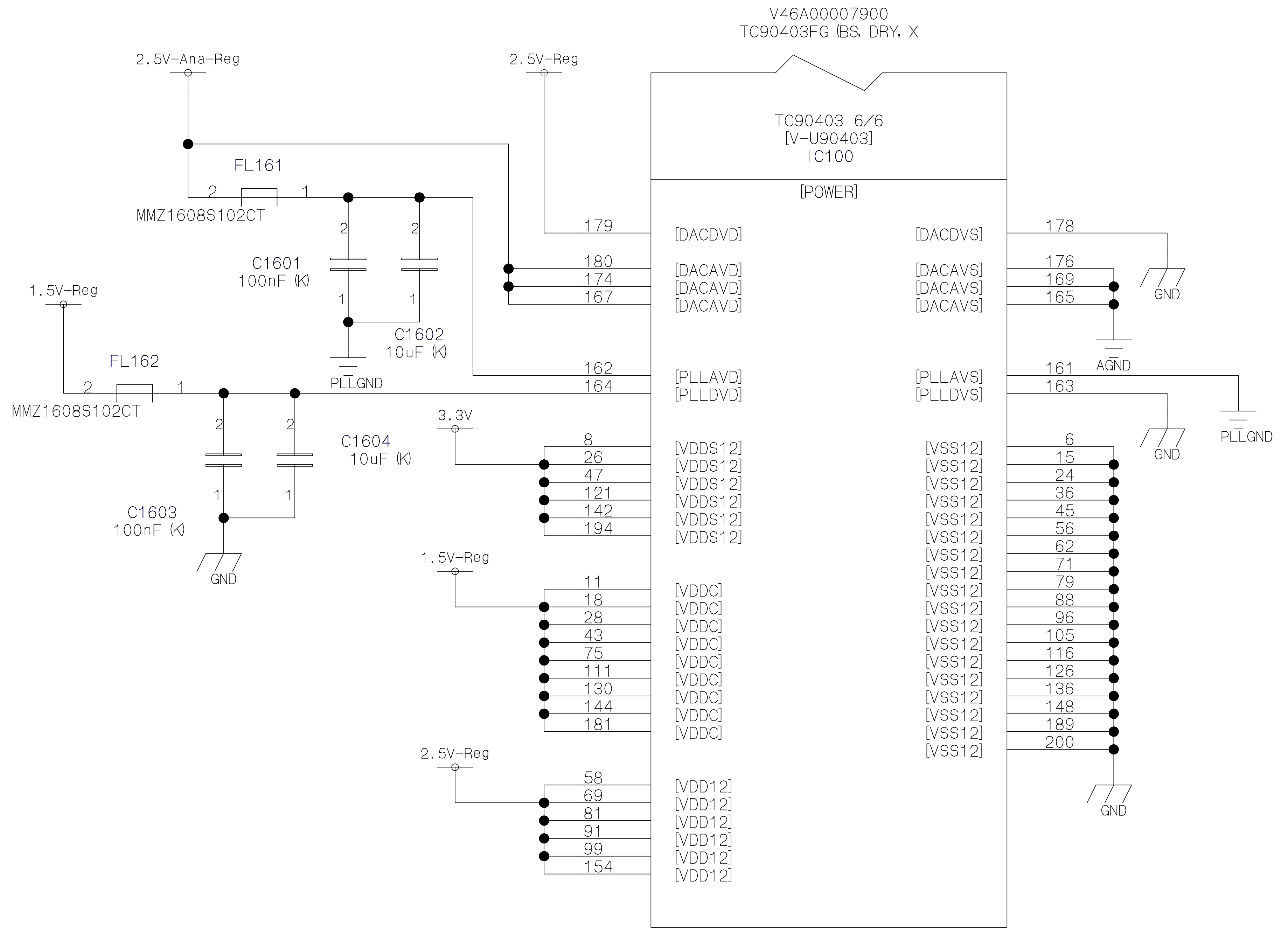


W1401
0 (X)
3.3V
IC142
NOT MOUNT
[7SH08]
[Vcc]
[Vss]
C1407
NOT MOUNT
GND
2.5V-Ana-Reg
R1401
0 (X)
C1401
10uF (K)
AGND
R1402
13K (J)
R1403
12K (J)
C1402
100nF (K)
C1403
100nF (K)
C1404
100nF (K)
V46A00007900
TC90403FG (BS, DRY, X
TC90403 4/6
[V-U90403]
IC100
[VIDEO OUT]
DVIOOFB
DVIOOCLK
DVIOOVS
DVIOOHS
DVIOOD0
DVIOOD1
DVIOOD2
DVIOOD3
DVIOOD4
DVIOOD5
DVIOOD6
DVIOOD7
AOUTP0
AOUTP1
AOUTP2
AOUTP3
AOUTP4
DACVREF
DACVBS1
DACVBS2
[AUDIO I/F]
ASDO10
ABITCKO1
ALRCKO1
AOSCLK1
ASDO02
ASDO01
ASDO00
ABITCKO0
ALRCKO0
AOSCLKO
ASDI
ABITCKI
ALRCKI
SPDIFOUT
R1404
33 (F)
R1405
33 (F)
R1406
33 (F)
RM142
47 (J)
RM143
47 (J)
[RMX4A]
C1410
C1411
33pF (J)
C1412
33pF (J)
C1413 … C1420
C1421
C1422
NOT MOUNT
C1423
NOT MOUNT
C1424 … C1431
FL141
FL142
NOT MOUNT
FL143
NOT MOUNT
FL144 … FL151
220ohm
ITU-R656 output
[C16A]
P200B
TO U04AS SIGNAL [301]
DIGITAL IN_B (PE0399)
P200B → P200A
FL156
NOT MOUNT
FL152
0 (X)
M_UPRXD_0
M_UPTXD_0
UART_GND
M_UPIRQ_0
111:4B
B_CVBS
300:1E
VCXO-PWM
109:1D
D_JTAG_TCK
D_JTAG_TMS
D_JTAG_TRST_N
D_JTAG_TDI
D_JTAG_TDO
111:5B
TP42
TP40
TP41
R1419
NOT MOUNT
R1417
NOT MOUNT
R1408
4.7K (J)
R1409
4.7K (J)
NOT MOUNT
RM144
33 (J)
A_ASDO0
A_ABCKO
A_ALRCKO
A_AOSCLKO
300:1C
300:1B
RM141
10K (J)
R1420
4.7K (J)
R1418
4.7K (J)
R1410
4.7K (J)
R1411
4.7K (J)
TX49 internal timer interrupt
0:interrupt enabled
1:interrupt disabled
Nandflash access activation
0:No Nandflash support
1:Nandflash support
FL154
60ohm
FL155
60ohm
C1406
10uF (K)
R1407
47 (J)
[OTB]
PJ142
GP1FMV31TK0F
(VCC)
(Vin)
(GND)
LED
Digital Audio

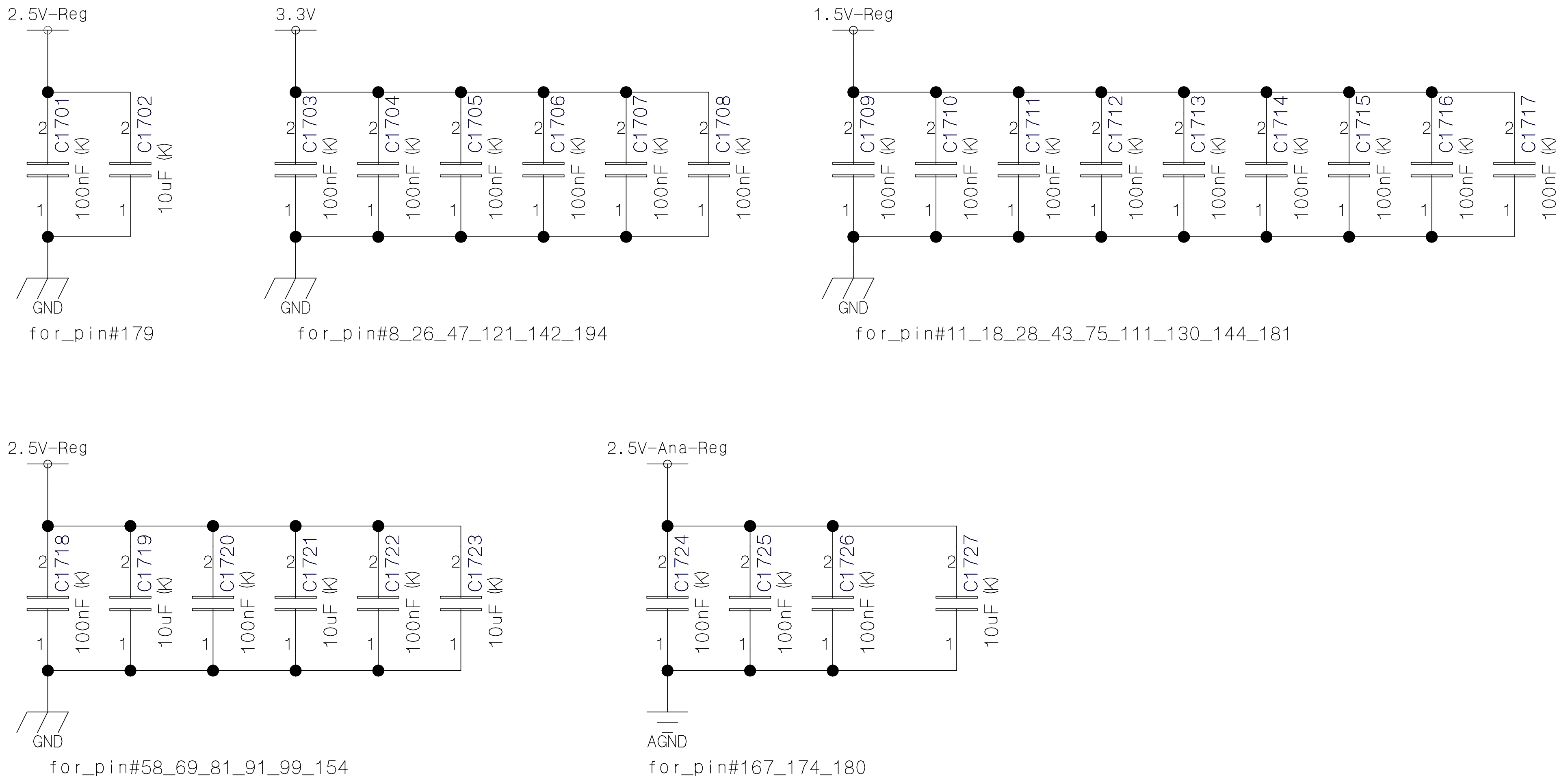
U05A DIGITAL [104] AUDIO, VIDEO (PE0284)


V46A00007900
TC90403FG (BS, DRY, X
TC90403 5/6
[V-U90403]
IC100
[DDR I/F]
114:1B M_DDRDQ0 63 SDMD0
114:1B M_DDRDQ1 61 SDMD1
114:1B M_DDRDQ2 60 SDMD2
114:1B M_DDRDQ3 59 SDMD3
114:1B M_DDRDQ4 57 SDMD4
114:1B M_DDRDQ5 55 SDMD5
114:1B M_DDRDQ6 54 SDMD6
114:1B M_DDRDQ7 53 SDMD7
114:1B M_DDRDQ8 104 SDMD8
114:1B M_DDRDQ9 103 SDMD9
114:1B M_DDRDQ10 102 SDMD10
114:1B M_DDRDQ11 101 SDMD11
114:1B M_DDRDQ12 100 SDMD12
114:1B M_DDRDQ13 98 SDMD13
114:1B M_DDRDQ14 97 SDMD14
114:1B M_DDRDQ15 95 SDMD15
112:2E;115:6C M_DDRVREF 106 SDVREF
SDDQS0 64 M_DDRDQS0 113:1D
SDDQS1 94 M_DDRDQS1 113:1D
SDDM0 65 M_DDRDM0 113:1D
SDDM1 93 M_DDRDM1 113:1D
SDADD0 74 M_DDRAD0 113:1B
SDADD1 76 M_DDRAD1 113:1B
SDADD2 77 M_DDRAD2 113:1B
SDADD3 78 M_DDRAD3 113:1B
SDADD4 80 M_DDRAD4 113:1B
SDADD5 82 M_DDRAD5 113:1B
SDADD6 83 M_DDRAD6 113:1B
SDADD7 84 M_DDRAD7 113:1B
SDADD8 85 M_DDRAD8 113:1B
SDADD9 86 M_DDRAD9 113:1B
SDADD10 73 M_DDRAD10 113:1B
SDADD11 87 M_DDRAD11 113:1B
SDADD12 89 M_DDRAD12 113:1B
SDCLK 90
SDBCLK 92
C1501
5pF (C)
1
2
NOT MOUNT
M_DDRCLK0 113:1D
M_DDRCLKB0 113:1D
SDCKE 107 M_DDRCKE 113:1D
SDBA0 70 M_DDRBA0 113:1D
SDBA1 72 M_DDRBA1 113:1D
SDWE_N 66 M_DDRWE 113:1D
SDRAS_N 68 M_DDRRAS 113:1D
SDCAS_N 67 M_DDRCAS 113:1D

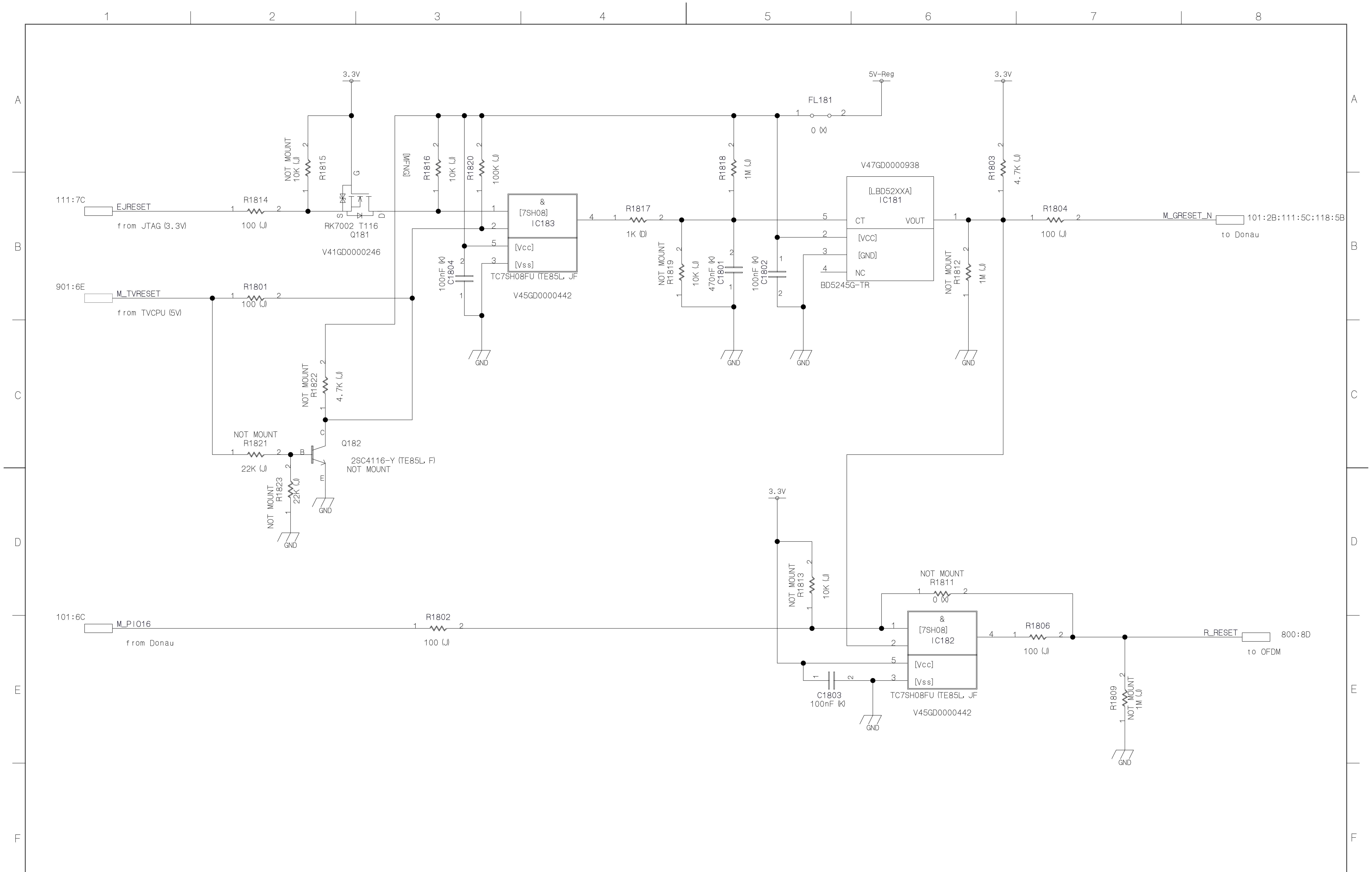
U05A DIGITAL [105] DDRSDRAM I/F (PE0284)


V46A00007900
TC90403FG (BS, DRY, X
TC90403 6/6
[V-U90403]
IC100
[POWER]
2.5V-Ana-Reg
2.5V-Reg
FL161
MMZ1608S102CT
C1601
100nF (K)
C1602
10uF (K)
PLLGND
1.5V-Reg
FL162
MMZ1608S102CT
C1603
100nF (K)
C1604
10uF (K)
GND
3.3V
1.5V-Reg
2.5V-Reg
179 [DACDVD]
180 [DACAVD]
174 [DACAVD]
167 [DACAVD]
162 [PLLAVD]
164 [PLLDVD]
8 [VDDS12]
26 [VDDS12]
47 [VDDS12]
121 [VDDS12]
142 [VDDS12]
194 [VDDS12]
11 [VDDC]
18 [VDDC]
28 [VDDC]
43 [VDDC]
75 [VDDC]
111 [VDDC]
130 [VDDC]
144 [VDDC]
181 [VDDC]
58 [VDD12]
69 [VDD12]
81 [VDD12]
91 [VDD12]
99 [VDD12]
154 [VDD12]
[DACDVS] 178
[DACAVS] 176
[DACAVS] 169
[DACAVS] 165
[PLLAVS] 161
[PLLDVS] 163
[VSS12] 6
[VSS12] 15
[VSS12] 24
[VSS12] 36
[VSS12] 45
[VSS12] 56
[VSS12] 62
[VSS12] 71
[VSS12] 79
[VSS12] 88
[VSS12] 96
[VSS12] 105
[VSS12] 116
[VSS12] 126
[VSS12] 136
[VSS12] 148
[VSS12] 189
[VSS12] 200
GND
AGND
PLLGND
GND
GND

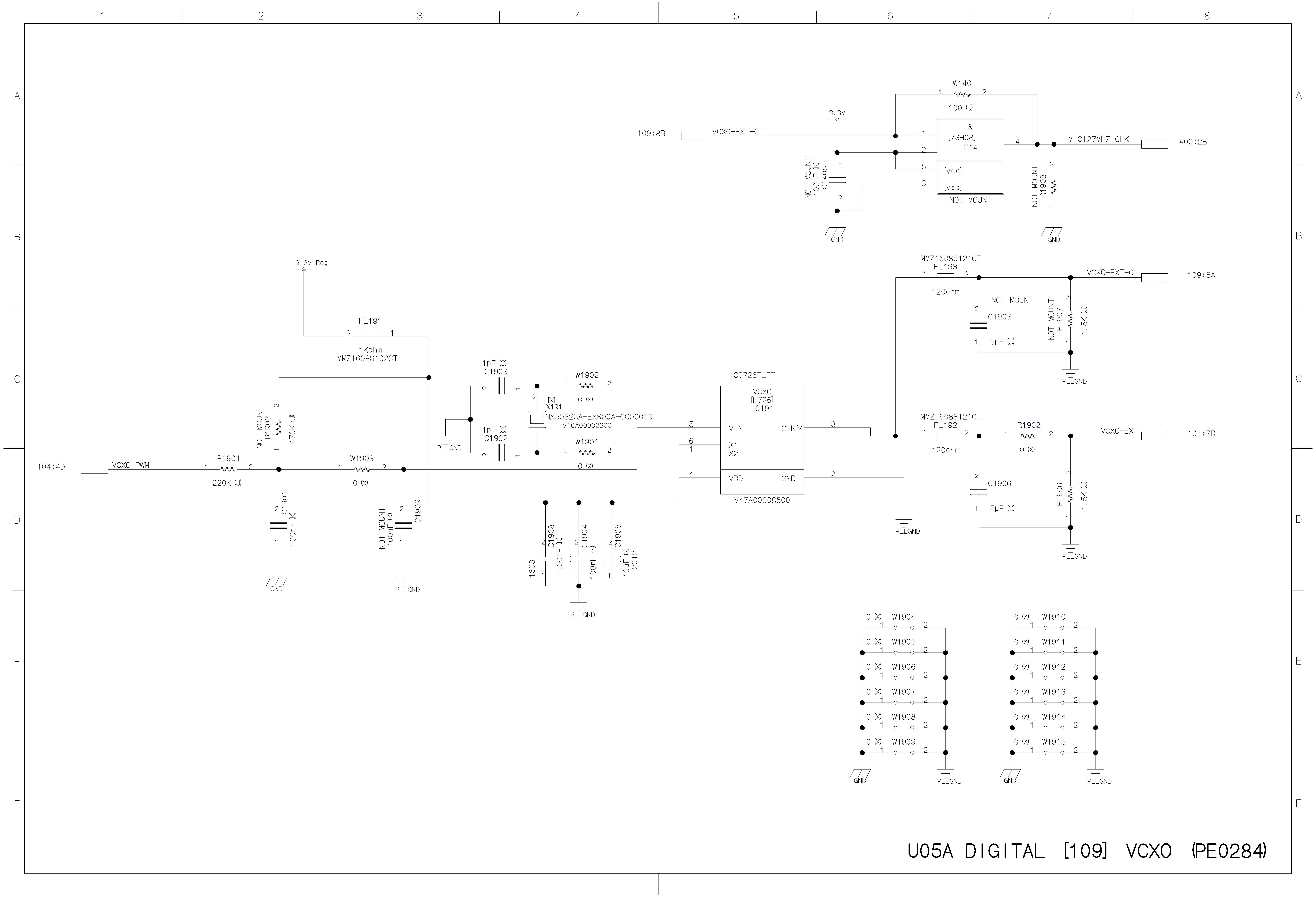
U05A DIGITAL [106] Donau PWR1 (PE0284)


2.5V-Reg
C1701
100nF (K)
C1702
10uF (K)
GND
for_pin#179
3.3V
C1703
100nF (K)
C1704
100nF (K)
C1705
100nF (K)
C1706
100nF (K)
C1707
100nF (K)
C1708
100nF (K)
GND
for_pin#8_26_47_121_142_194
1.5V-Reg
C1709
100nF (K)
C1710
100nF (K)
C1711
100nF (K)
C1712
100nF (K)
C1713
100nF (K)
C1714
100nF (K)
C1715
100nF (K)
C1716
100nF (K)
C1717
100nF (K)
GND
for_pin#11_18_28_43_75_111_130_144_181
2.5V-Reg
C1718
100nF (K)
C1719
10uF (K)
C1720
100nF (K)
C1721
10uF (K)
C1722
100nF (K)
C1723
10uF (K)
GND
for_pin#58_69_81_91_99_154
2.5V-Ana-Reg
C1724
100nF (K)
C1725
100nF (K)
C1726
100nF (K)
C1727
10uF (K)
AGND
for_pin#167_174_180

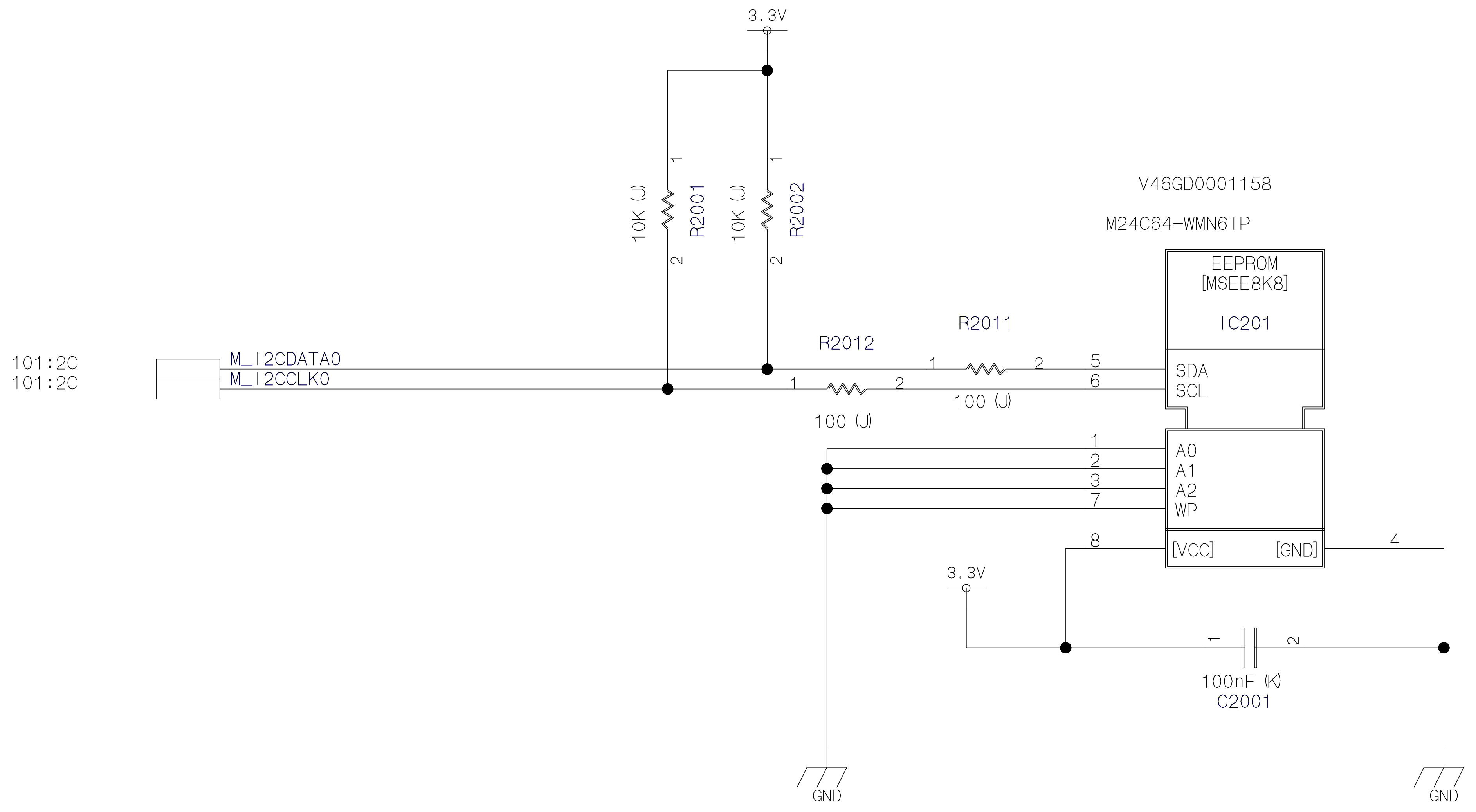
U05A DIGITAL [107] Donau PATHCON (PE0284)


1
2
3
4
5
6
7
8
A
B
C
D
E
F
3.3V
5V-Reg
3.3V
FL181
1
2
0 (X)
NOT MOUNT
10K (J)
R1815
G
[MFNG]
R1816
10K (J)
R1820
100K (J)
R1818
1M (J)
R1803
4.7K (J)
V47GD0000938
[LBD52XXA]
IC181
CT
VOUT
[VCC]
[GND]
NC
BD5245G-TR
111:7C
EJRESET
from JTAG (3.3V)
R1814
100 (J)
S
D
RK7002 T116
Q181
V41GD0000246
&
[7SH08]
IC183
[Vcc]
[Vss]
TC7SH08FU (TE85L, JF
V45GD0000442
100nF (K)
C1804
R1817
1K (D)
NOT MOUNT
R1819
10K (J)
470nF (K)
C1801
100nF (K)
C1802
NOT MOUNT
R1812
1M (J)
R1804
100 (J)
M_GRESET_N
101:2B;111:5C;118:5B
to Donau
901:6E
M_TVRESET
from TVCPU (5V)
R1801
100 (J)
GND
NOT MOUNT
R1822
4.7K (J)
C
B
E
Q182
2SC4116-Y (TE85L, F)
NOT MOUNT
NOT MOUNT
R1821
22K (J)
NOT MOUNT
R1823
22K (J)
3.3V
NOT MOUNT
R1813
10K (J)
NOT MOUNT
R1811
0 (X)
&
[7SH08]
IC182
[Vcc]
[Vss]
TC7SH08FU (TE85L, JF
V45GD0000442
R1806
100 (J)
R_RESET
800:8D
to OFDM
C1803
100nF (K)
R1809
NOT MOUNT
1M (J)
101:6C
M_PIO16
from Donau
R1802
100 (J)

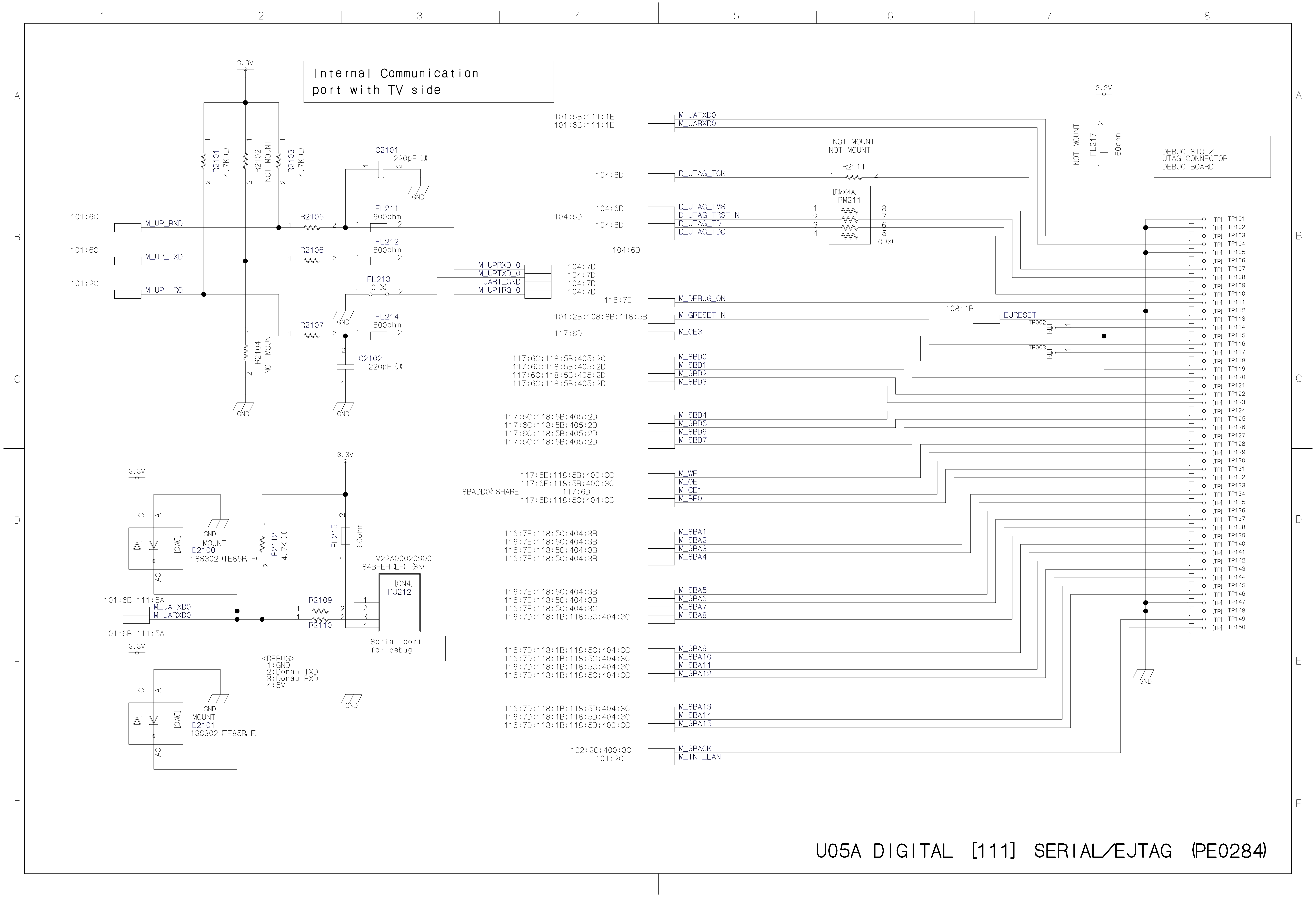
U05A DIGITAL [108] RST/CLK (PE0284)


1
2
3
4
5
6
7
8
A
B
C
D
E
F
W140
100 (J)
3.3V
109:8B
VCXO-EXT-CI
&
[7SH08]
IC141
[Vcc]
[Vss]
NOT MOUNT
M_CI27MHZ_CLK
400:2B
NOT MOUNT
100nF (K)
C1405
NOT MOUNT
R1908
GND
GND
MMZ1608S121CT
FL193
120ohm
VCXO-EXT-CI
109:5A
NOT MOUNT
C1907
5pF (C)
NOT MOUNT
R1907
1.5K (J)
PLLGND
3.3V-Reg
FL191
1Kohm
MMZ1608S102CT
1pF (C)
C1903
W1902
0 (X)
(X)
X191
NX5032GA-EXS00A-CG00019
V10A00002600
1pF (C)
C1902
W1901
0 (X)
PLLGND
ICS726TLFT
VCXO
[L726]
IC191
VIN
X1
X2
CLK
VDD
GND
V47A00008500
PLLGND
MMZ1608S121CT
FL192
120ohm
R1902
0 (X)
VCXO-EXT
101:7D
C1906
5pF (C)
R1906
1.5K (J)
PLLGND
NOT MOUNT
R1903
470K (J)
104:4D
VCXO-PWM
R1901
220K (J)
W1903
0 (X)
C1901
100nF (K)
GND
NOT MOUNT
100nF (K)
C1909
PLLGND
1608
C1908
100nF (K)
C1904
100nF (K)
C1905
10uF (K)
2012
PLLGND
0 (X) W1904
0 (X) W1905
0 (X) W1906
0 (X) W1907
0 (X) W1908
0 (X) W1909
GND
PLLGND
0 (X) W1910
0 (X) W1911
0 (X) W1912
0 (X) W1913
0 (X) W1914
0 (X) W1915
GND
PLLGND

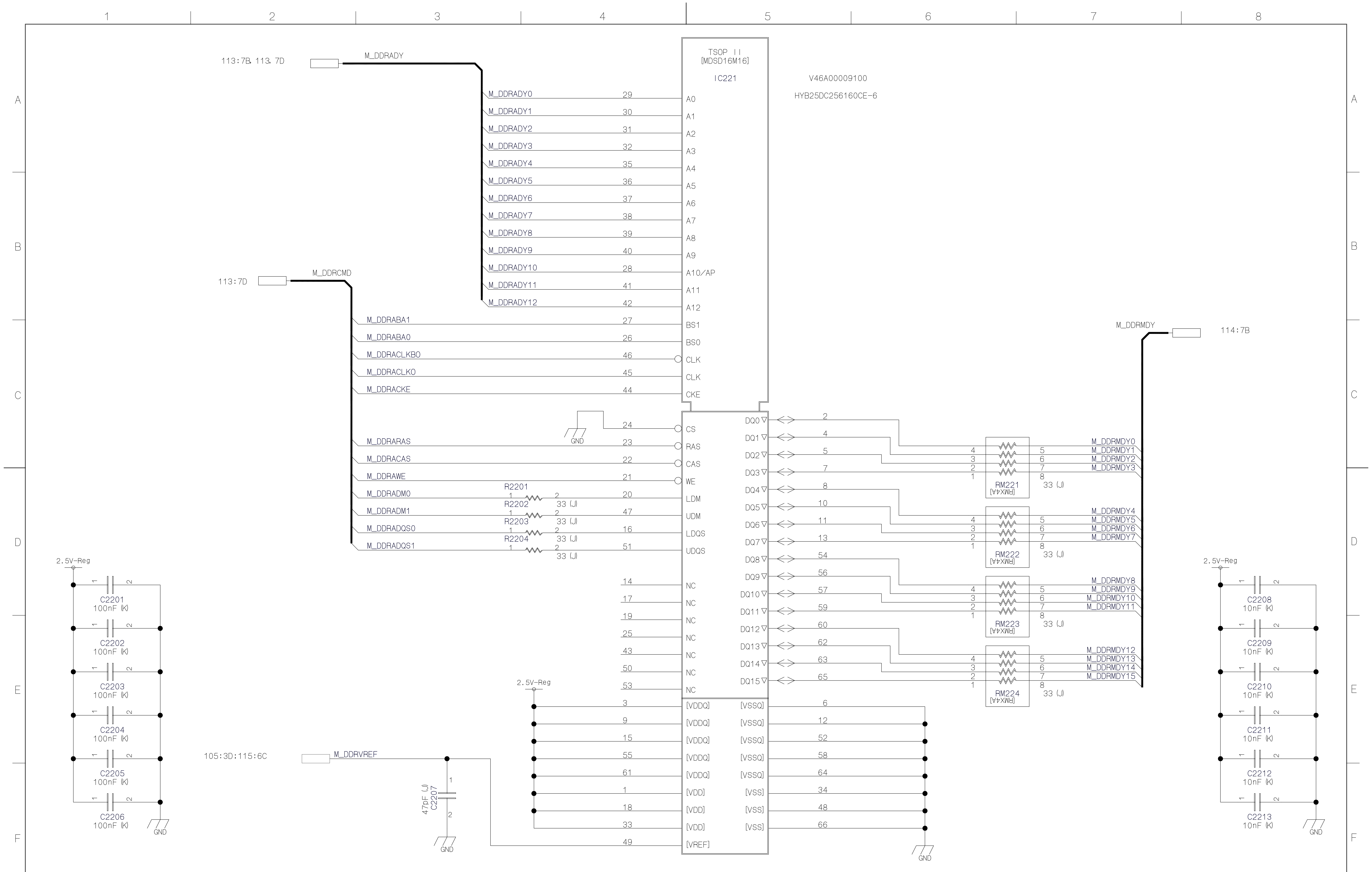
U05A DIGITAL [109] VCXO (PE0284)


3.3V
10K (J)
R2001
10K (J)
R2002
1
2
1
2
V46GD0001158
M24C64-WMN6TP
EEPROM
[MSEE8K8]
IC201
101:2C
101:2C
M_I2CDATA0
M_I2CCLK0
R2012
R2011
1
2
1
2
100 (J)
100 (J)
5
6
SDA
SCL
1
2
3
7
A0
A1
A2
WP
8
[VCC]
[GND]
4
3.3V
1
2
100nF (K)
C2001
GND
GND

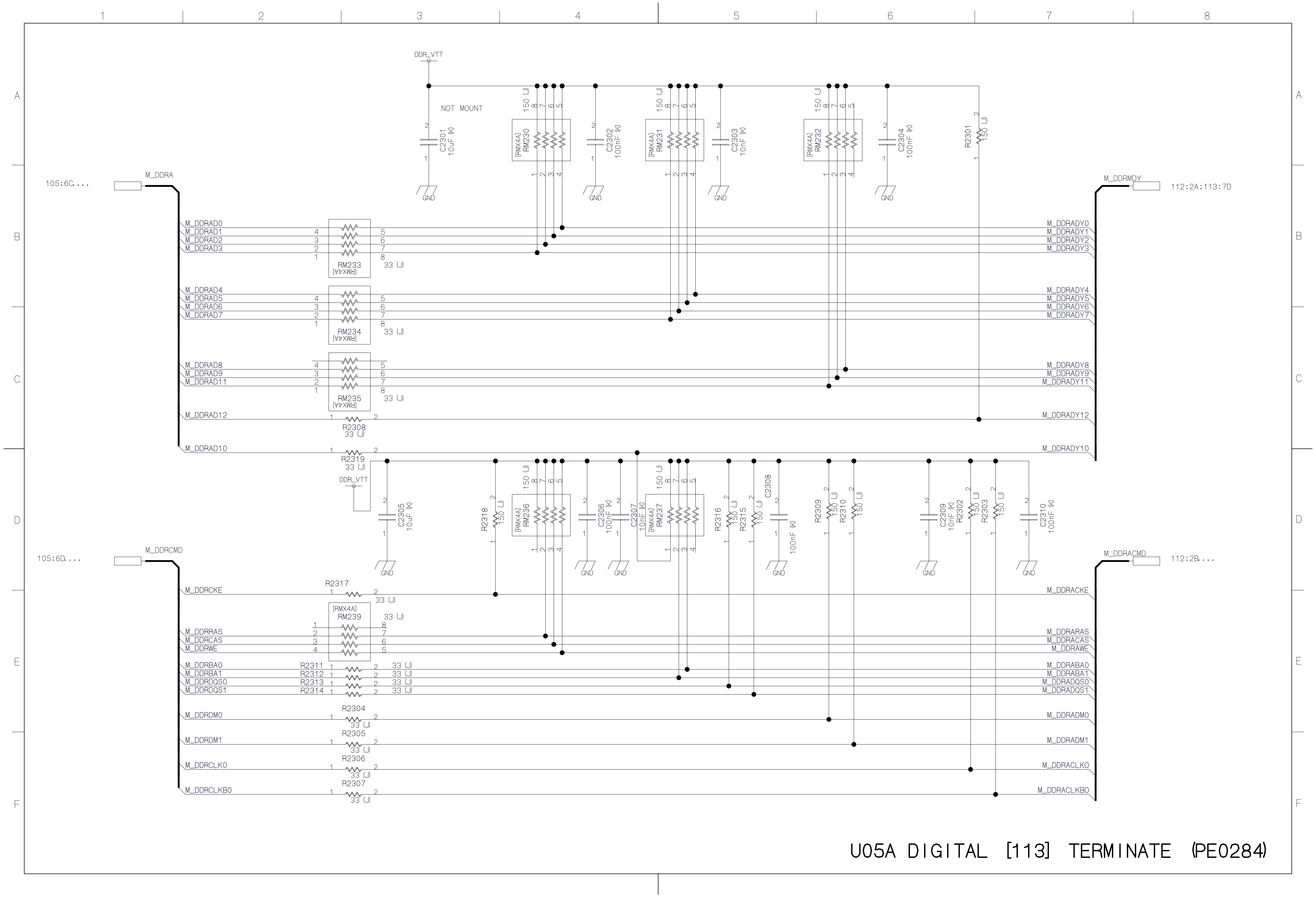
U05A DIGITAL [110] E2PROM (PE0284)


Internal Communication port with TV side
3.3V
R2101 4.7K (J)
R2102 NOT MOUNT
R2103 4.7K (J)
C2101 220pF (J)
GND
101:6C M_UP_RXD
101:6C M_UP_TXD
101:2C M_UP_IRQ
R2105
R2106
R2107
FL211 600ohm
FL212 600ohm
FL213 0 (X)
FL214 600ohm
R2104 NOT MOUNT
C2102 220pF (J)
M_UPRXD_0
M_UPTXD_0
UART_GND
M_UPIRQ_0
104:7D
104:7D
104:7D
104:7D
101:6B:111:1E M_UATXD0
101:6B:111:1E M_UARXD0
NOT MOUNT
NOT MOUNT
R2111
104:6D D_JTAG_TCK
104:6D D_JTAG_TMS
104:6D D_JTAG_TRST_N
104:6D D_JTAG_TDI
104:6D D_JTAG_TDO
[RMX4A] RM211
0 (X)
116:7E M_DEBUG_ON
101:2B:108:8B:118:5B M_GRESET_N
117:6D M_CE3
108:1B EJRESET
TP002
TP003
117:6C:118:5B:405:2C M_SBD0
117:6C:118:5B:405:2D M_SBD1
117:6C:118:5B:405:2D M_SBD2
117:6C:118:5B:405:2D M_SBD3
117:6C:118:5B:405:2D M_SBD4
117:6C:118:5B:405:2D M_SBD5
117:6C:118:5B:405:2D M_SBD6
117:6C:118:5B:405:2D M_SBD7
117:6E:118:5B:400:3C M_WE
117:6E:118:5B:400:3C M_OE
SBADD0と SHARE 117:6D M_CE1
117:6D:118:5C:404:3B M_BE0
116:7E:118:5C:404:3B M_SBA1
116:7E:118:5C:404:3B M_SBA2
116:7E:118:5C:404:3B M_SBA3
116:7E:118:5C:404:3B M_SBA4
116:7E:118:5C:404:3B M_SBA5
116:7E:118:5C:404:3B M_SBA6
116:7E:118:5C:404:3C M_SBA7
116:7D:118:1B:118:5C:404:3C M_SBA8
116:7D:118:1B:118:5C:404:3C M_SBA9
116:7D:118:1B:118:5C:404:3C M_SBA10
116:7D:118:1B:118:5C:404:3C M_SBA11
116:7D:118:1B:118:5C:404:3C M_SBA12
116:7D:118:1B:118:5D:404:3C M_SBA13
116:7D:118:1B:118:5D:404:3C M_SBA14
116:7D:118:1B:118:5D:400:3C M_SBA15
102:2C:400:3C M_SBACK
101:2C M_INT_LAN
3.3V
NOT MOUNT
FL217 60ohm
DEBUG SIO / JTAG CONNECTOR DEBUG BOARD
[TP] TP101 – TP150
GND
3.3V
MOUNT D2100 1SS302 (TE85R, F)
MOUNT D2101 1SS302 (TE85R, F)
3.3V
R2112 4.7K (J)
FL215 600ohm
V22A00020900 S4B-EH (LF) (SN)
[CN4] PJ212
101:6B:111:5A M_UATXD0
101:6B:111:5A M_UARXD0
R2109
R2110
Serial port for debug
<DEBUG>
1:GND
2:Donau TXD
3:Donau RXD
4:5V
GND

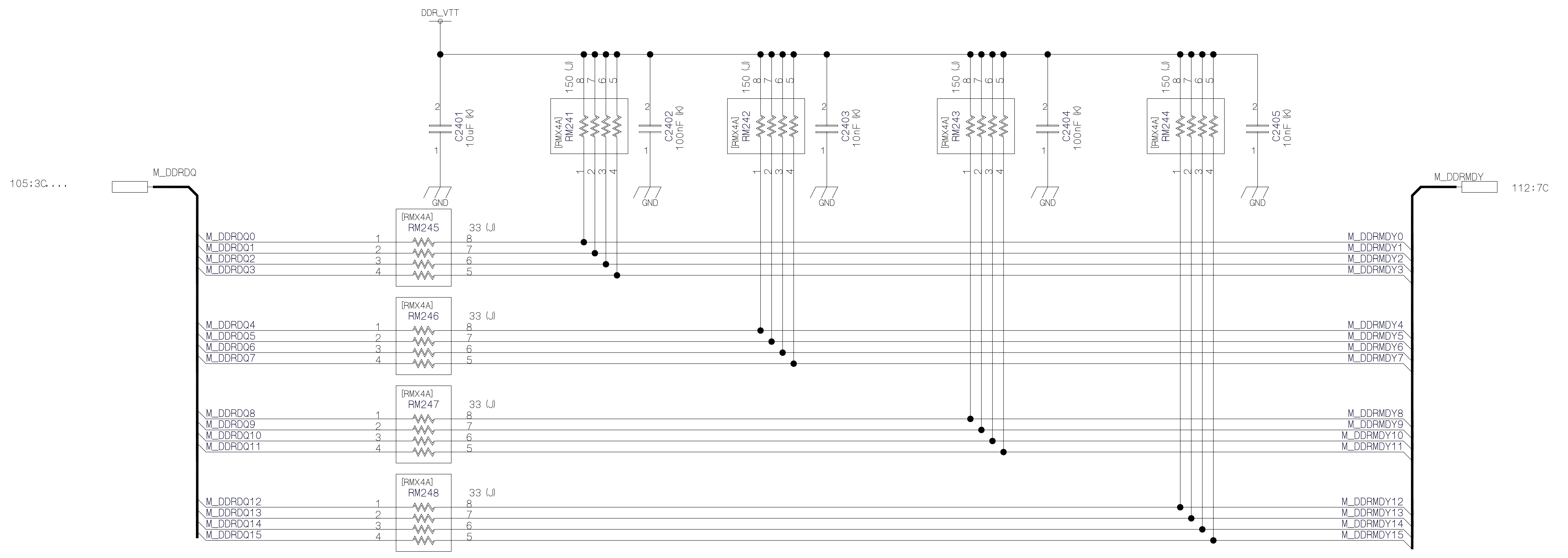
U05A DIGITAL [111] SERIAL/EJTAG (PE0284)


1
2
3
4
5
6
7
8
A
B
C
D
E
F
TSOP II
[MDSD16M16]
IC221
V46A00009100
HYB25DC256160CE-6
113:7B, 113: 7D
M_DDRADY
M_DDRADY0 29 A0
M_DDRADY1 30 A1
M_DDRADY2 31 A2
M_DDRADY3 32 A3
M_DDRADY4 35 A4
M_DDRADY5 36 A5
M_DDRADY6 37 A6
M_DDRADY7 38 A7
M_DDRADY8 39 A8
M_DDRADY9 40 A9
M_DDRADY10 28 A10/AP
M_DDRADY11 41 A11
M_DDRADY12 42 A12
113:7D
M_DDRCMD
M_DDRABA1 27 BS1
M_DDRABA0 26 BS0
M_DDRACLKB0 46 CLK
M_DDRACLK0 45 CLK
M_DDRACKE 44 CKE
GND 24 CS
M_DDRARAS 23 RAS
M_DDRACAS 22 CAS
M_DDRAWE 21 WE
R2201 M_DDRADM0 20 LDM
R2202 M_DDRADM1 47 UDM
R2203 M_DDRADQS0 16 LDQS
R2204 M_DDRADQS1 51 UDQS
33 (J)
14 NC
17 NC
19 NC
25 NC
43 NC
50 NC
53 NC
2 DQ0
4 DQ1
5 DQ2
7 DQ3
8 DQ4
10 DQ5
11 DQ6
13 DQ7
54 DQ8
56 DQ9
57 DQ10
59 DQ11
60 DQ12
62 DQ13
63 DQ14
65 DQ15
RM221
RM222
RM223
RM224
[RMX4A]
33 (J)
M_DDRMDY
114:7B
M_DDRMDY0
M_DDRMDY1
M_DDRMDY2
M_DDRMDY3
M_DDRMDY4
M_DDRMDY5
M_DDRMDY6
M_DDRMDY7
M_DDRMDY8
M_DDRMDY9
M_DDRMDY10
M_DDRMDY11
M_DDRMDY12
M_DDRMDY13
M_DDRMDY14
M_DDRMDY15
2.5V-Reg
3 [VDDQ]
9 [VDDQ]
15 [VDDQ]
55 [VDDQ]
61 [VDDQ]
1 [VDD]
18 [VDD]
33 [VDD]
49 [VREF]
[VSSQ] 6
[VSSQ] 12
[VSSQ] 52
[VSSQ] 58
[VSSQ] 64
[VSS] 34
[VSS] 48
[VSS] 66
105:3D:115:6C
M_DDRVREF
47pF (J)
C2207
C2201 100nF (K)
C2202 100nF (K)
C2203 100nF (K)
C2204 100nF (K)
C2205 100nF (K)
C2206 100nF (K)
C2208 10nF (K)
C2209 10nF (K)
C2210 10nF (K)
C2211 10nF (K)
C2212 10nF (K)
C2213 10nF (K)
GND

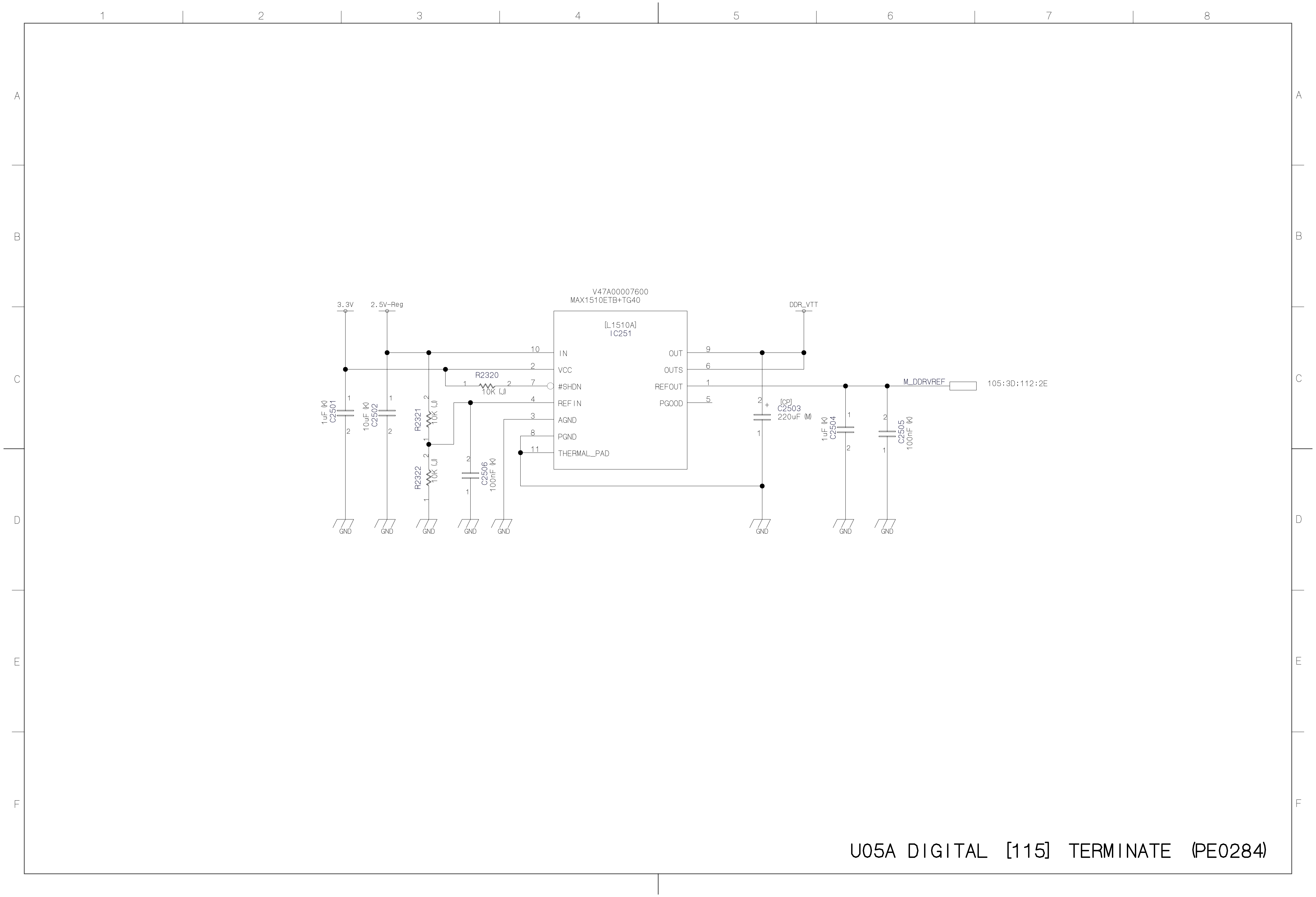
U05A DIGITAL [112] DDR1 SDRAM (PE0284)


DDR_VTT
NOT MOUNT
C2301 10uF (K)
RM230 [RMX4A] 150 (J)
C2302 100nF (K)
RM231 [RMX4A] 150 (J)
C2303 10nF (K)
RM232 [RMX4A] 150 (J)
C2304 100nF (K)
R2301 150 (J)
GND
105:6C...
M_DDRA
M_DDRMDY
112:2A;113:7D
M_DDRAD0
M_DDRAD1
M_DDRAD2
M_DDRAD3
RM233 [RMX4A] 33 (J)
M_DDRAD4
M_DDRAD5
M_DDRAD6
M_DDRAD7
RM234 [RMX4A] 33 (J)
M_DDRAD8
M_DDRAD9
M_DDRAD11
RM235 [RMX4A] 33 (J)
M_DDRAD12
R2308 33 (J)
M_DDRAD10
R2319 33 (J)
M_DDRADY0
M_DDRADY1
M_DDRADY2
M_DDRADY3
M_DDRADY4
M_DDRADY5
M_DDRADY6
M_DDRADY7
M_DDRADY8
M_DDRADY9
M_DDRADY11
M_DDRADY12
M_DDRADY10
DDR_VTT
C2305 10uF (K)
R2318 150 (J)
RM236 [RMX4A] 150 (J)
C2306 100nF (K)
C2307 10nF (K)
RM237 [RMX4A] 150 (J)
R2316 150 (J)
R2315 150 (J)
C2308 100nF (K)
R2309 150 (J)
R2310 150 (J)
C2309 10nF (K)
R2302 150 (J)
R2303 150 (J)
C2310 100nF (K)
105:6D...
M_DDRCMD
M_DDRACMD
112:2B...
M_DDRCKE
R2317 33 (J)
RM239 [RMX4A] 33 (J)
M_DDRRAS
M_DDRCAS
M_DDRWE
M_DDRBA0
M_DDRBA1
M_DDRDQS0
M_DDRDQS1
R2311 33 (J)
R2312 33 (J)
R2313 33 (J)
R2314 33 (J)
M_DDRDM0
R2304 33 (J)
M_DDRDM1
R2305 33 (J)
M_DDRCLK0
R2306 33 (J)
M_DDRCLKB0
R2307 33 (J)
M_DDRACKE
M_DDRARAS
M_DDRACAS
M_DDRAWE
M_DDRABA0
M_DDRABA1
M_DDRADQS0
M_DDRADQS1
M_DDRADM0
M_DDRADM1
M_DDRACLK0
M_DDRACLKB0
1 2 3 4 5 6 7 8
A B C D E F

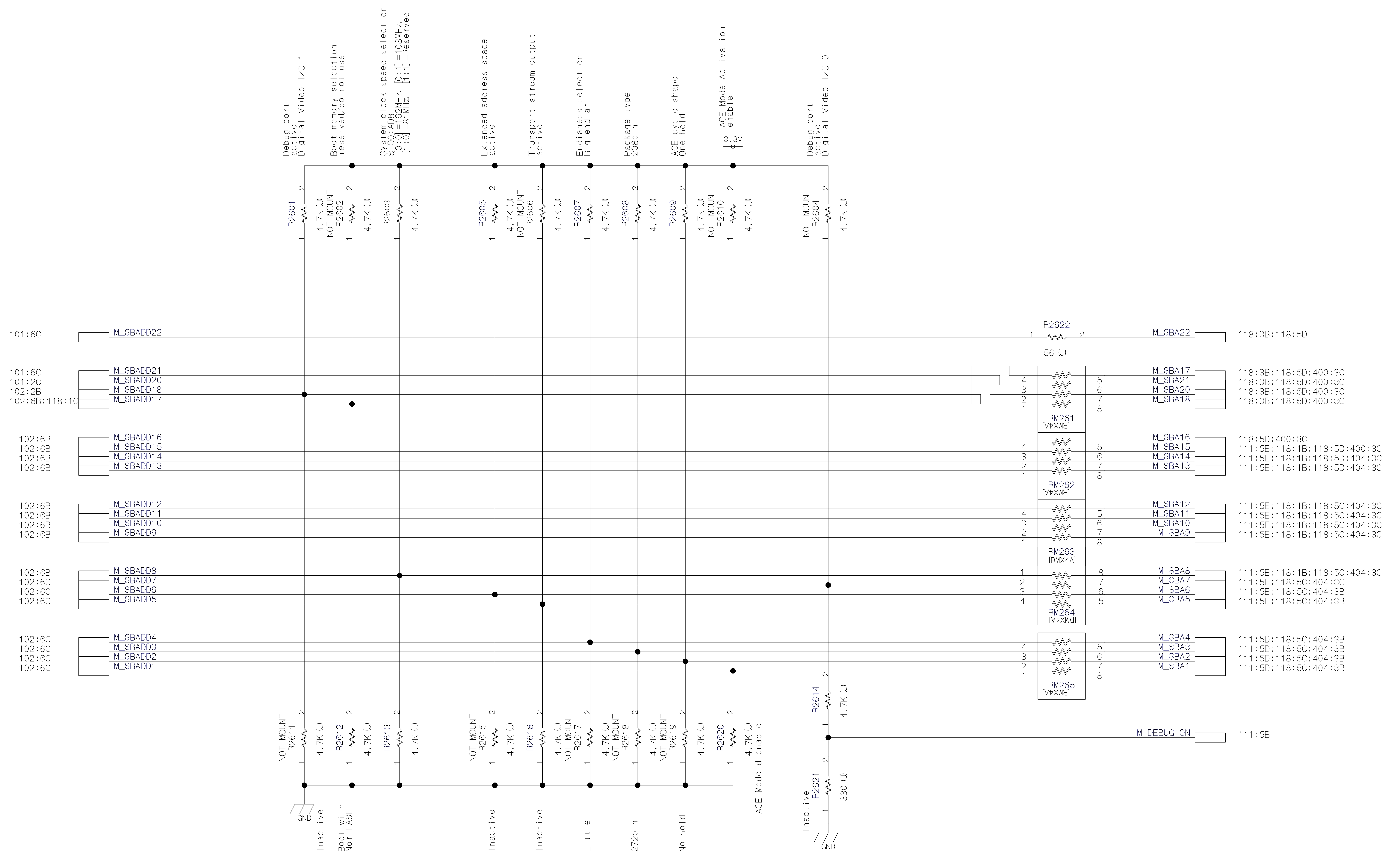
U05A DIGITAL [113] TERMINATE (PE0284)


DDR_VTT
C2401
10uF (K)
C2402
100nF (K)
C2403
10nF (K)
C2404
100nF (K)
C2405
10nF (K)
[RMX4A]
RM241
RM242
RM243
RM244
150 (J)
GND
105:3C...
M_DDRDQ
M_DDRMDY
112:7C
RM245
RM246
RM247
RM248
33 (J)
M_DDRDQ0
M_DDRDQ1
M_DDRDQ2
M_DDRDQ3
M_DDRDQ4
M_DDRDQ5
M_DDRDQ6
M_DDRDQ7
M_DDRDQ8
M_DDRDQ9
M_DDRDQ10
M_DDRDQ11
M_DDRDQ12
M_DDRDQ13
M_DDRDQ14
M_DDRDQ15
M_DDRMDY0
M_DDRMDY1
M_DDRMDY2
M_DDRMDY3
M_DDRMDY4
M_DDRMDY5
M_DDRMDY6
M_DDRMDY7
M_DDRMDY8
M_DDRMDY9
M_DDRMDY10
M_DDRMDY11
M_DDRMDY12
M_DDRMDY13
M_DDRMDY14
M_DDRMDY15

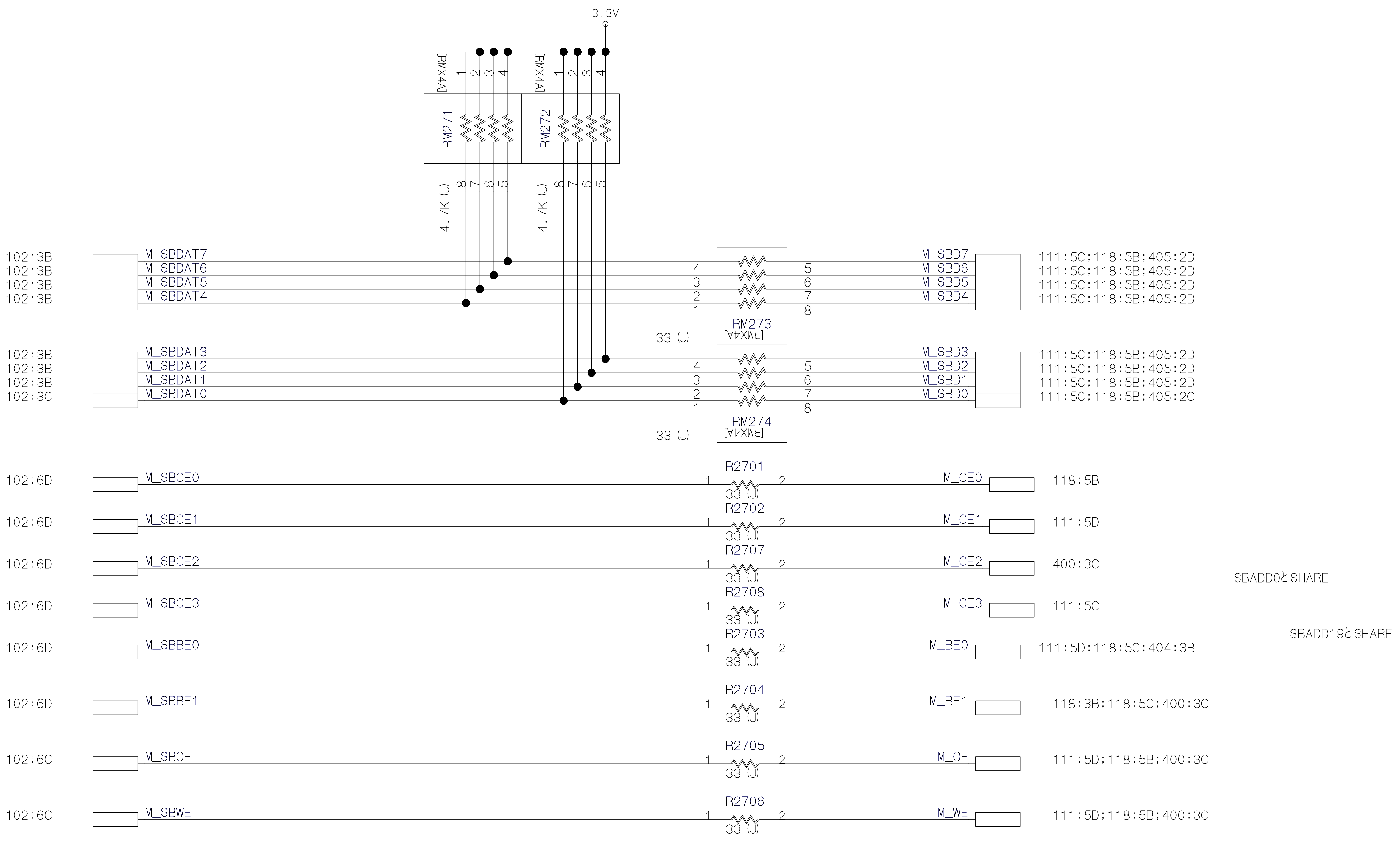
U05A DIGITAL [114] TERMINATE (PE0284)


1
2
3
4
5
6
7
8
A
B
C
D
E
F
V47A00007600
MAX1510ETB+TG40
[L1510A]
IC251
3.3V
2.5V-Reg
DDR_VTT
10
IN
2
VCC
7
#SHDN
4
REFIN
3
AGND
8
PGND
11
THERMAL_PAD
9
OUT
6
OUTS
1
REFOUT
5
PGOOD
R2320
10K (J)
R2321
10K (J)
R2322
10K (J)
C2501
1uF (K)
C2502
10uF (K)
C2506
100nF (K)
[CP]
C2503
220uF (M)
C2504
1uF (K)
C2505
100nF (K)
M_DDRVREF
105:3D;112:2E
GND

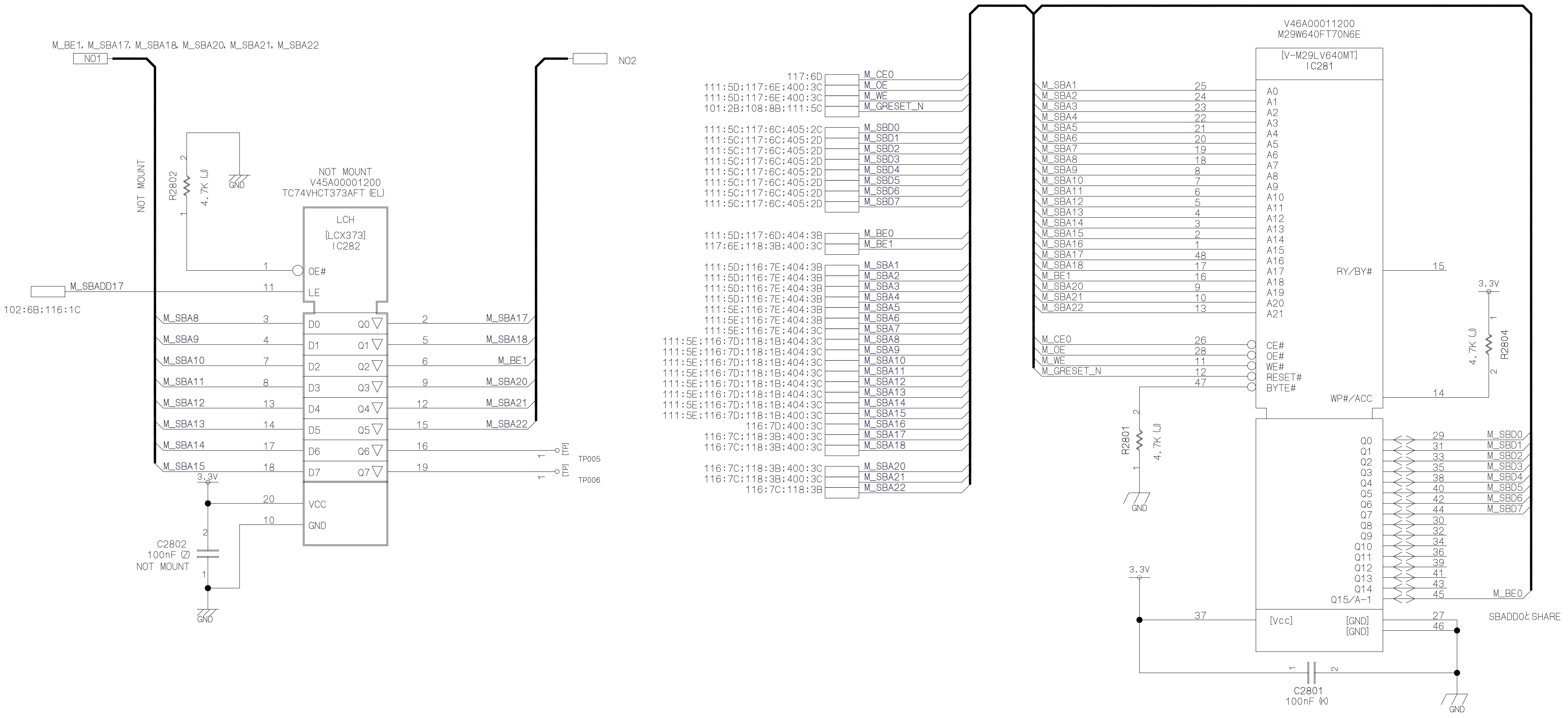
U05A DIGITAL [115] TERMINATE (PE0284)


Debug port
active
Digital Video I/O 1
Boot memory selection
reserved/do not use
System clock speed selection
SI00:AD8
[0:0] =162MHz, [0:1] =108MHz,
[1:0] =81MHz, [1:1] =Reserved
Extended address space
active
Transport stream output
active
Endianess selection
Big endian
Package type
208pin
ACE cycle shape
One hold
ACE Mode Activation
enable
3.3V
Debug port
active
Digital Video I/O 0
R2601
4.7K (J)
NOT MOUNT
R2602
4.7K (J)
R2603
4.7K (J)
R2605
4.7K (J)
NOT MOUNT
R2606
4.7K (J)
R2607
4.7K (J)
R2608
4.7K (J)
R2609
4.7K (J)
NOT MOUNT
R2610
4.7K (J)
NOT MOUNT
R2604
4.7K (J)
101:6C
M_SBADD22
R2622
56 (J)
M_SBA22
118:3B;118:5D
101:6C
101:2C
102:2B
102:6B;118:1C
M_SBADD21
M_SBADD20
M_SBADD18
M_SBADD17
RM261
[RMX4A]
M_SBA17
M_SBA21
M_SBA20
M_SBA18
118:3B;118:5D;400:3C
118:3B;118:5D;400:3C
118:3B;118:5D;400:3C
118:3B;118:5D;400:3C
102:6B
102:6B
102:6B
102:6B
M_SBADD16
M_SBADD15
M_SBADD14
M_SBADD13
RM262
[RMX4A]
M_SBA16
M_SBA15
M_SBA14
M_SBA13
118:5D;400:3C
111:5E;118:1B;118:5D;400:3C
111:5E;118:1B;118:5D;404:3C
111:5E;118:1B;118:5D;404:3C
102:6B
102:6B
102:6B
102:6B
M_SBADD12
M_SBADD11
M_SBADD10
M_SBADD9
RM263
[RMX4A]
M_SBA12
M_SBA11
M_SBA10
M_SBA9
111:5E;118:1B;118:5C;404:3C
111:5E;118:1B;118:5C;404:3C
111:5E;118:1B;118:5C;404:3C
111:5E;118:1B;118:5C;404:3C
102:6B
102:6C
102:6C
102:6C
M_SBADD8
M_SBADD7
M_SBADD6
M_SBADD5
RM264
[RMX4A]
M_SBA8
M_SBA7
M_SBA6
M_SBA5
111:5E;118:1B;118:5C;404:3C
111:5E;118:5C;404:3C
111:5E;118:5C;404:3B
111:5E;118:5C;404:3B
102:6C
102:6C
102:6C
102:6C
M_SBADD4
M_SBADD3
M_SBADD2
M_SBADD1
RM265
[RMX4A]
M_SBA4
M_SBA3
M_SBA2
M_SBA1
111:5D;118:5C;404:3B
111:5D;118:5C;404:3B
111:5D;118:5C;404:3B
111:5D;118:5C;404:3B
R2614
4.7K (J)
M_DEBUG_ON
111:5B
NOT MOUNT
R2611
4.7K (J)
R2612
4.7K (J)
R2613
4.7K (J)
NOT MOUNT
R2615
4.7K (J)
R2616
4.7K (J)
NOT MOUNT
R2617
4.7K (J)
NOT MOUNT
R2618
4.7K (J)
NOT MOUNT
R2619
4.7K (J)
R2620
4.7K (J)
ACE Mode dienable
R2621
330 (J)
GND
Inactive
Boot with
NorFLASH
Inactive
Inactive
Little
272pin
No hold
Inactive
GND

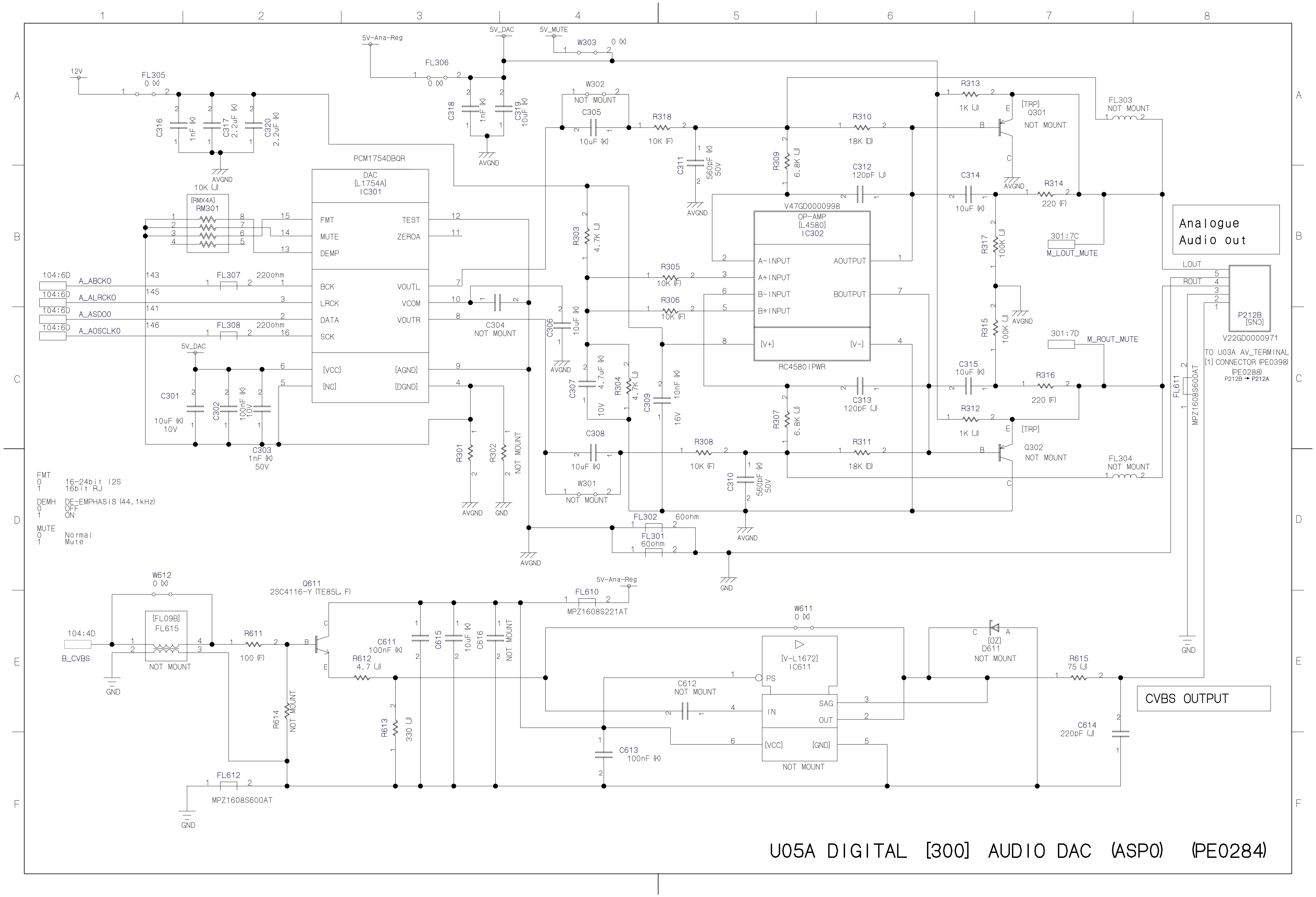
U05A DIGITAL [116] PIN CONFIG (PE0284)


3.3V
[RMX4A]
RM271
4.7K (J)
[RMX4A]
RM272
4.7K (J)
1 2 3 4
8 7 6 5
102:3B M_SBDAT7
102:3B M_SBDAT6
102:3B M_SBDAT5
102:3B M_SBDAT4
4 3 2 1
5 6 7 8
RM273
[RMX4A]
33 (J)
M_SBD7 111:5C;118:5B;405:2D
M_SBD6 111:5C;118:5B;405:2D
M_SBD5 111:5C;118:5B;405:2D
M_SBD4 111:5C;118:5B;405:2D
102:3B M_SBDAT3
102:3B M_SBDAT2
102:3B M_SBDAT1
102:3C M_SBDAT0
RM274
[RMX4A]
33 (J)
M_SBD3 111:5C;118:5B;405:2D
M_SBD2 111:5C;118:5B;405:2D
M_SBD1 111:5C;118:5B;405:2D
M_SBD0 111:5C;118:5B;405:2C
102:6D M_SBCE0
R2701 33 (J)
M_CE0 118:5B
102:6D M_SBCE1
R2702 33 (J)
M_CE1 111:5D
102:6D M_SBCE2
R2707 33 (J)
M_CE2 400:3C
SBADD0とSHARE
102:6D M_SBCE3
R2708 33 (J)
M_CE3 111:5C
SBADD19とSHARE
102:6D M_SBBE0
R2703 33 (J)
M_BE0 111:5D;118:5C;404:3B
102:6D M_SBBE1
R2704 33 (J)
M_BE1 118:3B;118:5C;400:3C
102:6C M_SBOE
R2705 33 (J)
M_OE 111:5D;118:5B;400:3C
102:6C M_SBWE
R2706 33 (J)
M_WE 111:5D;118:5B;400:3C
1 2

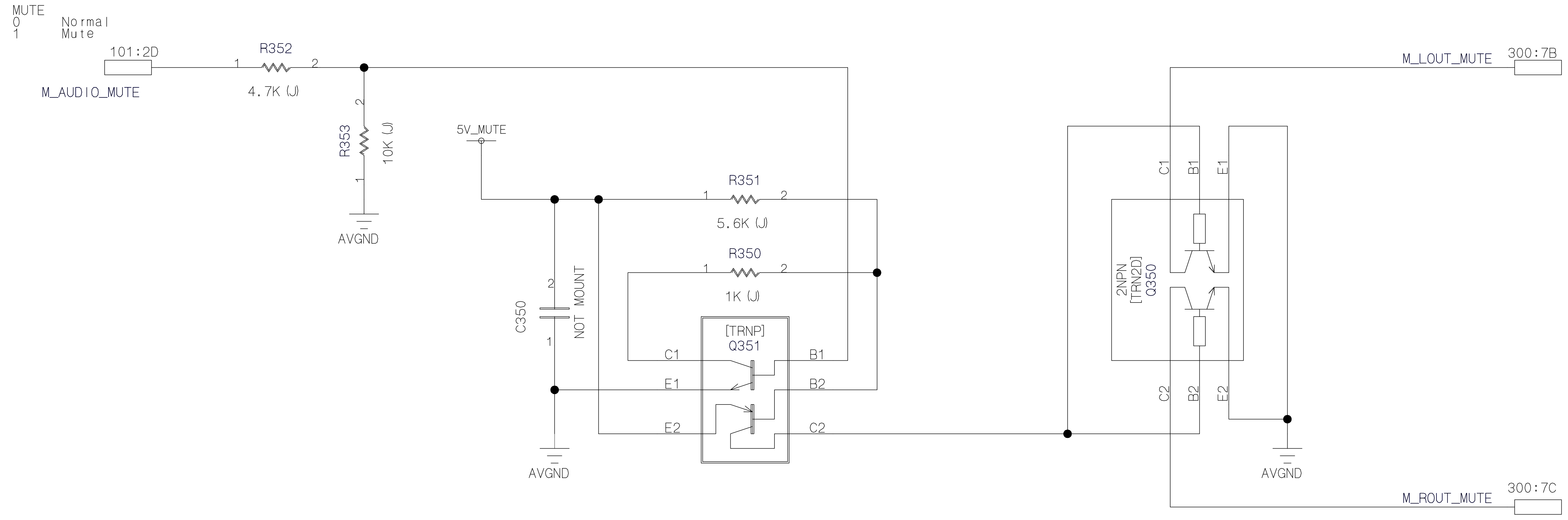
U05A DIGITAL [117] SBD REGISTER (PE0284)


M_BE1, M_SBA17, M_SBA18, M_SBA20, M_SBA21, M_SBA22
N01
N02
NOT MOUNT
R2802
4.7K (J)
GND
NOT MOUNT
V45A00001200
TC74VHCT373AFT (EL)
LCH
[LCX373]
IC282
OE#
LE
M_SBADD17
102:6B;116:1C
M_SBA8 3 D0 Q0 2 M_SBA17
M_SBA9 4 D1 Q1 5 M_SBA18
M_SBA10 7 D2 Q2 6 M_BE1
M_SBA11 8 D3 Q3 9 M_SBA20
M_SBA12 13 D4 Q4 12 M_SBA21
M_SBA13 14 D5 Q5 15 M_SBA22
M_SBA14 17 D6 Q6 16
M_SBA15 18 D7 Q7 19
[TP] TP005
[TP] TP006
3.3V
20 VCC
10 GND
C2802
100nF (Z)
NOT MOUNT
117:6D M_CE0
111:5D;117:6E;400:3C M_OE
111:5D;117:6E;400:3C M_WE
101:2B;108:8B;111:5C M_GRESET_N
111:5C;117:6C;405:2C M_SBD0
111:5C;117:6C;405:2D M_SBD1
111:5C;117:6C;405:2D M_SBD2
111:5C;117:6C;405:2D M_SBD3
111:5C;117:6C;405:2D M_SBD4
111:5C;117:6C;405:2D M_SBD5
111:5C;117:6C;405:2D M_SBD6
111:5C;117:6C;405:2D M_SBD7
111:5D;117:6D;404:3B M_BE0
117:6E;118:3B;400:3C M_BE1
111:5D;116:7E;404:3B M_SBA1
111:5D;116:7E;404:3B M_SBA2
111:5D;116:7E;404:3B M_SBA3
111:5D;116:7E;404:3B M_SBA4
111:5E;116:7E;404:3B M_SBA5
111:5E;116:7E;404:3B M_SBA6
111:5E;116:7E;404:3C M_SBA7
111:5E;116:7D;118:1B;404:3C M_SBA8
111:5E;116:7D;118:1B;404:3C M_SBA9
111:5E;116:7D;118:1B;404:3C M_SBA10
111:5E;116:7D;118:1B;404:3C M_SBA11
111:5E;116:7D;118:1B;404:3C M_SBA12
111:5E;116:7D;118:1B;404:3C M_SBA13
111:5E;116:7D;118:1B;404:3C M_SBA14
111:5E;116:7D;118:1B;400:3C M_SBA15
116:7D;400:3C M_SBA16
116:7C;118:3B;400:3C M_SBA17
116:7C;118:3B;400:3C M_SBA18
116:7C;118:3B;400:3C M_SBA20
116:7C;118:3B;400:3C M_SBA21
116:7C;118:3B M_SBA22
V46A00011200
M29W640FT70N6E
[V-M29LV640MT]
IC281
M_SBA1 25 A0
M_SBA2 24 A1
M_SBA3 23 A2
M_SBA4 22 A3
M_SBA5 21 A4
M_SBA6 20 A5
M_SBA7 19 A6
M_SBA8 18 A7
M_SBA9 8 A8
M_SBA10 7 A9
M_SBA11 6 A10
M_SBA12 5 A11
M_SBA13 4 A12
M_SBA14 3 A13
M_SBA15 2 A14
M_SBA16 1 A15
M_SBA17 48 A16
M_SBA18 17 A17
M_BE1 16 A18
M_SBA20 9 A19
M_SBA21 10 A20
M_SBA22 13 A21
RY/BY# 15
3.3V
R2804
4.7K (J)
M_CE0 26 CE#
M_OE 28 OE#
M_WE 11 WE#
M_GRESET_N 12 RESET#
47 BYTE#
WP#/ACC 14
R2801
4.7K (J)
GND
Q0 29 M_SBD0
Q1 31 M_SBD1
Q2 33 M_SBD2
Q3 35 M_SBD3
Q4 38 M_SBD4
Q5 40 M_SBD5
Q6 42 M_SBD6
Q7 44 M_SBD7
Q8 30
Q9 32
Q10 34
Q11 36
Q12 39
Q13 41
Q14 43
Q15/A-1 45 M_BE0
3.3V
37 [Vcc]
[GND] 27
[GND] 46
SBADD0と SHARE
C2801
100nF (K)
GND

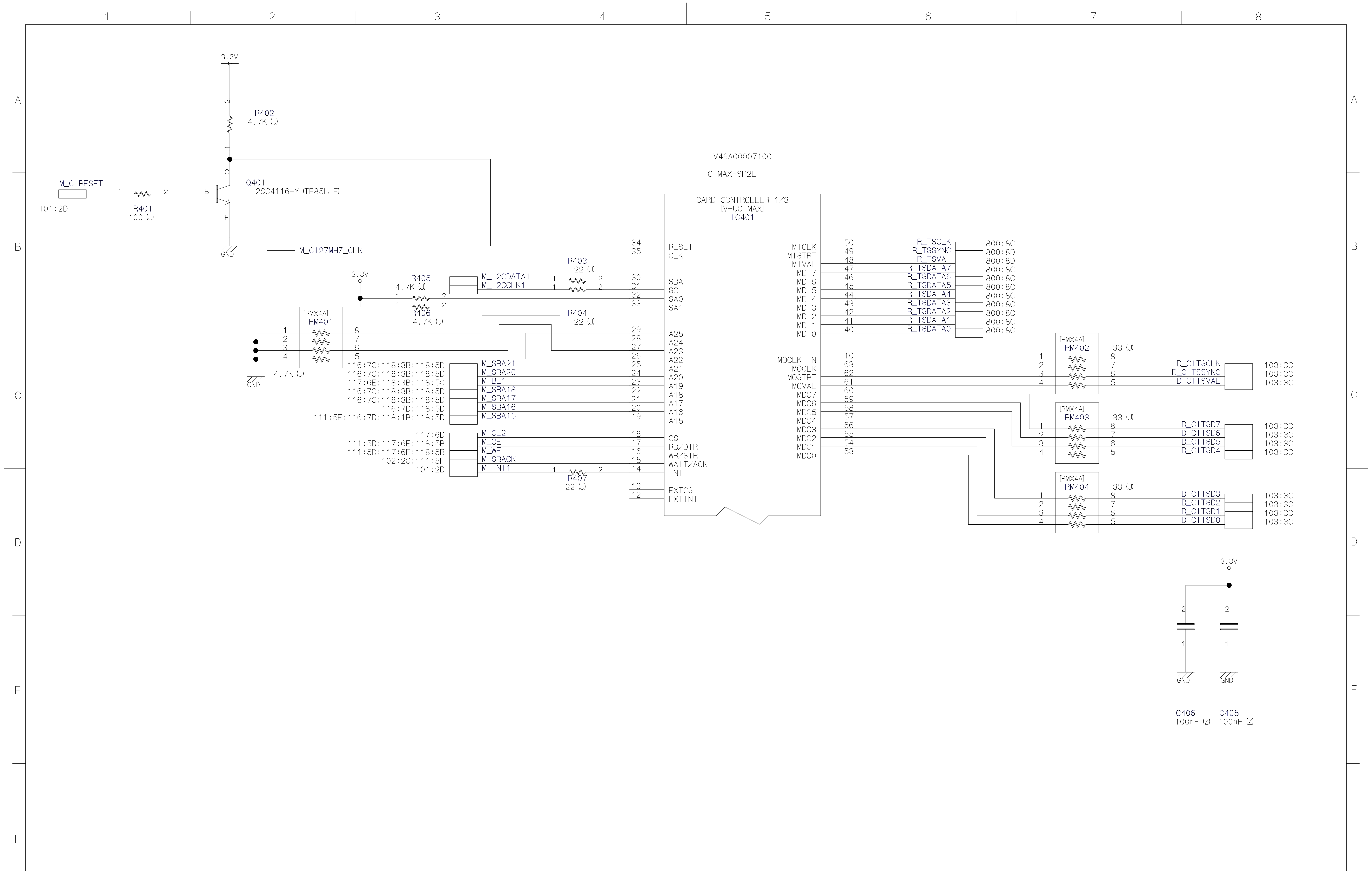
U05A DIGITAL [118] NOR FLASH (PE0284)


12V
FL305
0 (X)
C316
1nF (K)
C317
2.2uF (K)
C320
2.2uF (K)
AVGND
5V-Ana-Reg
FL306
0 (X)
5V_DAC
5V_MUTE
W303
0 (X)
C318
1nF (K)
C319
10uF (K)
W302
NOT MOUNT
C305
10uF (K)
R318
10K (F)
C311
560pF (K)
50V
R309
6.8K (J)
R310
18K (D)
C312
120pF (J)
R313
1K (J)
[TRP]
Q301
NOT MOUNT
FL303
NOT MOUNT
C314
10uF (K)
R314
220 (F)
PCM1754DBQR
DAC
[L1754A]
IC301
10K (J)
[RMX4A]
RM301
FMT
MUTE
DEMP
TEST
ZEROA
BCK
LRCK
DATA
SCK
VOUTL
VCOM
VOUTR
[VCC]
[NC]
[AGND]
[DGND]
104:6D
A_ABCK0
A_ALRCK0
A_ASD00
A_AOSCLK0
FL307
220ohm
FL308
220ohm
5V_DAC
C301
10uF (K)
10V
C302
100nF (K)
10V
C303
1nF (K)
50V
C304
NOT MOUNT
C306
10uF (K)
R301
R302
NOT MOUNT
GND
R303
4.7K (J)
V47GD0000998
OP-AMP
[L4580]
IC302
A-INPUT
A+INPUT
B-INPUT
B+INPUT
[V+]
[V-]
AOUTPUT
BOUTPUT
RC4580IPWR
R305
10K (F)
R306
10K (F)
C307
4.7uF (K)
10V
R304
4.7K (J)
C309
10nF (K)
16V
R317
100K (J)
R315
100K (J)
301:7C
M_LOUT_MUTE
301:7D
M_ROUT_MUTE
Analogue
Audio out
LOUT
ROUT
P212B
[SNO]
V22GD0000971
TO U03A AV_TERMINAL
[1] CONNECTOR (PE0398)
(PE0288)
P212B → P212A
C315
10uF (K)
R316
220 (F)
C313
120pF (J)
R307
6.8K (J)
R312
1K (J)
Q302
NOT MOUNT
FL304
NOT MOUNT
FL611
MPZ1608S600AT
C308
10uF (K)
W301
NOT MOUNT
R308
10K (F)
R311
18K (D)
C310
560pF (K)
50V
FL302
60ohm
FL301
60ohm
FMT
0 16-24bit I2S
1 16bit RJ
DEMH DE-EMPHASIS (44.1kHz)
0 OFF
1 ON
MUTE
0 Normal
1 Mute
W612
0 (X)
104:4D
B_CVBS
[FL09B]
FL615
NOT MOUNT
Q611
2SC4116-Y (TE85L, F)
R611
100 (F)
R612
4.7 (J)
C611
100nF (K)
C615
10uF (K)
C616
NOT MOUNT
FL610
MPZ1608S221AT
R613
330 (J)
R614
NOT MOUNT
FL612
MPZ1608S600AT
W611
0 (X)
[V-L1672]
IC611
PS
IN
SAG
OUT
[VCC]
[GND]
NOT MOUNT
C612
NOT MOUNT
C613
100nF (K)
C
A
[QZ]
D611
NOT MOUNT
R615
75 (J)
C614
220pF (J)
CVBS OUTPUT

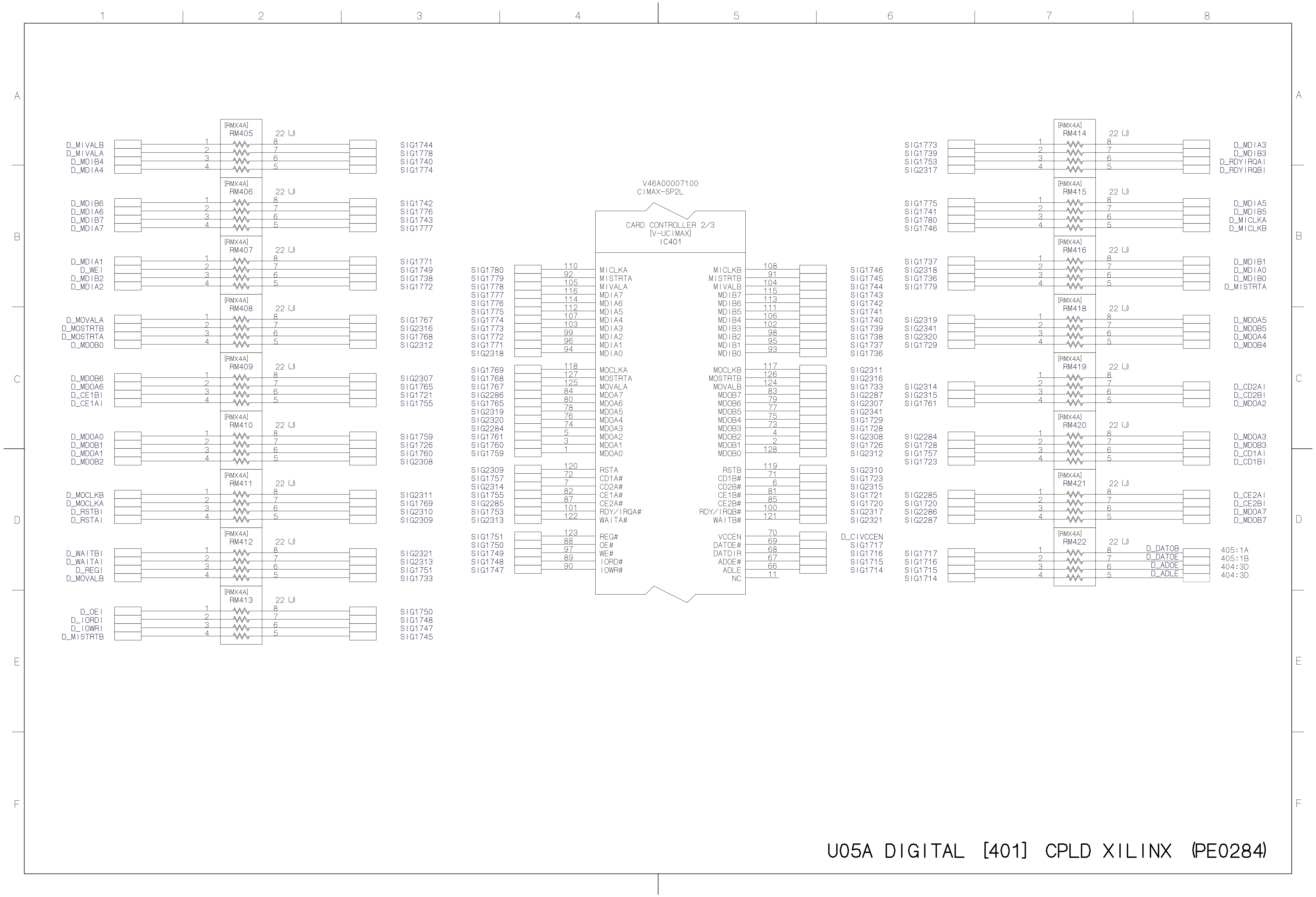
U05A DIGITAL [300] AUDIO DAC (ASP0) (PE0284)


1
2
3
4
5
6
7
8
A
B
C
D
E
F
MUTE
0 Normal
1 Mute
101:2D
M_AUDIO_MUTE
R352
1
2
4.7K (J)
R353
10K (J)
2
1
AVGND
5V_MUTE
R351
1
2
5.6K (J)
R350
1
2
1K (J)
C350
NOT MOUNT
2
1
AVGND
[TRNP]
Q351
C1
E1
E2
B1
B2
C2
C1
B1
E1
2NPN
[TRN2D]
Q350
C2
B2
E2
AVGND
M_LOUT_MUTE
300:7B
M_ROUT_MUTE
300:7C

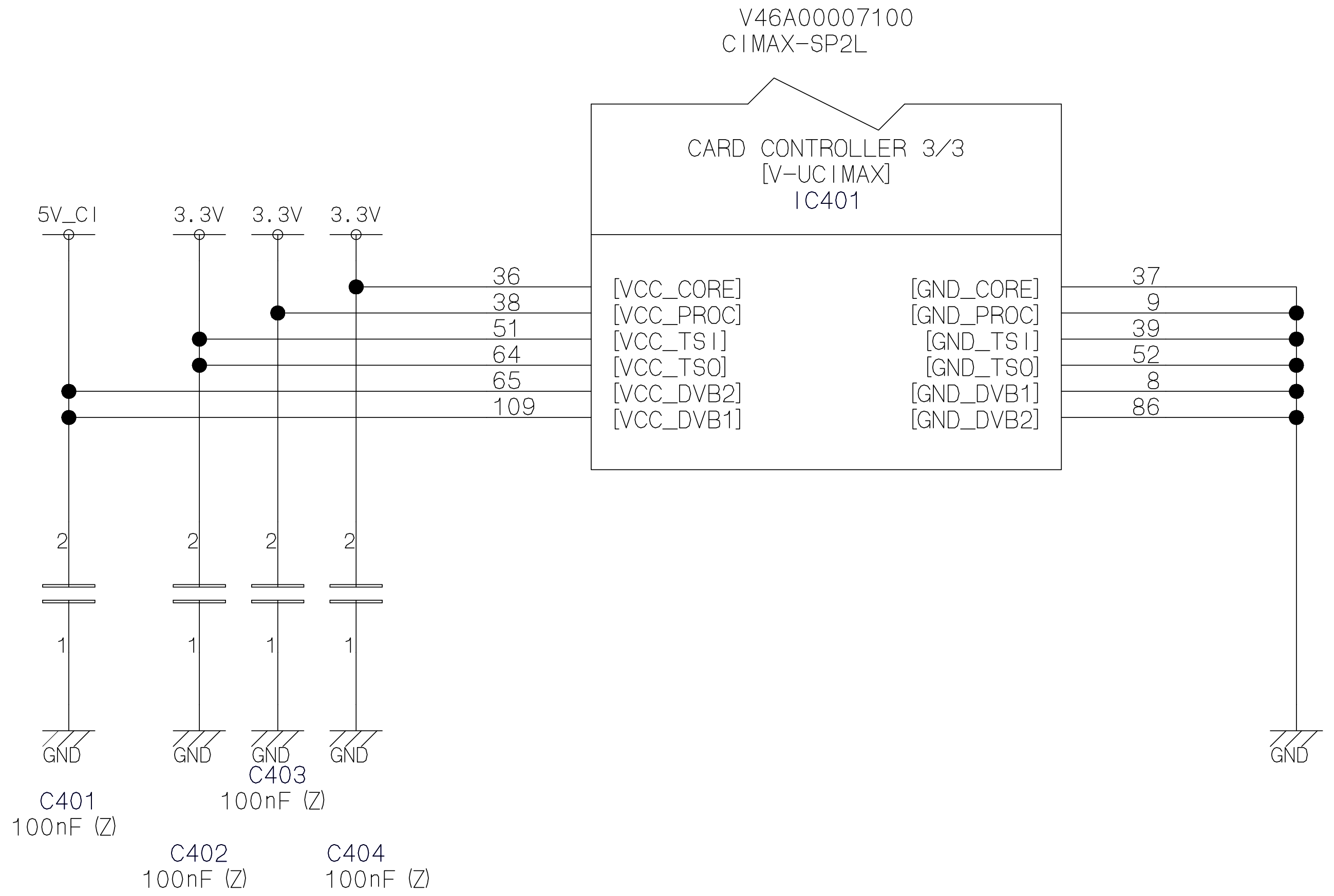
U05A DIGITAL [301] AUDIO MUTE (PE0284)


1
2
3
4
5
6
7
8
A
B
C
D
E
F
3.3V
R402
4.7K (J)
M_CIRESET
101:2D
R401
100 (J)
Q401
2SC4116-Y (TE85L, F)
B
C
E
GND
M_CI27MHZ_CLK
V46A00007100
CIMAX-SP2L
CARD CONTROLLER 1/3
[V-UCIMAX]
IC401
R403
22 (J)
M_I2CDATA1
M_I2CCLK1
R405
4.7K (J)
R406
4.7K (J)
R404
22 (J)
[RMX4A]
RM401
4.7K (J)
116:7C;118:3B;118:5D M_SBA21
116:7C;118:3B;118:5D M_SBA20
117:6E;118:3B;118:5C M_BE1
116:7C;118:3B;118:5D M_SBA18
116:7C;118:3B;118:5D M_SBA17
116:7D;118:5D M_SBA16
111:5E;116:7D;118:1B;118:5D M_SBA15
117:6D M_CE2
111:5D;117:6E;118:5B M_OE
111:5D;117:6E;118:5B M_WE
102:2C;111:5F M_SBACK
101:2D M_INT1
R407
22 (J)
34 RESET
35 CLK
30 SDA
31 SCL
32 SA0
33 SA1
29 A25
28 A24
27 A23
26 A22
25 A21
24 A20
23 A19
22 A18
21 A17
20 A16
19 A15
18 CS
17 RD/DIR
16 WR/STR
15 WAIT/ACK
14 INT
13 EXTCS
12 EXTINT
MICLK 50 R_TSCLK 800:8C
MISTRT 49 R_TSSYNC 800:8D
MIVAL 48 R_TSVAL 800:8D
MDI7 47 R_TSDATA7 800:8C
MDI6 46 R_TSDATA6 800:8C
MDI5 45 R_TSDATA5 800:8C
MDI4 44 R_TSDATA4 800:8C
MDI3 43 R_TSDATA3 800:8C
MDI2 42 R_TSDATA2 800:8C
MDI1 41 R_TSDATA1 800:8C
MDI0 40 R_TSDATA0 800:8C
MOCLK_IN 10
MOCLK 63
MOSTRT 62
MOVAL 61
MDO7 60
MDO6 59
MDO5 58
MDO4 57
MDO3 56
MDO2 55
MDO1 54
MDO0 53
[RMX4A]
RM402
33 (J)
D_CITSCLK 103:3C
D_CITSSYNC 103:3C
D_CITSVAL 103:3C
[RMX4A]
RM403
33 (J)
D_CITSD7 103:3C
D_CITSD6 103:3C
D_CITSD5 103:3C
D_CITSD4 103:3C
[RMX4A]
RM404
33 (J)
D_CITSD3 103:3C
D_CITSD2 103:3C
D_CITSD1 103:3C
D_CITSD0 103:3C
3.3V
GND
GND
C406
100nF (Z)
C405
100nF (Z)

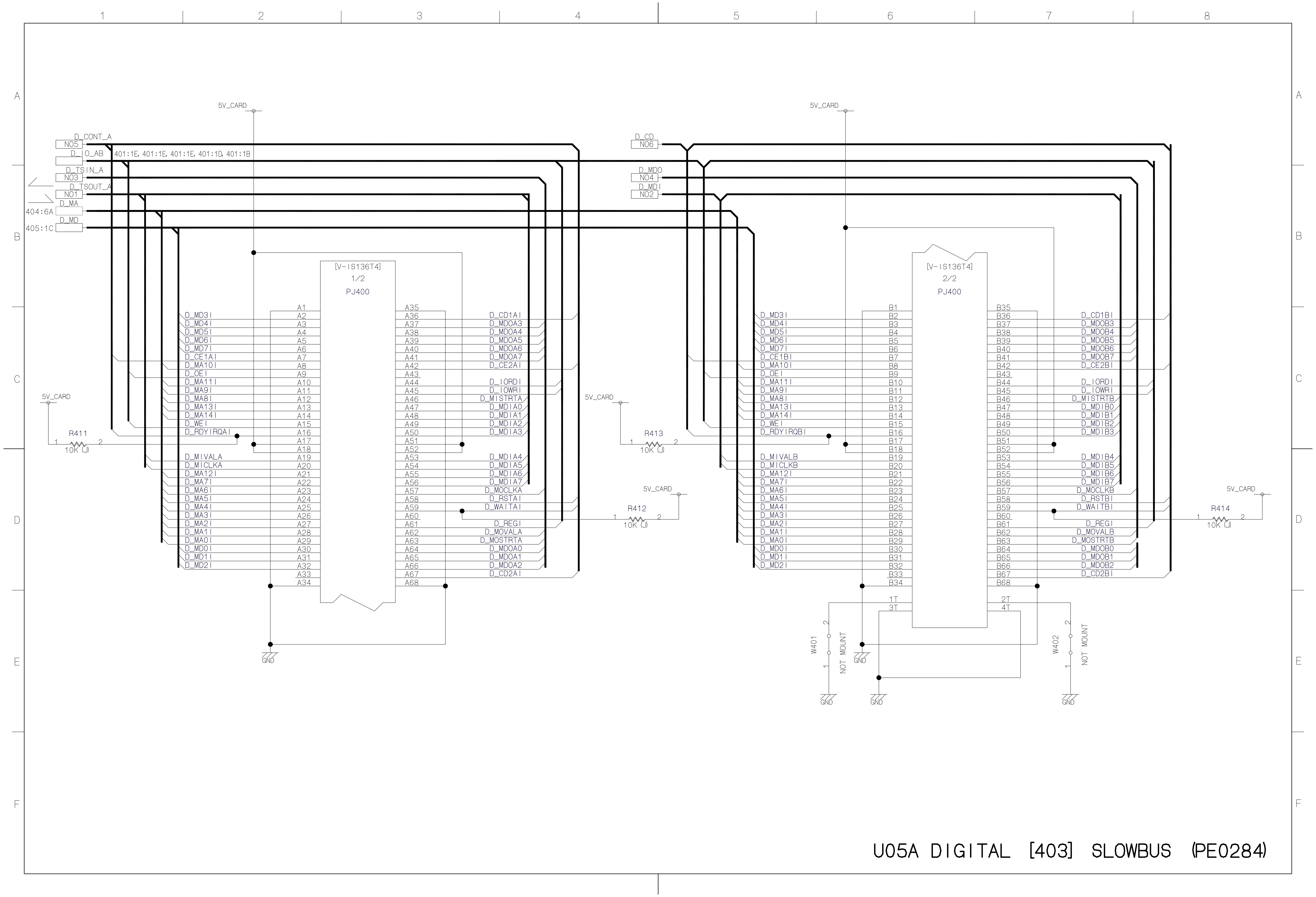
U05A DIGITAL [400] CIMAX (PE0284)


1
2
3
4
5
6
7
8
A
B
C
D
E
F
V46A00007100
CIMAX-SP2L
CARD CONTROLLER 2/3
[V-UCIMAX]
IC401
SIG1780 110 MICLKA
SIG1779 92 MISTRTA
SIG1778 105 MIVALA
SIG1777 116 MDIA7
SIG1776 114 MDIA6
SIG1775 112 MDIA5
SIG1774 107 MDIA4
SIG1773 103 MDIA3
SIG1772 99 MDIA2
SIG1771 96 MDIA1
SIG2318 94 MDIA0
SIG1769 118 MOCLKA
SIG1768 127 MOSTRTA
SIG1767 125 MOVALA
SIG2286 84 MDOA7
SIG1765 80 MDOA6
SIG2319 78 MDOA5
SIG2320 76 MDOA4
SIG2284 74 MDOA3
SIG1761 5 MDOA2
SIG1760 3 MDOA1
SIG1759 1 MDOA0
SIG2309 120 RSTA
SIG1757 72 CD1A#
SIG2314 7 CD2A#
SIG1755 82 CE1A#
SIG2285 87 CE2A#
SIG1753 101 RDY/IRQA#
SIG2313 122 WAITA#
SIG1751 123 REG#
SIG1750 88 OE#
SIG1749 97 WE#
SIG1748 89 IORD#
SIG1747 90 IOWR#
MICLKB 108 SIG1746
MISTRTB 91 SIG1745
MIVALB 104 SIG1744
MDIB7 115 SIG1743
MDIB6 113 SIG1742
MDIB5 111 SIG1741
MDIB4 106 SIG1740
MDIB3 102 SIG1739
MDIB2 98 SIG1738
MDIB1 95 SIG1737
MDIB0 93 SIG1736
MOCLKB 117 SIG2311
MOSTRTB 126 SIG2316
MOVALB 124 SIG1733
MDOB7 83 SIG2287
MDOB6 79 SIG2307
MDOB5 77 SIG2341
MDOB4 75 SIG1729
MDOB3 73 SIG1728
MDOB2 4 SIG2308
MDOB1 2 SIG1726
MDOB0 128 SIG2312
RSTB 119 SIG2310
CD1B# 71 SIG1723
CD2B# 6 SIG2315
CE1B# 81 SIG1721
CE2B# 85 SIG1720
RDY/IRQB# 100 SIG2317
WAITB# 121 SIG2321
VCCEN 70 D_CIVCCEN
DATOE# 69 SIG1717
DATDIR 68 SIG1716
ADOE# 67 SIG1715
ADLE 66 SIG1714
NC 11
[RMX4A] RM405 22 (J)
D_MIVALB 1 8 SIG1744
D_MIVALA 2 7 SIG1778
D_MDIB4 3 6 SIG1740
D_MDIA4 4 5 SIG1774
[RMX4A] RM406 22 (J)
D_MDIB6 1 8 SIG1742
D_MDIA6 2 7 SIG1776
D_MDIB7 3 6 SIG1743
D_MDIA7 4 5 SIG1777
[RMX4A] RM407 22 (J)
D_MDIA1 1 8 SIG1771
D_WEI 2 7 SIG1749
D_MDIB2 3 6 SIG1738
D_MDIA2 4 5 SIG1772
[RMX4A] RM408 22 (J)
D_MOVALA 1 8 SIG1767
D_MOSTRTB 2 7 SIG2316
D_MOSTRTA 3 6 SIG1768
D_MDOB0 4 5 SIG2312
[RMX4A] RM409 22 (J)
D_MDOB6 1 8 SIG2307
D_MDOA6 2 7 SIG1765
D_CE1BI 3 6 SIG1721
D_CE1AI 4 5 SIG1755
[RMX4A] RM410 22 (J)
D_MDOA0 1 8 SIG1759
D_MDOB1 2 7 SIG1726
D_MDOA1 3 6 SIG1760
D_MDOB2 4 5 SIG2308
[RMX4A] RM411 22 (J)
D_MOCLKB 1 8 SIG2311
D_MOCLKA 2 7 SIG1769
D_RSTBI 3 6 SIG2310
D_RSTAI 4 5 SIG2309
[RMX4A] RM412 22 (J)
D_WAITBI 1 8 SIG2321
D_WAITAI 2 7 SIG2313
D_REGI 3 6 SIG1751
D_MOVALB 4 5 SIG1733
[RMX4A] RM413 22 (J)
D_OEI 1 8 SIG1750
D_IORDI 2 7 SIG1748
D_IOWRI 3 6 SIG1747
D_MISTRTB 4 5 SIG1745
[RMX4A] RM414 22 (J)
SIG1773 1 8 D_MDIA3
SIG1739 2 7 D_MDIB3
SIG1753 3 6 D_RDYIRQAI
SIG2317 4 5 D_RDYIRQBI
[RMX4A] RM415 22 (J)
SIG1775 1 8 D_MDIA5
SIG1741 2 7 D_MDIB5
SIG1780 3 6 D_MICLKA
SIG1746 4 5 D_MICLKB
[RMX4A] RM416 22 (J)
SIG1737 1 8 D_MDIB1
SIG2318 2 7 D_MDIA0
SIG1736 3 6 D_MDIB0
SIG1779 4 5 D_MISTRTA
[RMX4A] RM418 22 (J)
SIG2319 1 8 D_MDOA5
SIG2341 2 7 D_MDOB5
SIG2320 3 6 D_MDOA4
SIG1729 4 5 D_MDOB4
[RMX4A] RM419 22 (J)
1 8
SIG2314 2 7 D_CD2AI
SIG2315 3 6 D_CD2BI
SIG1761 4 5 D_MDOA2
[RMX4A] RM420 22 (J)
SIG2284 1 8 D_MDOA3
SIG1728 2 7 D_MDOB3
SIG1757 3 6 D_CD1AI
SIG1723 4 5 D_CD1BI
[RMX4A] RM421 22 (J)
SIG2285 1 8 D_CE2AI
SIG1720 2 7 D_CE2BI
SIG2286 3 6 D_MDOA7
SIG2287 4 5 D_MDOB7
[RMX4A] RM422 22 (J)
SIG1717 1 8 D_DATOB 405:1A
SIG1716 2 7 D_DATOE 405:1B
SIG1715 3 6 D_ADOE 404:3D
SIG1714 4 5 D_ADLE 404:3D

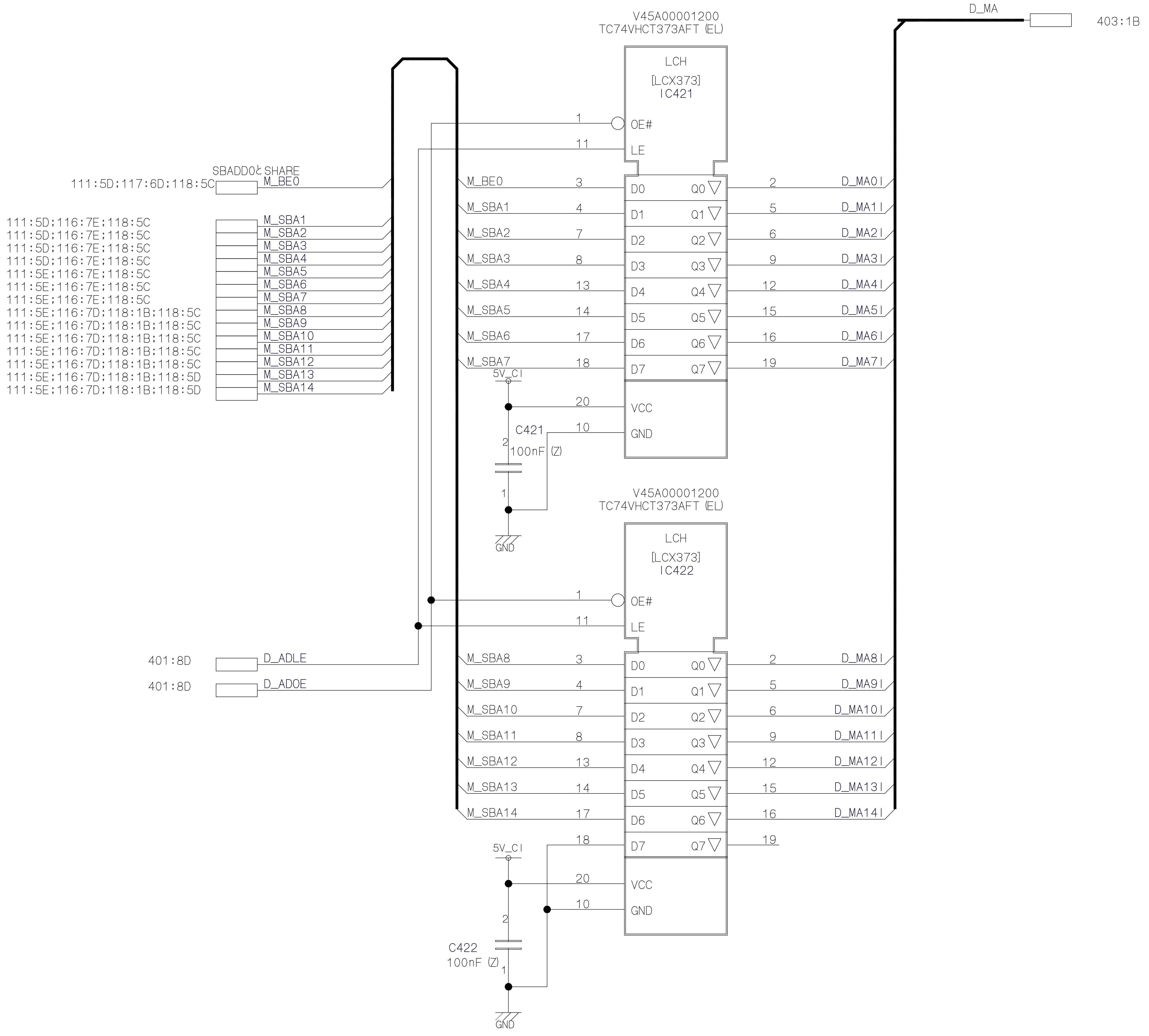
U05A DIGITAL [401] CPLD XILINX (PE0284)


V46A00007100
CIMAX-SP2L
CARD CONTROLLER 3/3
[V-UCIMAX]
IC401
5V_CI
3.3V
3.3V
3.3V
36
38
51
64
65
109
[VCC_CORE]
[VCC_PROC]
[VCC_TSI]
[VCC_TSO]
[VCC_DVB2]
[VCC_DVB1]
[GND_CORE]
[GND_PROC]
[GND_TSI]
[GND_TSO]
[GND_DVB1]
[GND_DVB2]
37
9
39
52
8
86
2
2
2
2
1
1
1
1
GND
GND
GND
GND
GND
C401
100nF (Z)
C402
100nF (Z)
C403
100nF (Z)
C404
100nF (Z)

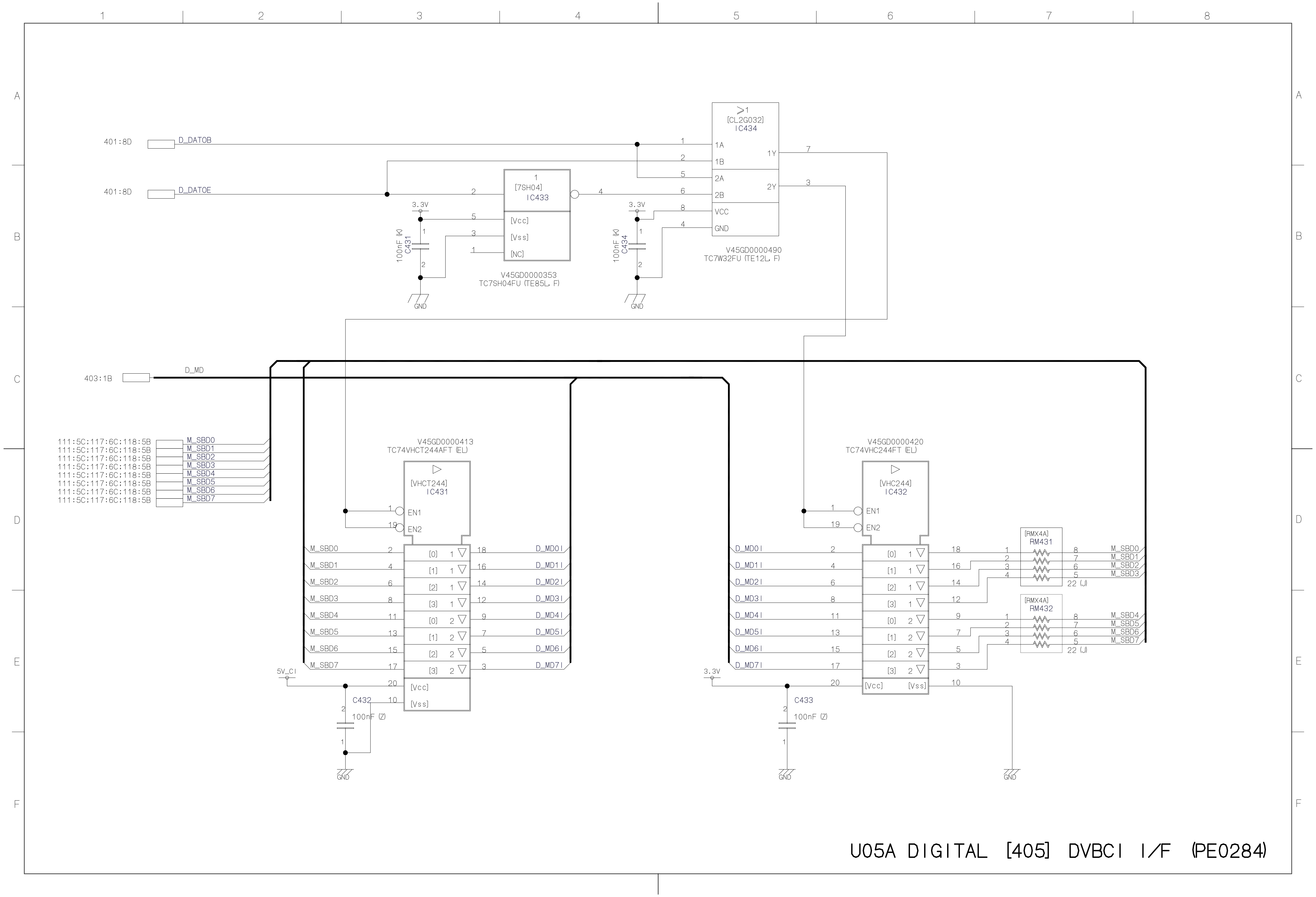
U05A DIGITAL [402] CPLD XILINX (PE0284)


1
2
3
4
5
6
7
8
A
B
C
D
E
F
5V_CARD
D_CONT_A
NO5
D_IO_AB
401:1E, 401:1E, 401:1E, 401:1D, 401:1B
D_TSIN_A
NO3
D_TSOUT_A
NO1
D_MA
404:6A
D_MD
405:1C
[V-IS136T4]
1/2
PJ400
A1
A2
A3
A4
A5
A6
A7
A8
A9
A10
A11
A12
A13
A14
A15
A16
A17
A18
A19
A20
A21
A22
A23
A24
A25
A26
A27
A28
A29
A30
A31
A32
A33
A34
D_MD3I
D_MD4I
D_MD5I
D_MD6I
D_MD7I
D_CE1AI
D_MA10I
D_OEI
D_MA11I
D_MA9I
D_MA8I
D_MA13I
D_MA14I
D_WEI
D_RDYIRQAI
D_MIVALA
D_MICLKA
D_MA12I
D_MA7I
D_MA6I
D_MA5I
D_MA4I
D_MA3I
D_MA2I
D_MA1I
D_MA0I
D_MD0I
D_MD1I
D_MD2I
A35
A36
A37
A38
A39
A40
A41
A42
A43
A44
A45
A46
A47
A48
A49
A50
A51
A52
A53
A54
A55
A56
A57
A58
A59
A60
A61
A62
A63
A64
A65
A66
A67
A68
D_CD1AI
D_MDOA3
D_MDOA4
D_MDOA5
D_MDOA6
D_MDOA7
D_CE2AI
D_IORDI
D_IOWRI
D_MISTRTA
D_MDIA0
D_MDIA1
D_MDIA2
D_MDIA3
D_MDIA4
D_MDIA5
D_MDIA6
D_MDIA7
D_MOCLKA
D_RSTAI
D_WAITAI
D_REGI
D_MOVALA
D_MOSTRTA
D_MDOA0
D_MDOA1
D_MDOA2
D_CD2AI
5V_CARD
R411
1
2
10K (J)
5V_CARD
R412
1
2
10K (J)
GND
D_CD
NO6
D_MDO
NO4
D_MDI
NO2
[V-IS136T4]
2/2
PJ400
B1
B2
B3
B4
B5
B6
B7
B8
B9
B10
B11
B12
B13
B14
B15
B16
B17
B18
B19
B20
B21
B22
B23
B24
B25
B26
B27
B28
B29
B30
B31
B32
B33
B34
D_MD3I
D_MD4I
D_MD5I
D_MD6I
D_MD7I
D_CE1BI
D_MA10I
D_OEI
D_MA11I
D_MA9I
D_MA8I
D_MA13I
D_MA14I
D_WEI
D_RDYIRQBI
D_MIVALB
D_MICLKB
D_MA12I
D_MA7I
D_MA6I
D_MA5I
D_MA4I
D_MA3I
D_MA2I
D_MA1I
D_MA0I
D_MD0I
D_MD1I
D_MD2I
B35
B36
B37
B38
B39
B40
B41
B42
B43
B44
B45
B46
B47
B48
B49
B50
B51
B52
B53
B54
B55
B56
B57
B58
B59
B60
B61
B62
B63
B64
B65
B66
B67
B68
D_CD1BI
D_MDOB3
D_MDOB4
D_MDOB5
D_MDOB6
D_MDOB7
D_CE2BI
D_IORDI
D_IOWRI
D_MISTRTB
D_MDIB0
D_MDIB1
D_MDIB2
D_MDIB3
D_MDIB4
D_MDIB5
D_MDIB6
D_MDIB7
D_MOCLKB
D_RSTBI
D_WAITBI
D_REGI
D_MOVALB
D_MOSTRTB
D_MDOB0
D_MDOB1
D_MDOB2
D_CD2BI
1T
3T
2T
4T
5V_CARD
R413
1
2
10K (J)
5V_CARD
R414
1
2
10K (J)
W401
2
1
NOT MOUNT
W402
2
1
NOT MOUNT
GND
GND
GND
GND

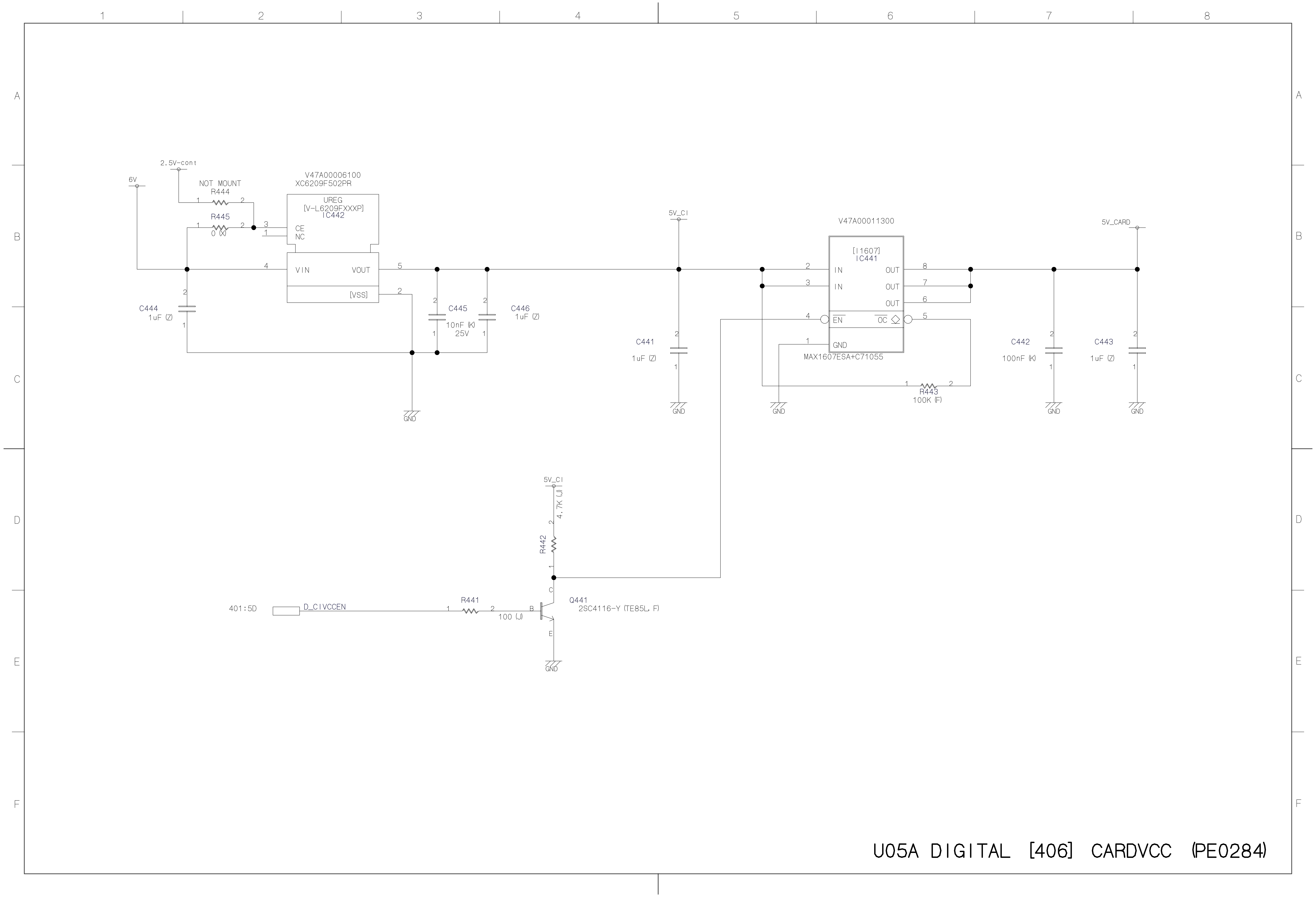
U05A DIGITAL [403] SLOWBUS (PE0284)


V45A00001200
TC74VHCT373AFT (EL)
LCH
[LCX373]
IC421
OE#
LE
D0
D1
D2
D3
D4
D5
D6
D7
Q0
Q1
Q2
Q3
Q4
Q5
Q6
Q7
VCC
GND
SBADD0と SHARE
111:5D;117:6D;118:5C
M_BE0
111:5D;116:7E;118:5C
111:5D;116:7E;118:5C
111:5D;116:7E;118:5C
111:5D;116:7E;118:5C
111:5E;116:7E;118:5C
111:5E;116:7E;118:5C
111:5E;116:7E;118:5C
111:5E;116:7D;118:1B;118:5C
111:5E;116:7D;118:1B;118:5C
111:5E;116:7D;118:1B;118:5C
111:5E;116:7D;118:1B;118:5C
111:5E;116:7D;118:1B;118:5C
111:5E;116:7D;118:1B;118:5D
111:5E;116:7D;118:1B;118:5D
M_SBA1
M_SBA2
M_SBA3
M_SBA4
M_SBA5
M_SBA6
M_SBA7
M_SBA8
M_SBA9
M_SBA10
M_SBA11
M_SBA12
M_SBA13
M_SBA14
D_MA
403:1B
D_MA0
D_MA1
D_MA2
D_MA3
D_MA4
D_MA5
D_MA6
D_MA7
5V_C
C421
100nF (Z)
GND
V45A00001200
TC74VHCT373AFT (EL)
LCH
[LCX373]
IC422
401:8D
D_ADLE
401:8D
D_ADOE
D_MA8
D_MA9
D_MA10
D_MA11
D_MA12
D_MA13
D_MA14
5V_C
C422
100nF (Z)
GND

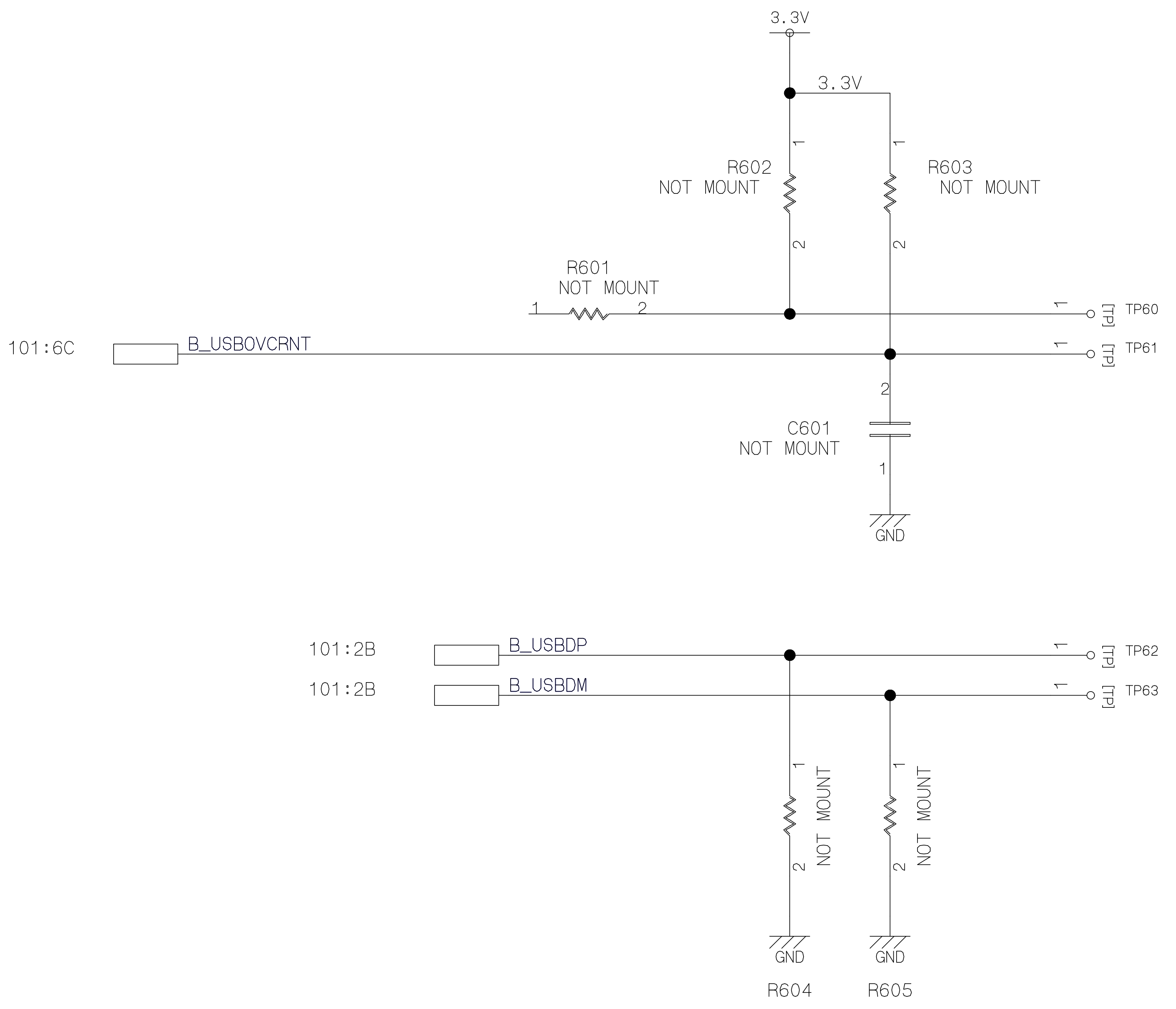
U05A DIGITAL [404] PODVPP (PE0284)


401:8D
D_DATOB
401:8D
D_DATOE
403:1B
D_MD
111:5C:117:6C:118:5B
M_SBD0
M_SBD1
M_SBD2
M_SBD3
M_SBD4
M_SBD5
M_SBD6
M_SBD7
[7SH04]
IC433
[Vcc]
[Vss]
[NC]
V45GD0000353
TC7SH04FU (TE85L, F)
3.3V
C431
100nF (K)
C434
GND
≥1
[CL2G032]
IC434
1A
1B
2A
2B
VCC
GND
1Y
2Y
V45GD0000490
TC7W32FU (TE12L, F)
V45GD0000413
TC74VHCT244AFT (EL)
[VHCT244]
IC431
EN1
EN2
D_MD0I
D_MD1I
D_MD2I
D_MD3I
D_MD4I
D_MD5I
D_MD6I
D_MD7I
5V_CI
C432
100nF (Z)
[Vcc]
[Vss]
V45GD0000420
TC74VHC244FT (EL)
[VHC244]
IC432
C433
100nF (Z)
[RMX4A]
RM431
22 (J)
[RMX4A]
RM432
22 (J)

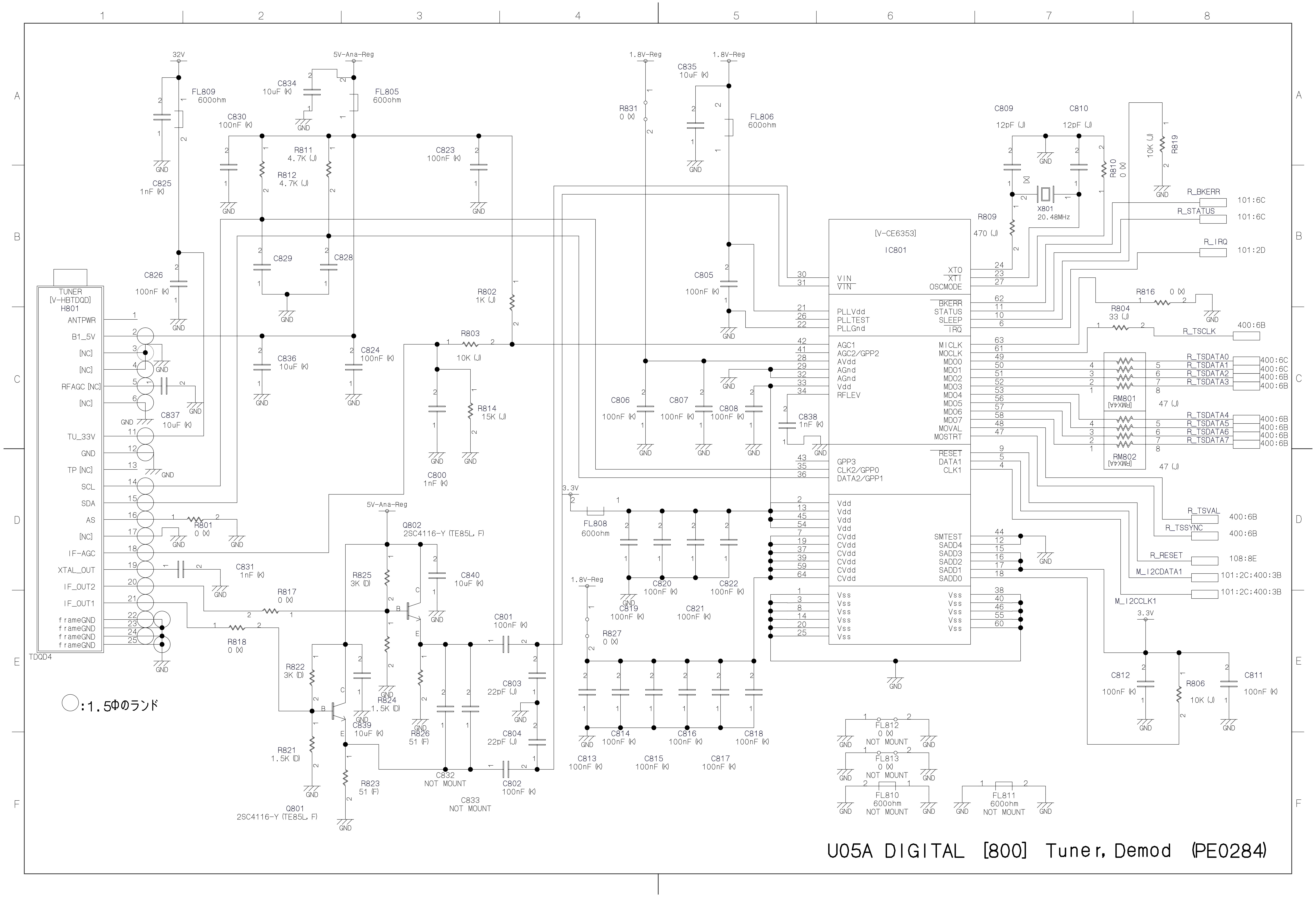
U05A DIGITAL [405] DVBCI I/F (PE0284)


1
2
3
4
5
6
7
8
A
B
C
D
E
F
6V
2.5V-cont
NOT MOUNT
R444
1
2
R445
0 (X)
V47A00006100
XC6209F502PR
UREG
[V-L6209FXXXP]
IC442
3
CE
1
NC
4
VIN
VOUT
5
[VSS]
2
C444
1uF (Z)
C445
10nF (K)
25V
C446
1uF (Z)
GND
5V_CI
C441
1uF (Z)
V47A00011300
[I1607]
IC441
IN
IN
OUT
OUT
OUT
8
7
6
$\overline{EN}$
$\overline{OC}$
GND
MAX1607ESA+C71055
R443
100K (F)
5V_CARD
C442
100nF (K)
C443
1uF (Z)
R442
4.7K (J)
Q441
2SC4116-Y (TE85L, F)
C
B
E
R441
100 (J)
401:5D
D_CIVCCEN

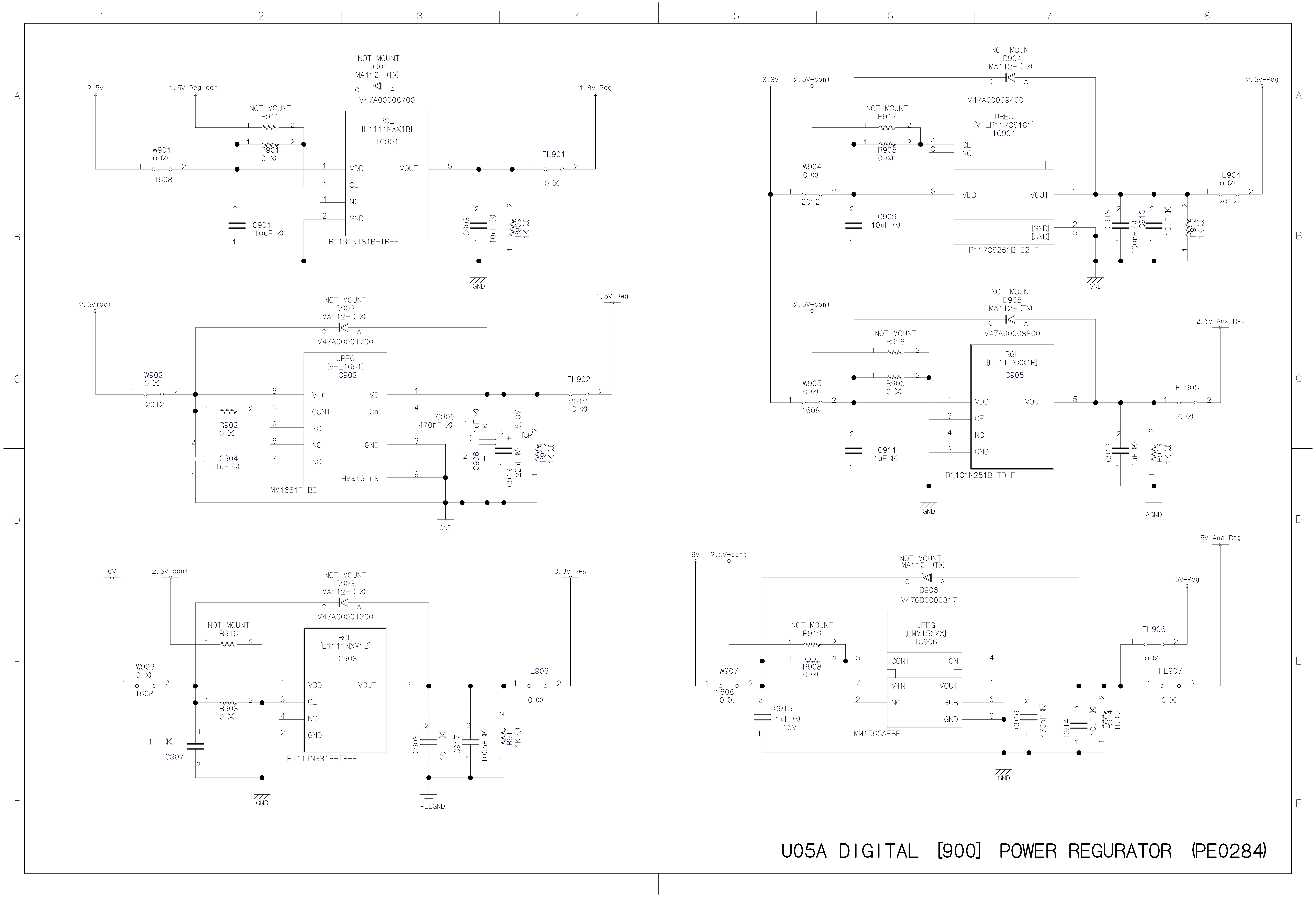
U05A DIGITAL [406] CARDVCC (PE0284)


3.3V
3.3V
R602
NOT MOUNT
R603
NOT MOUNT
R601
NOT MOUNT
TP60
TP61
101:6C
B_USBOVCRNT
C601
NOT MOUNT
GND
101:2B
B_USBDP
101:2B
B_USBDM
TP62
TP63
NOT MOUNT
NOT MOUNT
GND
GND
R604
R605

U05A DIGITAL [600] USB (PE0284)

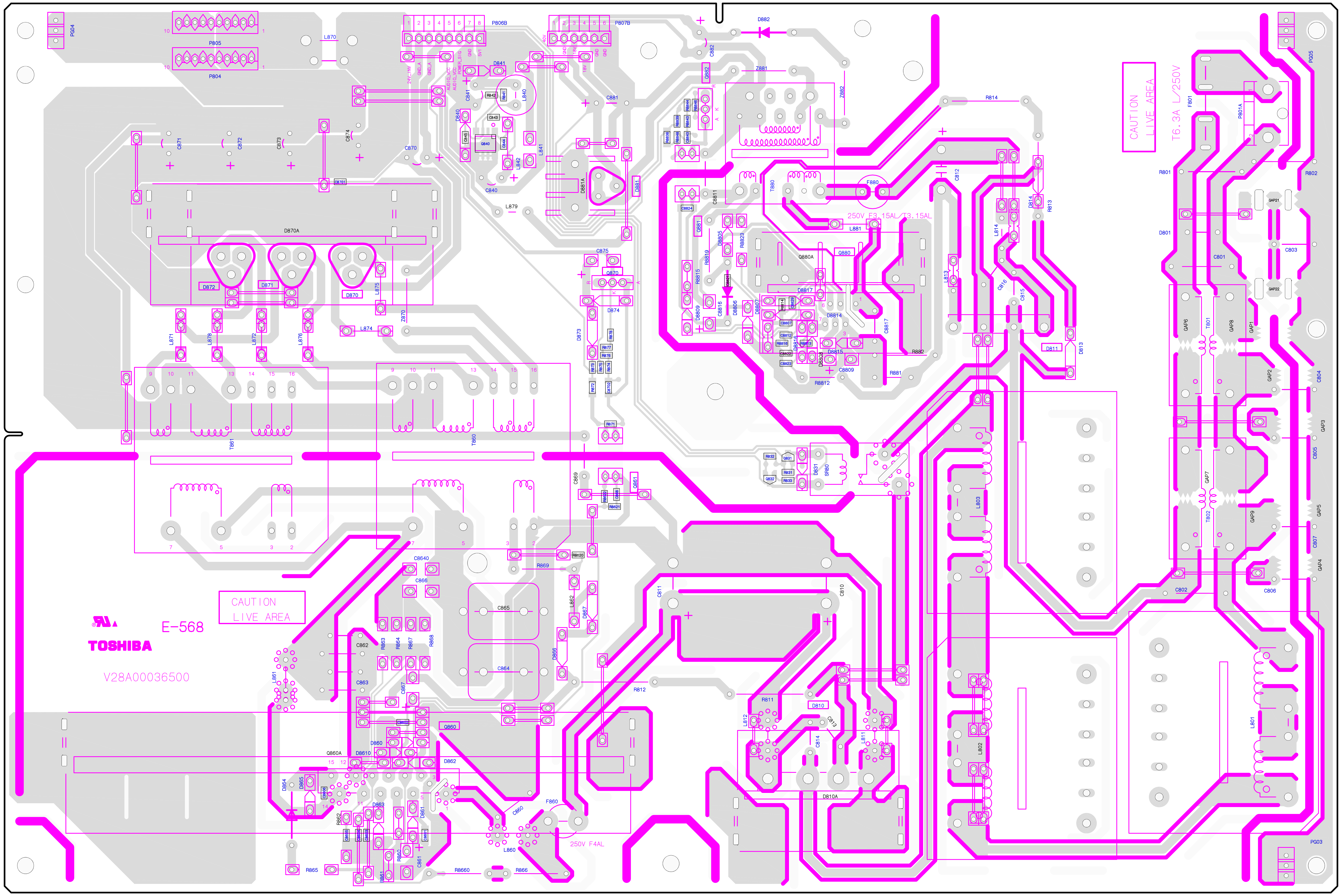

1
2
3
4
5
6
7
8
A
B
C
D
E
F
32V
5V-Ana-Reg
1.8V-Reg
3.3V
TUNER
[V-HBTDQD]
H801
ANTPWR
B1_5V
[NC]
RFAGC [NC]
TU_33V
GND
TP [NC]
SCL
SDA
AS
IF-AGC
XTAL_OUT
IF_OUT2
IF_OUT1
frameGND
TDQD4
FL809 600ohm
FL805 600ohm
FL806 600ohm
FL808 600ohm
C825 1nF (K)
C826 100nF (K)
C830 100nF (K)
C834 10uF (K)
C823 100nF (K)
C835 10uF (K)
C836 10uF (K)
C824 100nF (K)
C837 10uF (K)
C800 1nF (K)
C805 100nF (K)
C806 100nF (K)
C807 100nF (K)
C808 100nF (K)
C838 1nF (K)
C809 12pF (J)
C810 12pF (J)
X801 20.48MHz
R811 4.7K (J)
R812 4.7K (J)
R802 1K (J)
R803 10K (J)
R814 15K (J)
R831 0 (X)
R801 0 (X)
R809 470 (J)
R810 0 (X)
R819 10K (J)
R816 0 (X)
R804 33 (J)
RM801 [RMX4A] 47 (J)
RM802 [RMX4A] 47 (J)
[V-CE6353]
IC801
VIN
VIN
PLLVdd
PLLTEST
PLLGnd
AGC1
AGC2/GPP2
AVdd
AGnd
AGnd
Vdd
RFLEV
GPP3
CLK2/GPP0
DATA2/GPP1
CVdd
Vss
XTO
XTI
OSCMODE
BKERR
STATUS
SLEEP
IRQ
MICLK
MOCLK
MDO0
MDO1
MDO2
MDO3
MDO4
MDO5
MDO6
MDO7
MOVAL
MOSTRT
RESET
DATA1
CLK1
SMTEST
SADD4
SADD3
SADD2
SADD1
SADD0
R_BKERR 101:6C
R_STATUS 101:6B
R_IRQ 101:2D
R_TSCLK 400:6B
R_TSDATA0 400:6C
R_TSDATA1 400:6C
R_TSDATA2 400:6B
R_TSDATA3 400:6B
R_TSDATA4 400:6B
R_TSDATA5 400:6B
R_TSDATA6 400:6B
R_TSDATA7 400:6B
R_TSVAL 400:6B
R_TSYNC 400:6B
R_RESET 108:8E
M_I2CDATA1 101:2C:400:3B
M_I2CCLK1 101:2C:400:3B
C831 1nF (K)
R817 0 (X)
R818 0 (X)
Q802 2SC4116-Y (TE85L, F)
R825 3K (D)
C840 10uF (K)
R824 1.5K (D)
R826 51 (F)
C839 10uF (K)
R822 3K (D)
R821 1.5K (D)
R823 51 (F)
Q801 2SC4116-Y (TE85L, F)
C801 100nF (K)
C803 22pF (J)
C804 22pF (J)
C802 100nF (K)
C832 NOT MOUNT
C833 NOT MOUNT
R827 0 (X)
C819 100nF (K)
C820 100nF (K)
C821 100nF (K)
C822 100nF (K)
C813 100nF (K)
C814 100nF (K)
C815 100nF (K)
C816 100nF (K)
C817 100nF (K)
C818 100nF (K)
C812 100nF (K)
R806 10K (J)
C811 100nF (K)
FL812 0 (X) NOT MOUNT
FL813 0 (X) NOT MOUNT
FL810 600ohm NOT MOUNT
FL811 600ohm NOT MOUNT
GND
○:1.5Φのランド

U05A DIGITAL [800] Tuner, Demod (PE0284)

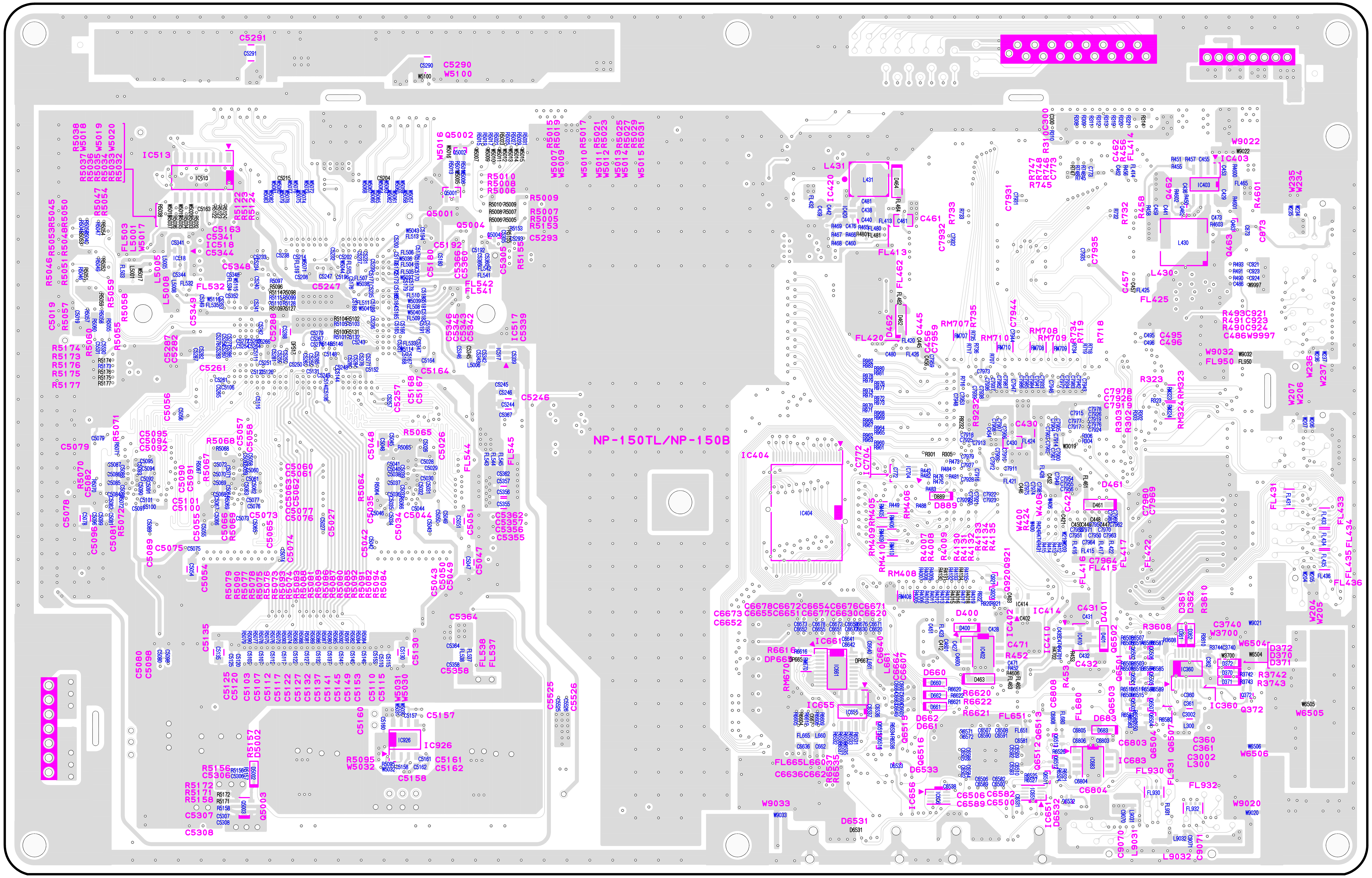

1
2
3
4
5
6
7
8
A
B
C
D
E
F
NOT MOUNT
D901
MA112- (TX)
V47A00008700
2.5V
1.5V-Reg-cont
1.8V-Reg
NOT MOUNT
R915
R901
0 (X)
RGL
[L1111NXX1B]
IC901
VDD
CE
NC
GND
VOUT
W901
0 (X)
1608
FL901
0 (X)
C901
10uF (K)
C903
10uF (K)
R909
1K (J)
R1131N181B-TR-F
GND
NOT MOUNT
D902
MA112- (TX)
V47A00001700
2.5Vroot
1.5V-Reg
UREG
[V-L1661]
IC902
Vin
CONT
NC
HeatSink
VO
Cn
GND
W902
0 (X)
2012
R902
0 (X)
C904
1uF (K)
C905
470pF (K)
C906
1uF (K)
C913
22uF (M)
6.3V
[CP]
R910
1K (J)
FL902
2012
0 (X)
MM1661FHBE
GND
NOT MOUNT
D903
MA112- (TX)
V47A00001300
6V
2.5V-cont
3.3V-Reg
NOT MOUNT
R916
RGL
[L1111NXX1B]
IC903
W903
0 (X)
1608
R903
0 (X)
1uF (K)
C907
C908
10uF (K)
C917
100nF (K)
R911
1K (J)
FL903
0 (X)
R1111N331B-TR-F
GND
PLLGND
NOT MOUNT
D904
MA112- (TX)
V47A00009400
3.3V
2.5V-cont
2.5V-Reg
NOT MOUNT
R917
R905
0 (X)
UREG
[V-LR1173S181]
IC904
W904
0 (X)
2012
C909
10uF (K)
[GND]
[GND]
R1173S251B-E2-F
C918
100nF (K)
C910
10uF (K)
R912
1K (J)
FL904
0 (X)
2012
GND
NOT MOUNT
D905
MA112- (TX)
V47A00008800
2.5V-cont
2.5V-Ana-Reg
NOT MOUNT
R918
R906
0 (X)
RGL
[L1111NXX1B]
IC905
W905
0 (X)
1608
C911
1uF (K)
R1131N251B-TR-F
C912
1uF (K)
R913
1K (J)
FL905
0 (X)
GND
AGND
6V
2.5V-cont
5V-Ana-Reg
5V-Reg
NOT MOUNT
MA112- (TX)
D906
V47GD0000817
UREG
[LMM156XX]
IC906
CONT
CN
VIN
NC
VOUT
SUB
GND
NOT MOUNT
R919
R908
0 (X)
W907
1608
0 (X)
C915
1uF (K)
16V
MM1565AFBE
C916
470pF (K)
C914
10uF (K)
R914
1K (J)
FL906
0 (X)
FL907
0 (X)
GND

U05A DIGITAL [900] POWER REGURATOR (PE0284)

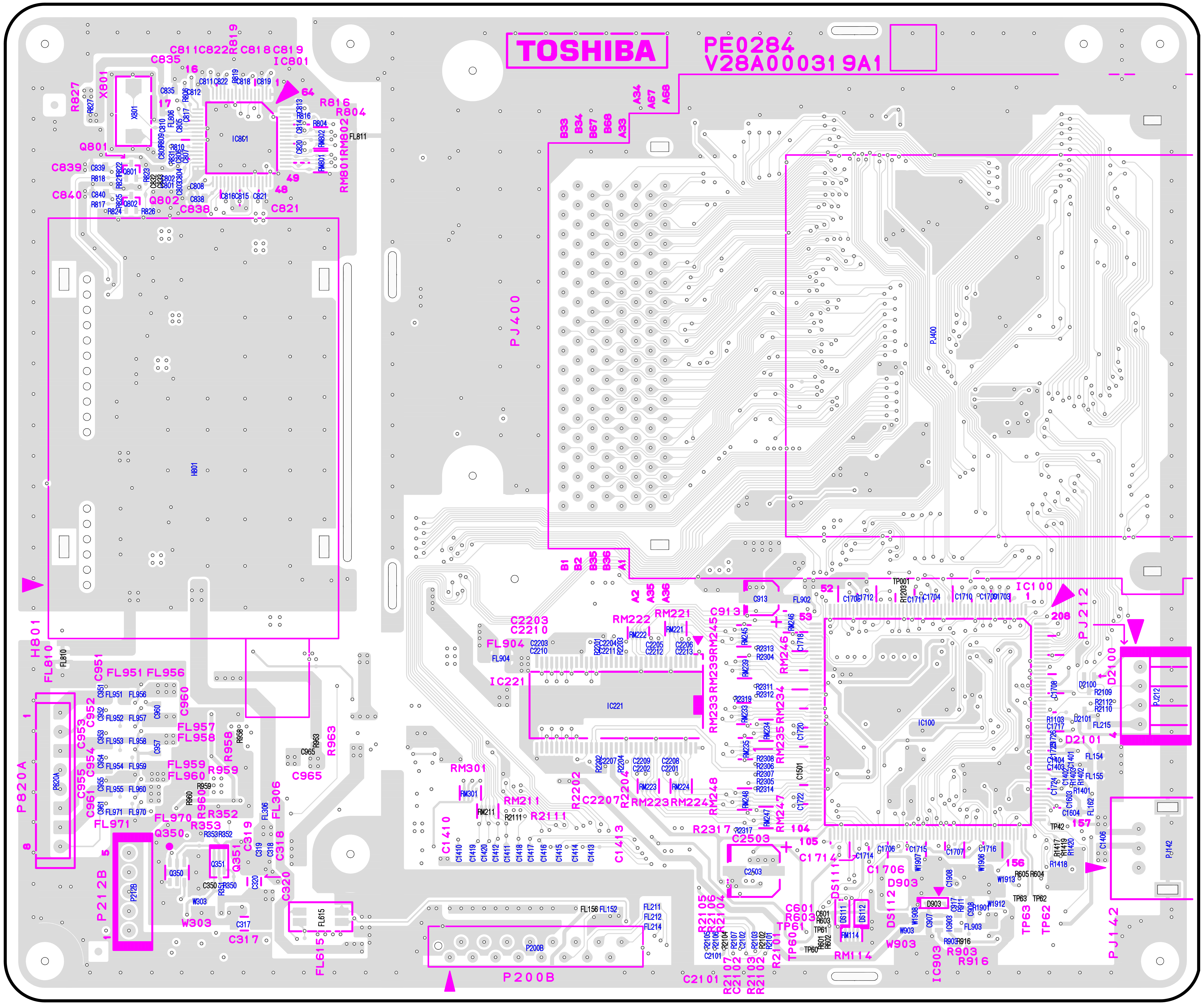
U01A POWER [Bottom] (PE0282)


CAUTION
LIVE AREA
T6.3A L/250V
250V F3.15AL/T3.15AL
E-568
TOSHIBA
V28A00036500
CAUTION
LIVE AREA
250V F4AL
P805
P804
P806B
P807B
D870A
T861
T860
D810A
Q880A
Q860A
L801
F801
F880
F860
PG03
PG04
PG05

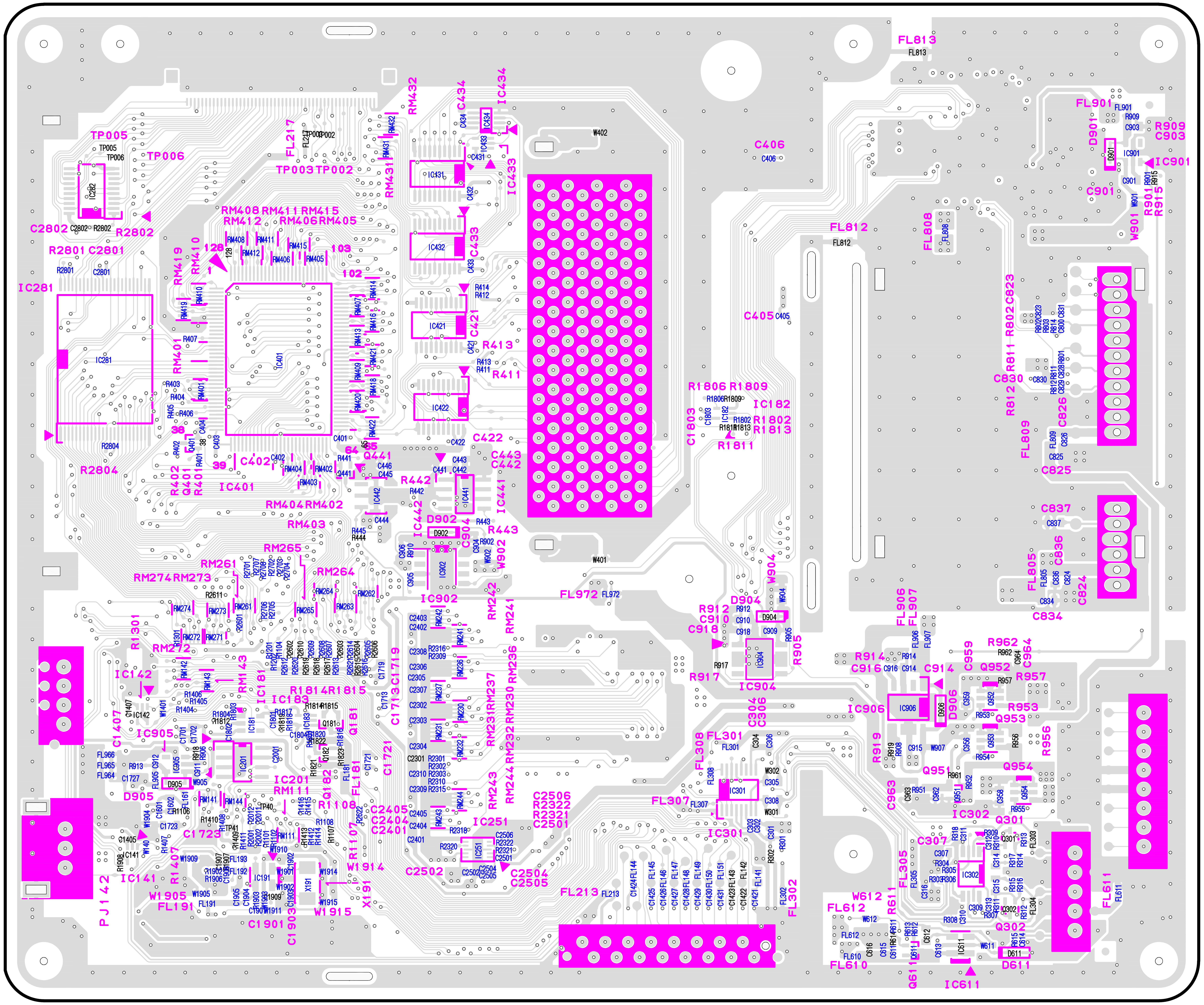
U04AS SIGNAL [Bottom] (PE0399)


NP-150TL/NP-150B
IC513
IC518
IC404
IC403
IC414
IC410
IC360
IC926
IC655
IC661
IC683
IC651
IC656
IC402
L431
L430
FL420
FL425
FL413
FL462
FL431
FL433
FL434
FL435
FL436
FL950
FL930
FL932
FL544
FL545
FL538
FL537
FL542
FL541
FL532
FL503
FL665
Q5001
Q5002
Q5003
Q5004
Q372
W9022
W9032
W9033
W9020
W3700
W6504
W6505
W6506
W5100
W5032
C5291
C5290
C5246
C5164
C5247
C5261
C5364
C5358
C5157
C5161
C5162
C5158
C3740
C3002
C360
C361
L300
L9032
D400
D461
D5002
D6531
R3742
R3743
R3608
RM708
RM709
RM707
RM710
RM408

U05A DIGITAL [Top] (PE0284)

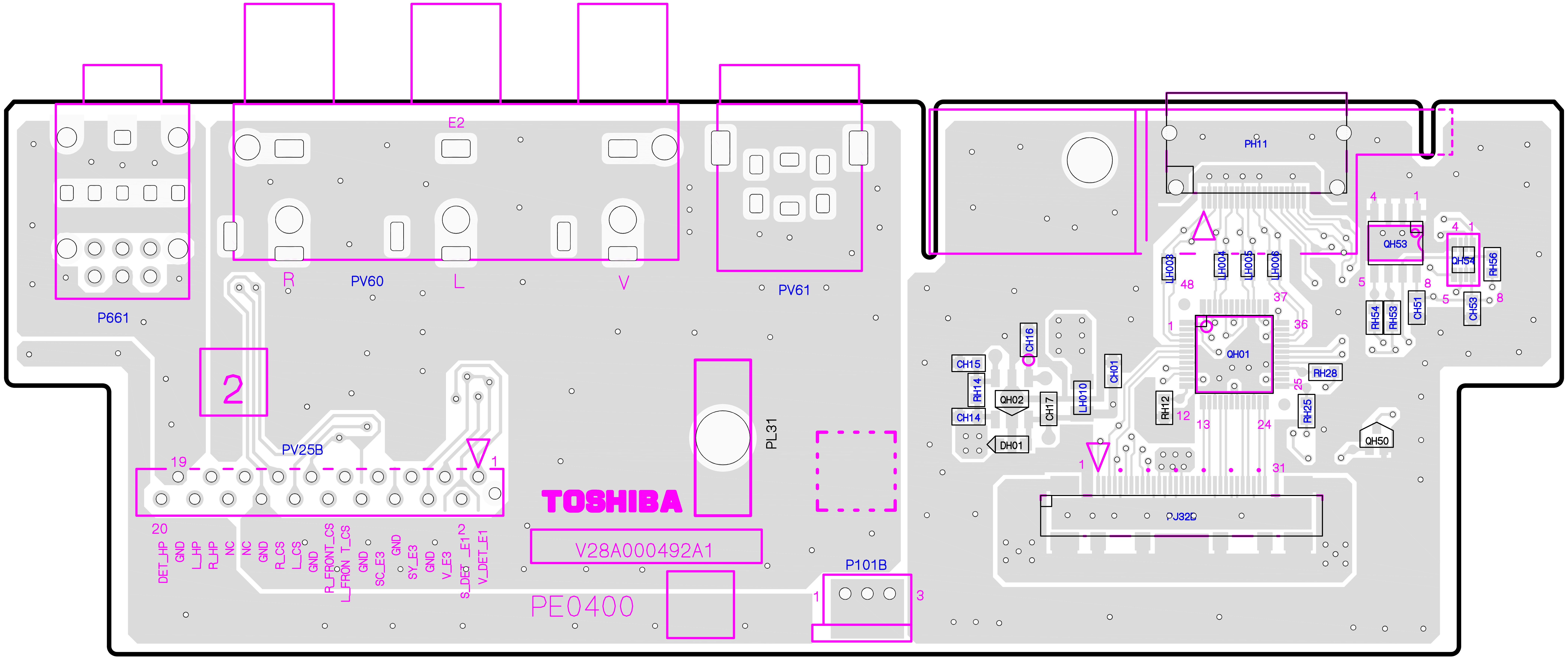

TOSHIBA
PE0284
V28A000319A1
PJ400
IC801
X801
R827
Q801
C839
C840
Q802
C838
C821
C835
C811
C822
C818
C819
R819
R816
R804
RM801
RM802
H801
FL810
P820A
FL951
FL956
C951
C952
C953
C954
C955
C961
C960
FL957
FL958
FL959
FL960
R958
R959
R960
FL970
FL971
R352
R353
Q350
Q351
C319
C318
C320
FL306
W303
C317
P212B
FL615
P200B
C965
R963
RM301
RM211
R2111
R2202
C2207
R2204
RM223
RM224
RM248
IC221
FL904
C2203
C2210
RM222
RM221
C913
RM245
RM239
RM233
RM246
RM234
RM235
RM247
R2317
C2503
C1410
C1413
C1714
C1706
D903
W903
R903
R916
IC903
C601
R603
TP61
TP60
RM114
DS111
DS112
C2101
R2105
R2106
R2104
R2107
C2102
R2103
R2102
R2101
IC100
PJ212
D2100
D2101
PJ142
TP63
TP62
B1
B2
B35
B36
A1
A2
A35
A36
B33
B34
B67
B68
A33
A34
A67
A68
16
17
64
49
48
52
53
104
105
156
157
208

U05A DIGITAL [Bottom] (PE0284)

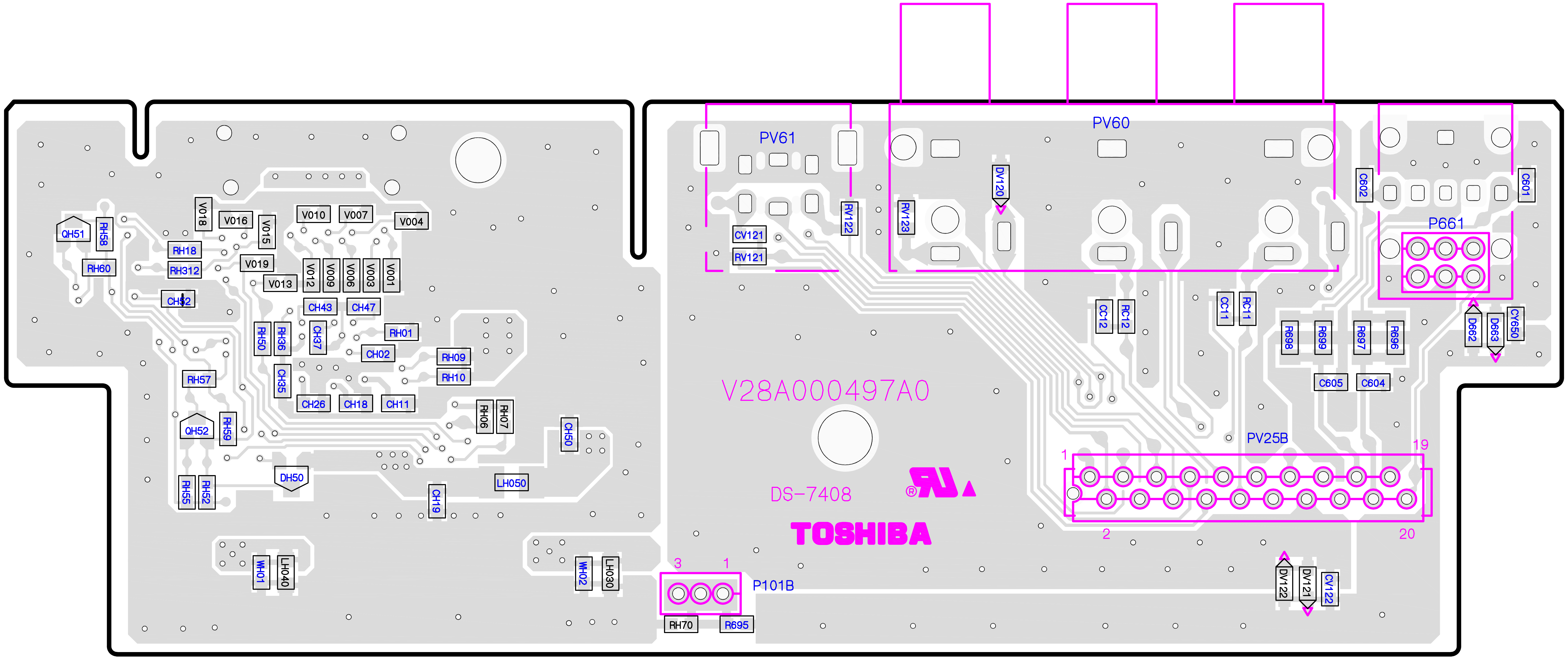


TP005
TP006
FL217
TP003 TP002
RM432
RM431
C434
IC434
IC433
C406
FL813
FL901
D901
R909
C903
IC901
C901
R901
R915
W901
C2802
R2802
R2801 C2801
IC281
RM408 RM411 RM415
RM412 RM406 RM405
RM419
RM410
128
103
102
C433
C421
R413
R411
FL812
FL808
C405
R1806 R1809
IC182
R1802
R1813
R1811
C1803
R802 C823
R811
R812
C830
C826
FL809
C825
RM401
R2804
R402
Q401
R401
38
39
C402
IC401
64
65
Q441
R442
C443
C442
C422
IC441
IC442
D902
R443
C904
W902
RM404 RM402
RM403
RM265
RM261
RM274 RM273
RM264
IC902
RM242
RM241
FL972
W904
D904
R912
C910
C918
R905
R917
IC904
C837
C836
FL805
C834
C824
FL906
FL907
R962
R914
C916
C914
C959
C954
Q952
R957
D906
IC906
R953
Q953
R1301
RM272
IC142
RM143
IC181
R1814 R1815
IC183
Q181
C1713 C1719
C1407
IC905
RM236
RM237
RM231
RM230
RM232
C304
C306
FL308
FL301
R919
R956
Q951
Q954
C963
D905
IC201
RM111
C1721
FL181
Q182
R1108
C2405
C2404
C2401
IC251
RM243
RM244
C2506
R2322
R2321
C2501
FL307
IC301
IC302
Q301
C307
C1723
R1407
IC141
PJ142
W1905
FL191
C1901
C1903
W1914
R1107
X191
W1915
C2502
C2504
C2505
FL213
FL302
FL305
R611
W612
FL612
FL610
Q611
IC611
D611
Q302
FL611

U06A FRONT_HDMI [Top] (PE0400)

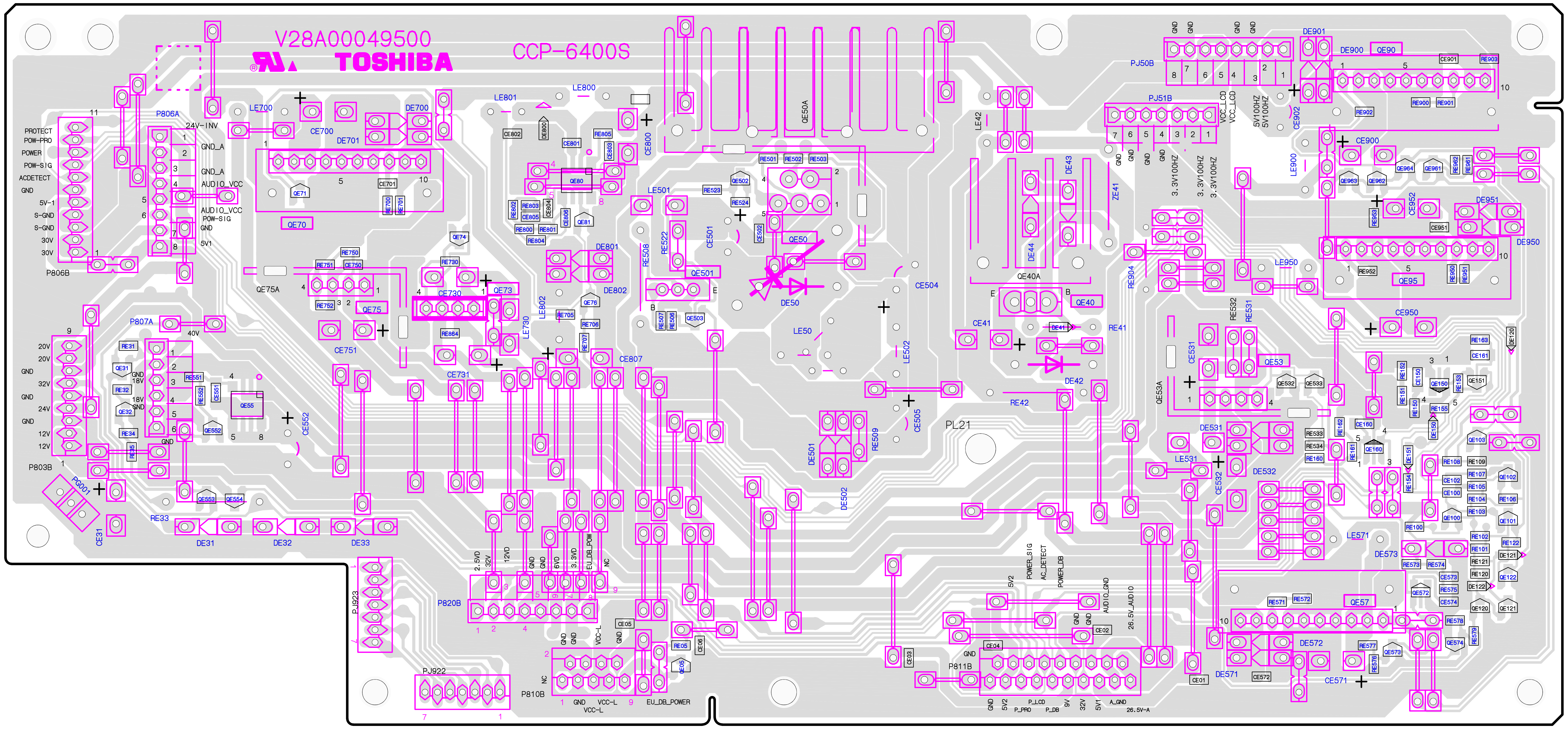

E2
P661
R
PV60
L
V
PV61
2
PV25B
19
1
20
2
DET_HP
GND
L_HP
R_HP
NC
NC
GND
R_CS
L_CS
GND
R_FRONT_CS
L_FRON T_CS
GND
SC_E3
GND
SY_E3
GND
V_E3
S_DET _E1
V_DET_E1
TOSHIBA
PL31
V28A000492A1
PE0400
P101B
1
3
PH11
LH003
LH004
LH005
LH006
48
37
36
1
QH01
25
24
13
12
RH12
RH28
RH25
QH53
QH54
RH56
RH54
RH53
CH51
CH53
4
1
5
8
CH15
CH16
RH14
CH14
QH02
CH17
LH010
CH01
DH01
QH50
31
PJ32D

U06A FRONT_HDMI [Bottom] (PE0400)

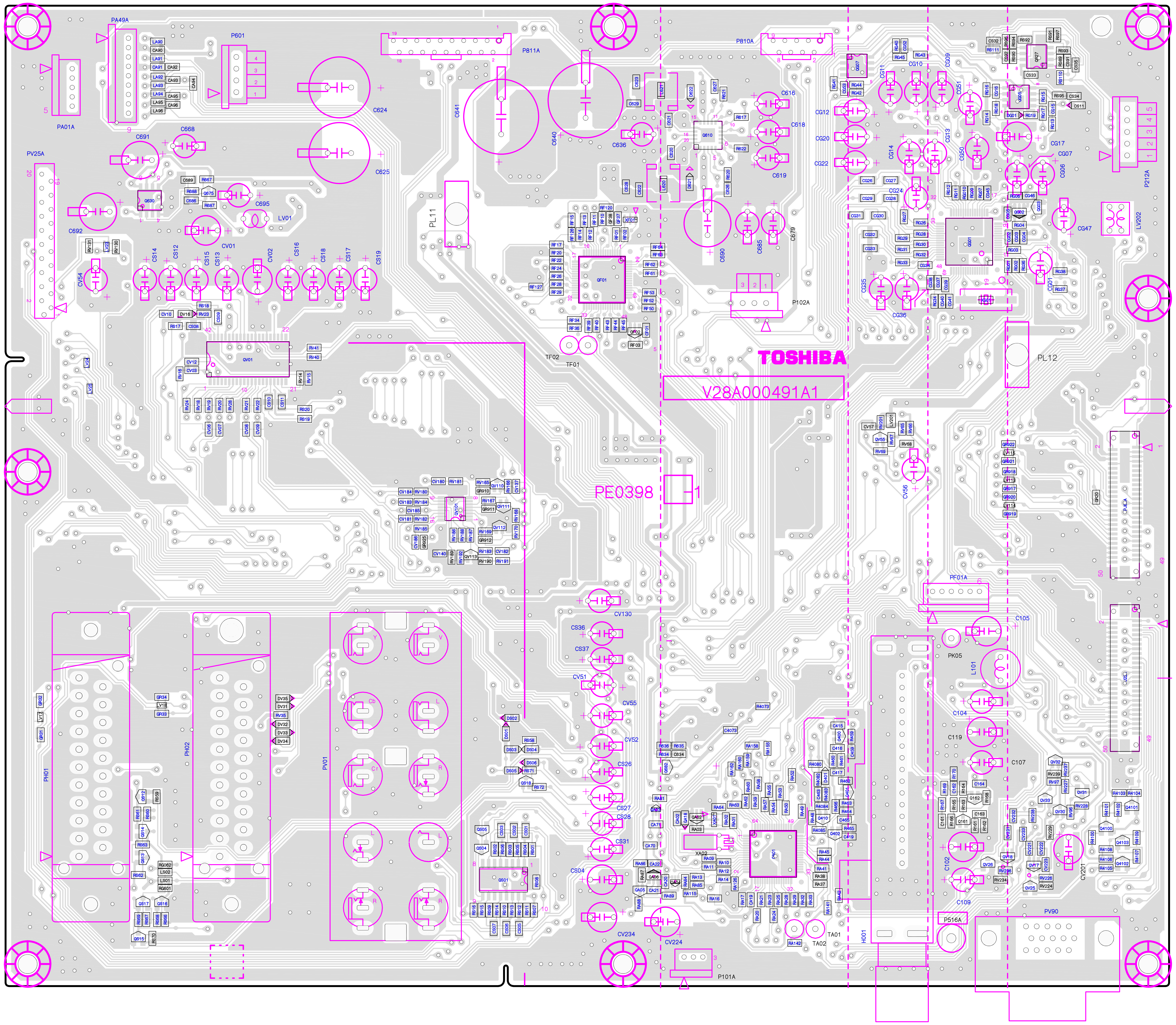


QH51
RH58
RH60
RH18
RH312
CH52
V018
V016
V015
V010
V007
V004
V019
V013
V012
V009
V006
V003
V001
CH43
CH47
RH01
RH50
RH36
CH37
CH02
RH09
RH10
CH35
CH26
CH18
CH11
RH57
RH06
RH07
QH52
RH59
CH50
DH50
LH050
RH55
RH52
CH19
WH01
LH040
WH02
LH030
PV61
CV121
RV121
RV122
RV123
DV120
PV60
C602
C601
P661
CC12
RC12
CC11
RC11
R698
R699
R697
R696
D662
D663
CY650
C605
C604
V28A000497A0
DS-7408
TOSHIBA
PV25B
1
19
2
20
P101B
3
1
RH70
R695
DV122
DV121
CV122

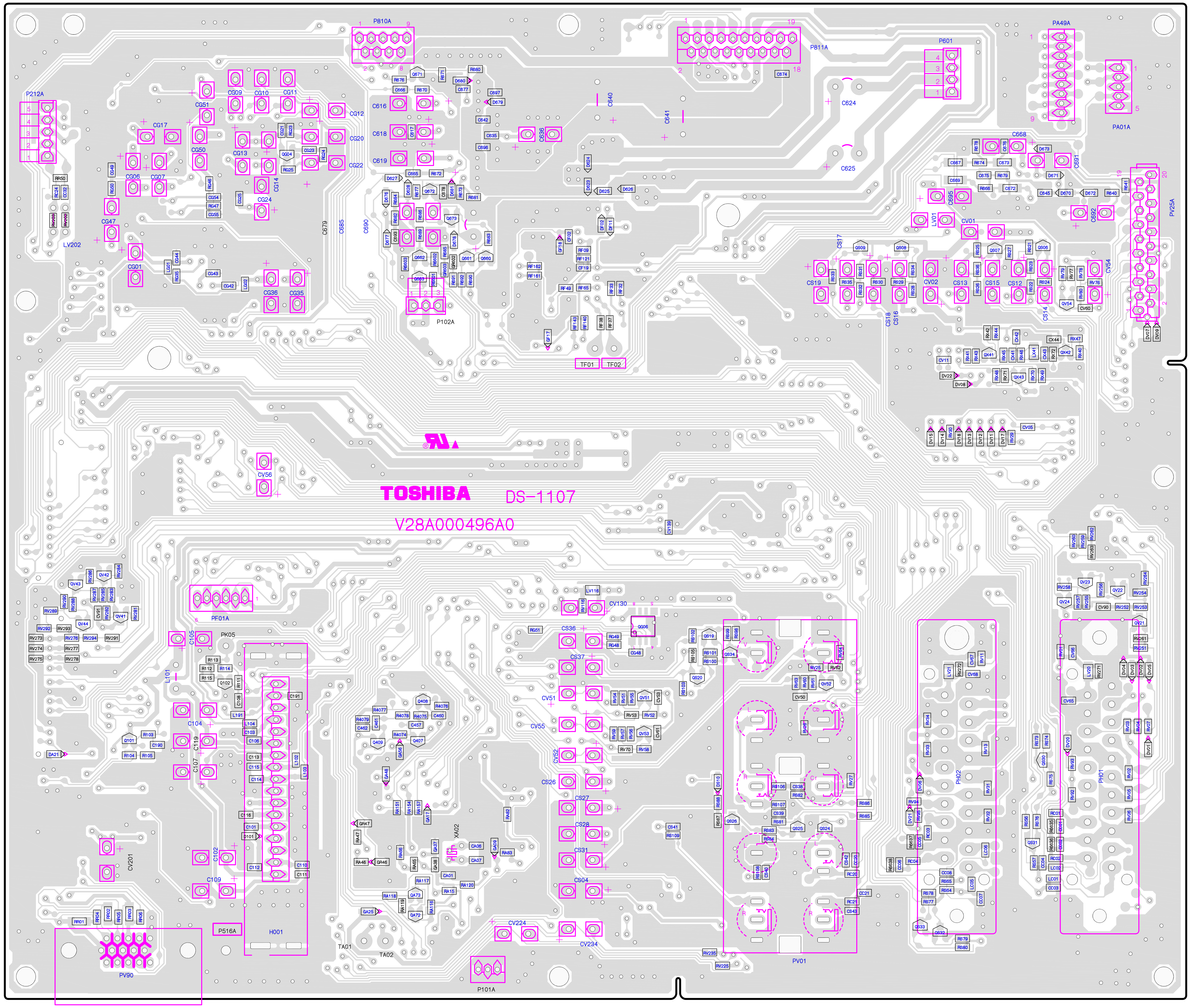
U02A LOW_B [Bottom] (PE0397)


V28A00049500
TOSHIBA
CCP-6400S
P806A
P806B
PROTECT
POW-PRO
POWER
POW-SIG
ACDETECT
GND
5V-1
S-GND
S-GND
30V
30V
24V-INV
GND_A
AUDIO_VCC
POW-SIG
5V1
P807A
40V
P803B
20V
20V
32V
24V
12V
12V
18V
LE700
CE700
DE700
DE701
QE70
QE71
CE701
QE75A
QE75
QE73
CE730
CE731
CE751
LE801
LE800
CE800
CE802
QE80
QE81
DE801
DE802
LE802
LE730
CE807
LE501
CE501
QE501
QE50
QE50A
DE50
LE50
CE504
CE505
LE502
DE501
DE502
RE509
PL21
LE42
QE40A
QE40
CE41
RE41
DE42
RE42
DE43
DE44
ZE41
RE904
PJ50B
PJ51B
VCC_LCD
5V100HZ
3.3V100HZ
DE901
DE900
QE90
CE900
CE902
LE900
DE951
CE952
DE950
QE95
LE950
CE950
RE531
RE532
CE531
QE53
QE53A
DE531
DE532
LE531
CE532
LE571
DE573
QE57
DE572
DE571
CE571
CE572
P811B
P820B
P810B
PJ922
PJ923
DE31
DE32
DE33
RE33
CE31
P9001
CE552
QE55
2.5VD
32V
12VD
GND
6VD
3.3VD
EU_DB_POW
NC
VCC-L
EU_DB_POWER
POWER_SIG
AC_DETECT
POWER_DB
AUDIO_GND
26.5V_AUDIO
5V2
P_PRO
P_LCD
P_DB
9V
5V1
A_GND
26.5V-A

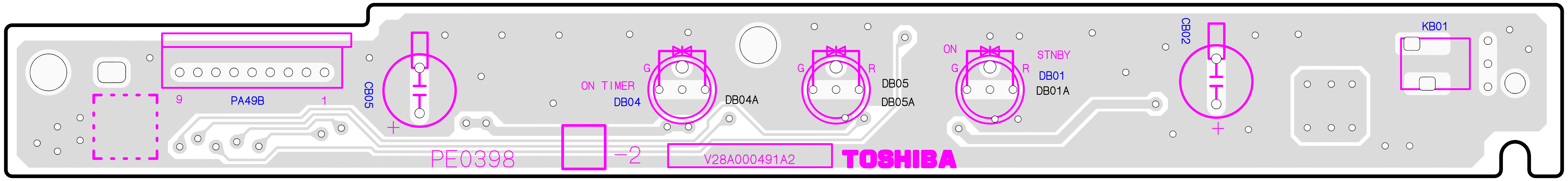
U03A AV_TERMINAL [Top] (PE0398)


PA49A
P601
P811A
P810A
PA01A
PV25A
C691
C668
C624
C625
C641
C640
C636
C692
C695
LV01
PL11
C616
C618
C619
CG12
CG20
CG22
CG10
CG17
CG07
CG47
P212A
LV202
P102A
TF02
TF01
TOSHIBA
V28A000491A1
PL12
PE0398
PF01A
C105
PK05
L101
C104
C119
C107
C109
CV201
CV130
CS36
CS37
CV51
CV55
CV52
CS26
CS27
CS28
CS31
CS04
CV234
CV224
P101A
TA01
TA02
H001
P516A
PV90
PH01
PH02
PV01

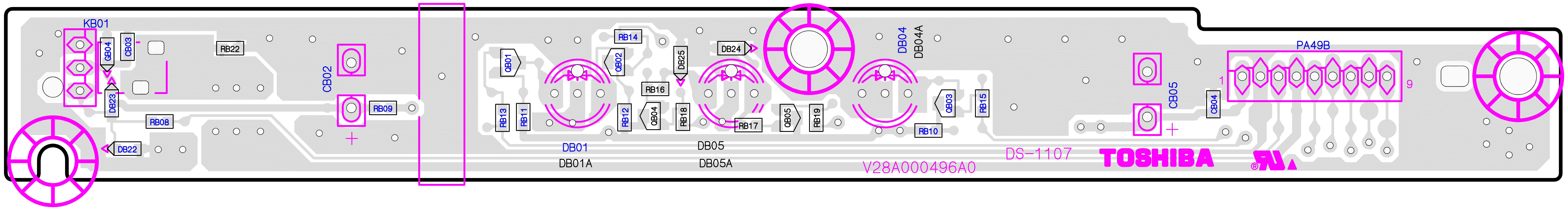
U03A AV_TERMINAL [Bottom] (PE0398)


TOSHIBA
DS-1107
V28A000496A0
P810A
P811A
P601
PA49A
PA01A
P212A
PV25A
P102A
PF01A
PK05
H001
P516A
PV90
P101A
PV01
PV02
PH01
LV202
CV201
TF01
TF02
TA01
TA02

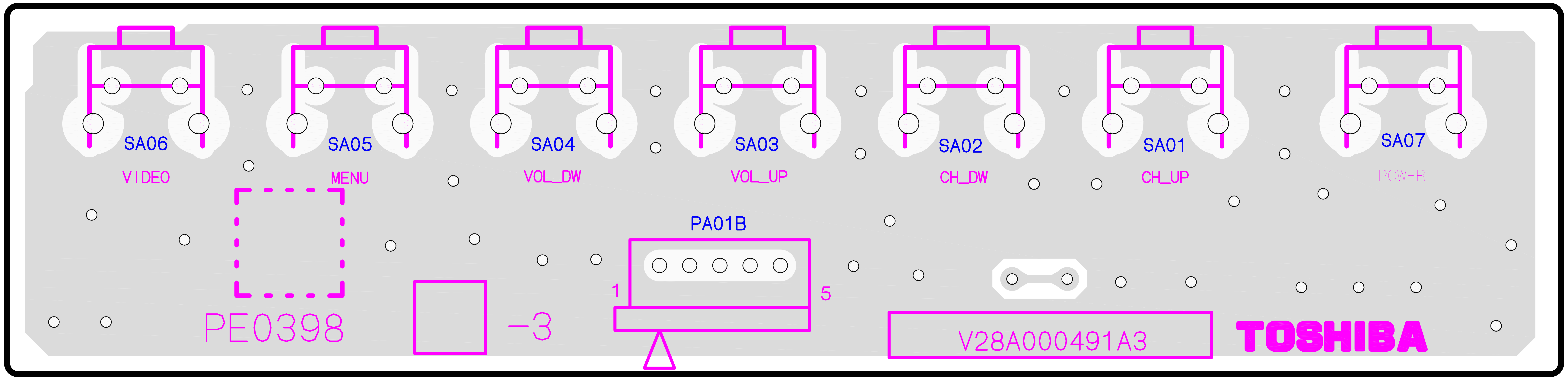
U03B LED [Top] (PE0398)


PA49B
9
1
CB05
+
ON TIMER
DB04
G
DB04A
G
R
DB05
DB05A
ON
G
R
STNBY
DB01
DB01A
CB02
+
KB01
PE0398
-2
V28A000491A2
TOSHIBA

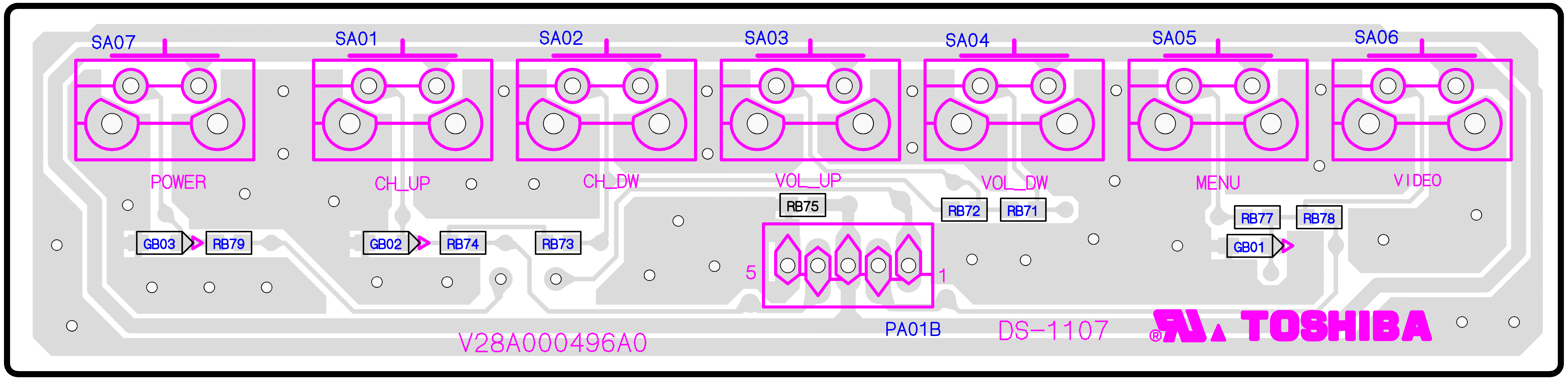
U03B LED [Bottom] (PE0398)


KB01
GB04
CB03
RB22
DB23
RB08
DB22
CB02
RB09
QB01
RB13
RB11
RB14
QB02
RB12
QB04
RB16
DB25
RB18
DB24
RB17
DB01
DB01A
DB05
DB05A
QB05
RB19
DB04
DB04A
QB03
RB10
RB15
V28A000496A0
DS-1107
CB05
TOSHIBA
CB04
PA49B
1
9

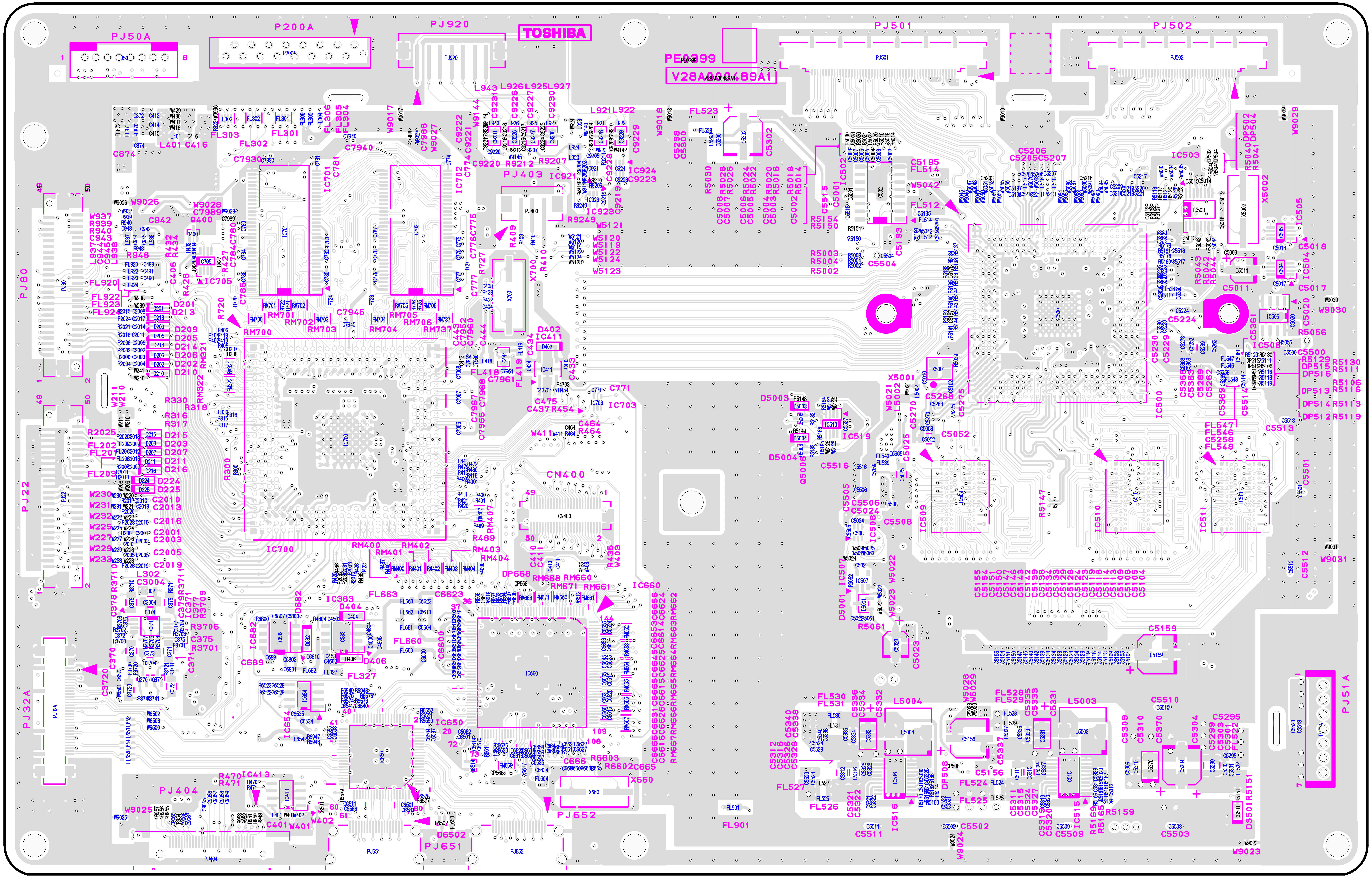
U03C KEY [Top] (PE0398)


SA06
VIDEO
SA05
MENU
SA04
VOL_DW
SA03
VOL_UP
SA02
CH_DW
SA01
CH_UP
SA07
POWER
PA01B
1
5
PE0398
-3
V28A000491A3
TOSHIBA

U03C KEY [Bottom] (PE0398)


SA07
SA01
SA02
SA03
SA04
SA05
SA06
POWER
CH_UP
CH_DW
VOL_UP
VOL_DW
MENU
VIDEO
RB75
RB72
RB71
RB77
RB78
GB03
RB79
GB02
RB74
RB73
GB01
5
1
PA01B
V28A000496A0
DS-1107
TOSHIBA

U04AS SIGNAL [Top] (PE0399)


PJ50A
P200A
PJ920
TOSHIBA
PJ501
PJ502
PE0399
V28A
PJ80
PJ22
PJ32A
PJ404
PJ651
PJ652
PJ51A
PJ403
IC700
IC701
IC702
IC703
IC705
IC411
IC383
IC650
IC660
IC500
IC503
IC505
IC506
IC509
IC510
IC511
IC516
IC519
IC921
IC923
IC924
CN400
X700
X5001
X5002
X660
FL901
L5003
L5004